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Table of Contents

1 Int	troduction	4
1.1	Supported Chips and Packages	4
1.2	System Requirements	
1.2.1	Hardware	8
1.2.2		8
1.3	Functional Description	
1.3.1		
1.3.2	Plug-In for Keil/IAR IDE	9
1.4	Features	
1.4.1		
1.4.2		
1.4.3	Display an alert for abnormal pin	11
1.4.4		
2 Ge	etting Started	.12
2.1	Running PinView	12
2.2	Main Window	
3 Re	vision History	.15

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List of Figures

Figure 1-1 Standalone PinView ·····	9
Figure 1-2 PinView Plug-In for IAR IDE ······1	0
Figure 2-1 Nu-Link Keil Debug Menu ······1	2
Figure 2-2 Nu-Link IAR Debug Menu······1	3
Figure 2-3 Main Window of PinView1	4

1 Introduction

NuMicro PinView Tool (abbreviated as PinView in the following) is a monitoring and visualization tool that can show the current status of I/O pins on the device immediately, and inform users of some common pin configuration errors. It can run as a standalone application or be used as a plug-in for Keil/IAR IDE.

1.1 Supported Chips and Packages

Please refer to the datasheets from http://www.nuvoton.com.

AU9100 series

• AU9110AN , LQFP48

M031 series

- M031AE , TSSOP20/TSSOP28/QFN33/LQFP48/LQFP64
- M032AE · LQFP48/LQFP64

M051 series

- M052/M054/M058/M0516AN , QFN33/LQFP48
- M052/M054/M058/M0516BN , QFN33/LQFP48
- M052/M054/M058/M0516DN , QFN33/LQFP48
- M052/M054/M058/M0516DE , QFN33/LQFP48

M0518 series

• M0518AE , LQFP48/LQFP64

M0519 series

• M0519AE · LQFP48/LQFP64/LQFP100

M0564 series

• M0564AE , LQFP48/LQFP64/LQFP100

M058S series

• M058SAN , TSSOP20/QFN33/LQFP48/LQFP64

M2351 series

• M2351AE , QFN33/LQFP64/LQFP128

M251 series

- M251AE , TSSOP20/TSSOP28/QFN33/LQFP48/LQFP64/LQFP128
- M252AE , TSSOP20/TSSOP28/QFN33/LQFP48/LQFP64/LQFP128

M451 series

- M451AE , LQFP48/LQFP64/LQFP100
- M452AE · LQFP48/LQFP64
- M453AE , LQFP48/LQFP64/LQFP100
- M451MAE , LQFP48

M4521 series

• M4521AE , LQFP48/LQFP64

M480 series

- M481AE , QFN33/LQFP48
- M482AE · ULQFP48/ULQFP64
- M483AE , HSULQFP64/HSULQFP128
- M484AE , HSULQFP64/HSULQFP128/UHSULQFP64
- M485AE , HSULQFP64/HSULQFP128
- M487AE , HSULQFP64/HSULQFP128/HSULQFP144

Mini51 series

- Mini51/Mini52/Mini54AN , QFN33/LQFP48
- Mini51/Mini52/Mini54DE , TSSOP20/QFN33/LQFP48
- Mini55DE , QFN33/LQFP48
- Mini58DE , TSSOP20/QFN33/LQFP48

Mini57 series

• Mini57DE , TSSOP20/TSSOP28/QFN33

ML51 series

 ML51AE , MSOP10/SOP20/SOP28/TSSOP14/TSSOP20/TSSOP28 /QFN20/QFN33/LQFP32/LQFP48

NM1120 series

• NM1120AE , TSSOP20/TSSOP28/QFN20/QFN33

NM1200 series

- NM1100AE , TSSOP20/QFN20
- NM1200AE , QFN33/LQFP48

NM1500 series

- NM1510AE , LQFP48
- NM1530AE , LQFP100
- NM1520AE · LQFP48/LQFP64

NUC029 series

- NUC029AN , QFN33/LQFP48
- NUC029AE , TSSOP20
- NUC029DE , LQFP48/LQFP64
- NUC029EE , LQFP48/LQFP64
- NUC029GE , LQFP48/LQFP64

NUC100 series

- NUC100AN , LQFP48/LQFP64/LQFP100
- NUC120AN , LQFP48/LQFP64/LQFP100
- NUC100BN , LQFP48/LQFP64
- NUC101BN , QFN36/LQFP48
- NUC120BN , LQFP48/LQFP64
- NUC130CN · LQFP48/LQFP64/LQFP100
- NUC140CN , LQFP48/LQFP64/LQFP100
- NUC100DN , LQFP48/LQFP64/LQFP100
- NUC120DN , LQFP48/LQFP64/LQFP100

NUC121 series

• NUC121AE , QFN33/LQFP48/LQFP64

NUC122 series

• NUC122AN , QFN33/LQFP48/LQFP64

NUC123 series

- NUC123AN , QFN33/LQFP48/LQFP64
- NUC123AE , QFN33/LQFP48/LQFP64

NUC125 series

• NUC125AE , QFN33/LQFP48/LQFP64

NUC126 series

• NUC126AE , LQFP48/LQFP64/LQFP100

NUC1261 series

• NUC1261AE , LQFP48/LQFP64

NUC131 series

• NUC131AE , LQFP48/LQFP64

NUC200 series

- NUC200AN , LQFP48/LQFP64/LQFP100
- NUC220AN , LQFP48/LQFP64/LQFP100
- NUC230AE , LQFP48/LQFP64/LQFP100
- NUC240AE , LQFP48/LQFP64/LQFP100

NUC2201 series

• NUC2201AE · LQFP48/LQFP64

NUC400 series

- NUC442AE · LQFP64/LQFP100/LQFP128/LQFP144
- NUC472AE , LQFP100/LQFP128/LQFP144/LQFP176

NUC505 series

• NUC505 , QFN88/LQFP64/LQFP48

Nano100 series

- Nano100AN , QFN33/LQFP48/LQFP64
- Nano120AN , QFN33/LQFP48/LQFP64
- Nano100BN , LQFP48/LQFP64/LQFP128
- Nano110BN , LQFP64/LQFP128
- Nano120BN , LQFP48/LQFP64/LQFP128
- Nano130BN · LQFP64/LQFP128

Nano103 series

• Nano103AE , LQFP33/LQFP48/LQFP64

Nano112 series

- Nano102AN , LQFP33/LQFP48/LQFP64
- Nano112AN · LQFP48/LQFP64/LQFP100

1.2 System Requirements

- 1.2.1 Hardware
 - Nu-Link In-Circuit Emulator(ICE)

1.2.2 Software

• Please download the standalone version of PinView or the Keil/IAR plug-in version which is included in Nu-Link driver (v1.25.6287 or higher) from nuvoton website, and click the installer file to start the installation wizard. Follow the instructions in the installation wizard to complete the installation.

1.3 Functional Description

1.3.1 Standalone Version

The standalone PinView can monitor the I/O pins on the device without stopping the process of target chip and let users view the pin status updates in real-time. Also, it allows users to load user-defined lua script through the menu **File** \rightarrow **Run Script** at the top of the main window to access the chip memory or register, stop a running target, etc. The main window is shown as follows:

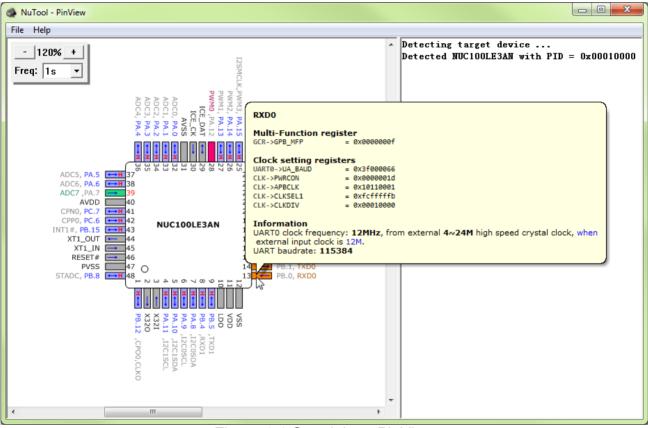


Figure 1-1 Standalone PinView

1.3.2 Plug-In for Keil/IAR IDE

To use PinView plug-in for Keil/IAR IDE, it's necessary to start a debugging session first and then execute it from the debug menu. For each debug step, it can display the variation in I/O pin status and refreshes the status information periodically until a breakpoint or program exit is reached. The PinView plug-in's main window of IAR IDE is shown as follows:

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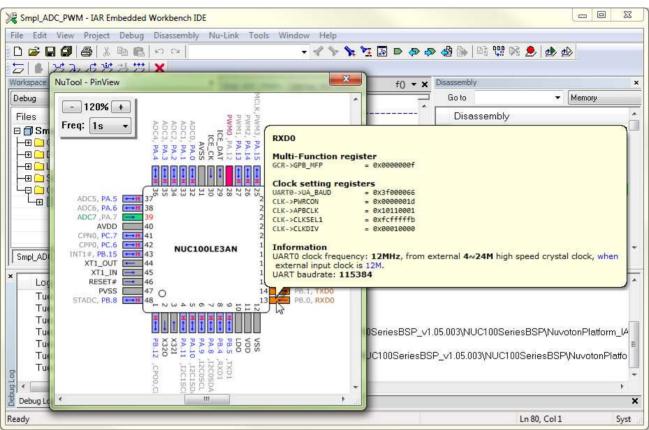


Figure 1-2 PinView Plug-In for IAR IDE

1.4 Features

1.4.1 Display the selected pin function

Identify the pin function using the background and text colors:

- As a general-purpose input/output (GPIO) pin, it shows in blue color.
- As a peripheral device pin (e.g., UART, SPI, PWM), it shows in other colorful colors except blue.
- Others, it shows in gray color.

1.4.2 Display the pin configuration

If you move the mouse cursor over the pin, a pop-up tooltip that includes one or more of the following descriptions will show up:

- The value of multi-function and clock setting registers corresponding to the current pin.
- The text of input and output state descriptions for GPIO.

- The clock source and frequency of peripheral device.
- The baud rate of universal asynchronous receiver/transmitter (UART) device.
- The sampling frequency of Pulse-width modulation (PWM) device.

1.4.3 Display an alert for abnormal pin

While detecting the pin in an abnormal status, the pin number will be displayed in red color. Meanwhile, if you move the mouse cursor over the pin, the pop-up tooltip will show the cause messages in red color. The possible abnormal status is described as follows:

- The electrical leakage for GPIO pin.
- The value of multi-function setting register is undefined.
- The clock source of peripheral device is unavailable.
- The baud rate setting of UART device exceeds the valid range.
- The pre-scale register setting of PWM device is invalid.

1.4.4 User-defined chip setting script

For standalone PinView, it allows user to load user-defined lua script to interact with target chip.

2 Getting Started

2.1 Running PinView

- To execute the standalone PinView, click the Start button in the lower-left corner of your screen to open the Start Menu, and then select the Nuvoton Tools → NuTool
 PinView.
- The PinView plug-in for Keil IDE can only be used during the debugging session. To execute it, please start a debugging session, and then click the **Debug Menu** → NuTool PinView.

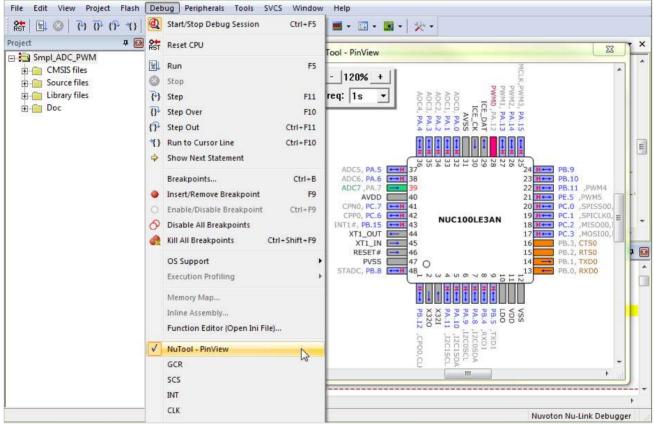


Figure 2-1 Nu-Link Keil Debug Menu

The PinView plug-in for IAR IDE can only be used during the debugging session. To execute it, please start a debugging session, and then click the Nu-Link Menu → NuTool - PinView.

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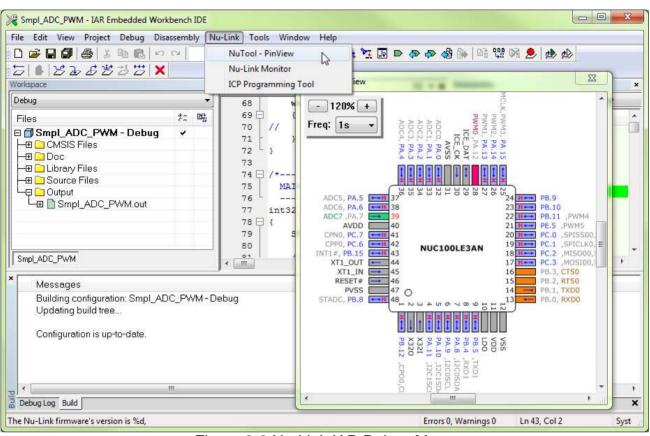


Figure 2-2 Nu-Link IAR Debug Menu

2.2 Main Window

The following figure provides a description for the main window of PinView.

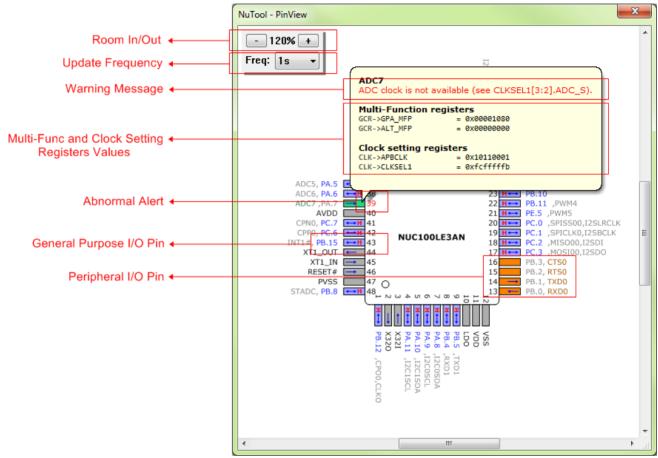


Figure 2-3 Main Window of PinView

3 Revision History

Revision	Date	Description
0.01	Dec. 23, 2013	1. First version.
0.02	Jan. 21, 2014	1. Updated all figures.
0.03	Jan. 14, 2015	1. Changed document format.
0.04	June. 2, 2017	1. Updated "Supported Chips and Packages" chapter.
0.05	Nov. 8, 2017	 Updated "Supported Chips and Packages" chapter Added M2351AE
0.06	June. 7, 2018	 Updated "Supported Chips and Packages" chapter Added M031AE, M0519AE, and NUC030AE.
0.07	June. 13, 2018	 Updated "Supported Chips and Packages" chapter Added M4521AE and NUC2201AE.
0.08	June. 28, 2018	 Updated "Supported Chips and Packages" chapter Added NUC029DE and NUC029EE.
0.09	July. 25, 2018	 Updated "Supported Chips and Packages" chapter Added NM1120AE.
0.10	Aug. 6, 2018	 Updated "Supported Chips and Packages" chapter Added NUC1261AE and NUC029GE.
0.11	Aug. 31, 2018	 Updated "Supported Chips and Packages" chapter Added ML51AE.
0.12	Jan. 22, 2019	 Updated "Supported Chips and Packages" chapter Added M251AE.

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