

**NuMicro<sup>®</sup> Family****ARM926EJ-S<sup>™</sup>-based 32-bit Microprocessor****NuMaker-IIoT-NUC980G2D  
User Manual*****Evaluation Board for NuMicro<sup>®</sup> NUC980 Series***

*The information described in this document is the exclusive intellectual property of Nuvoton Technology Corporation and shall not be reproduced without permission from Nuvoton.*

*Nuvoton is providing this document only for reference purposes of NuMicro microcontroller and microprocessor based system design. Nuvoton assumes no responsibility for errors or omissions.*

*All data and specifications are subject to change without notice.*

For additional information or questions, please contact: Nuvoton Technology Corporation.

[www.nuvoton.com](http://www.nuvoton.com)

## **Table of Contents**

<b>1 OVERVIEW .....</b>	<b>6</b>
<b>2 FEATURES .....</b>	<b>8</b>
2.1 NuTFT Kit Board Features .....	8
2.2 NuMaker-IIoT-NUC980G2 Features .....	8
<b>3 NUTFT KIT BOARD HARDWARE CONFIGURATION .....</b>	<b>9</b>
3.1 Front View .....	9
3.2 Rear View .....	10
3.3 Extension Connectors .....	11
3.3.1 Pin Assignment for Extension Connectors .....	11
3.3.2 Pin Function Description .....	13
<b>4 NUMAKER-IIOT-NUC980G2 BOARD HARDWARE CONFIGURATION .....</b>	<b>15</b>
4.1 Front View .....	15
4.2 Rear View .....	22
<b>5 QUICK START .....</b>	<b>23</b>
5.1 Nuvoton CDC Driver Installation .....	23
5.2 Nuvoton Virtual COM Driver Installation .....	25
5.3 BSP Firmware Download .....	28
5.4 Hardware Setup .....	29
5.5 NuWriter Tool .....	32
5.5.1 NuWriter Setup .....	32
5.5.2 SPI NAND Mode .....	34
<b>6 NUTFT KIT BOARD SCHEMATICS .....</b>	<b>38</b>
6.1 NuTFT Kit Board .....	38
6.2 PCB Placement .....	39
<b>7 NUMAKER-IIOT-NUC980G2D SCHEMATICS .....</b>	<b>40</b>
7.1 Block Diagram Schematic .....	40
7.2 GPIO List .....	41
7.3 Power .....	42
7.4 NUC980DK .....	43
7.5 Power Filter .....	44
7.6 Power-on Setting, CAN_1 and CAN_2 .....	45
7.7 NUC123ZD4AN0 .....	46
7.8 Memory .....	47

7.9 RMII_PE.....	48
7.10 Audio Codec and CAN_3 .....	49
7.11 SD1/eMMC1 .....	50
7.12 Arduino Uno Interface .....	51
7.13 USB.....	52
7.14 Expand EBI Interface and CAN_0 .....	53
7.15 PCB Placement.....	54
<b>8 REVISION HISTORY .....</b>	<b>55</b>

## List of Figures

Figure 1-1 NuMaker-IIoT-NUC980G2 Evaluation Board .....	7
Figure 3-1 Front View of NuMaker-IIoT-NUC980G2.....	15
Figure 3-2 Rear View of NuMaker-IIoT-NUC980G2 .....	22
Figure 4-1 CDC Driver Installation Setup.....	23
Figure 4-2 CDC Driver Installation .....	24
Figure 4-3 VCOM Driver Installation Setup.....	25
Figure 4-4 VCOM Driver Installation .....	27
Figure 4-5 Boot Source Selection .....	29
Figure 4-6 USB-Serial Debug Port.....	30
Figure 4-7 Device Manger.....	30
Figure 4-8 PuTTY Session Setting.....	31
Figure 4-9 Booting Log.....	31
Figure 4-10 USB D Port .....	32
Figure 4-11 Device Manger(2) .....	33
Figure 4-12 NuWriter Chip Setting .....	34
Figure 4-13 Download u-boot-spl to SPI NAND.....	35
Figure 4-14 Download u-boot to SPI NAND .....	35
Figure 4-15 Download dtb to SPI NAND.....	36
Figure 4-16 Download ulmage to SPI NAND.....	37
Figure 4-17 Download Environment to SPI NAND .....	37
Figure 5-1 NuMaker-IIoT-NUC980G2 Board Block Diagram.....	40
Figure 5-2 GPIO List .....	41
Figure 5-3 Power.....	42
Figure 5-4 NUC980DK .....	43
Figure 5-5 Power Filter.....	44
Figure 5-6 Power-on Setting .....	45
Figure 5-7 NUC123ZD4AN0 .....	46
Figure 5-8 Memory .....	47
Figure 5-9 RMII_PE.....	48
Figure 5-10 Audio Codec and CAN_3 .....	49
Figure 5-11 SD1/eMMC1 .....	50
Figure 5-12 Arduino Uno interface .....	51
Figure 5-13 USB.....	52
Figure 5-14 Expand EBI Interface and CAN_0 .....	53
Figure 5-15 Front Placement .....	54
Figure 5-16 Rear Placement.....	54

List of Tables

Table 4-1 Boot Source Selection Table ..... 29

## 1 OVERVIEW

The NuMaker-IIoT-NUC980G2D is an evaluation board based on Arm ARM926EJ-S microprocessor NUC980DK71YC which has rich peripherals to help users to design-in their products or application systems easily. The NuMaker-IIoT-NUC980G2D consists of two parts, a NuTFT Kit Board and a NuMaker-IIoT-NUC980G2 board.

The NuTFT Kit Board is a daughter board that extends the NuMaker development boards. The NuTFT Kit Board requires NuMaker development board as mother board to create a GUI development platform. It is designed for LCD display or GUI evaluation, prototype development and validation.

The NuTFT Kit Board is equipped with one SPI flash, one LCD panel with touch function, one five direction joystick and two push buttons. NuTFT Kit Board pin arrangement is compatible with Arduino UNO. The driver IC of the LCD panel is ILI9341, for more detailed information about the LCD panel, please refer to the ILI9341 datasheet.

The NuMaker-IIoT-NUC980G2 uses NUC980DK71YC microprocessor which runs up to 300 MHz with built-in 128 MB DDR2 memory, 16 KB I-cache, 16 KB D-cache and MMU, 16 KB embedded SRAM and 16.5 KB IBR (Internal Boot ROM) for system booting from USB, SPI NAND Flash and SD/EMMC. All functions of the NUC980DK71YC are placed on the board, including peripheral interfaces such as memory (SPI NAND Flash, eMMC, SD), UART, Audio controller (NAU88C22YG), 10/100 Mbps Ethernet MAC controller, high speed USB (device, HOST), JTAG, CAN and EBI. Furthermore, the board provides Arduino Uno compatible interface for expansion. You can use it to develop and verify applications to emulate the real behavior.

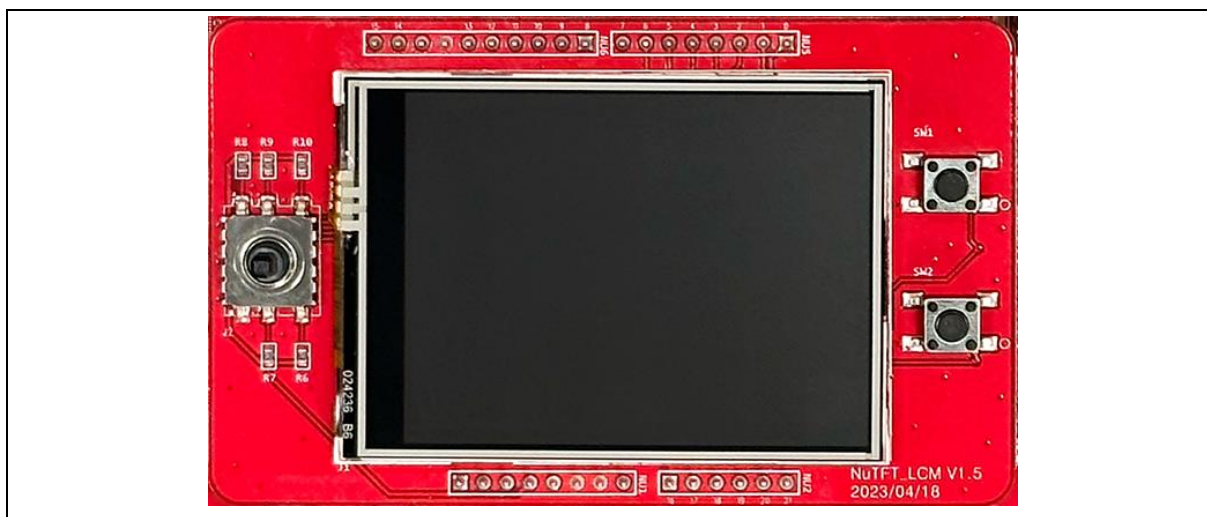


Figure 1-1 NuTFT Kit Board

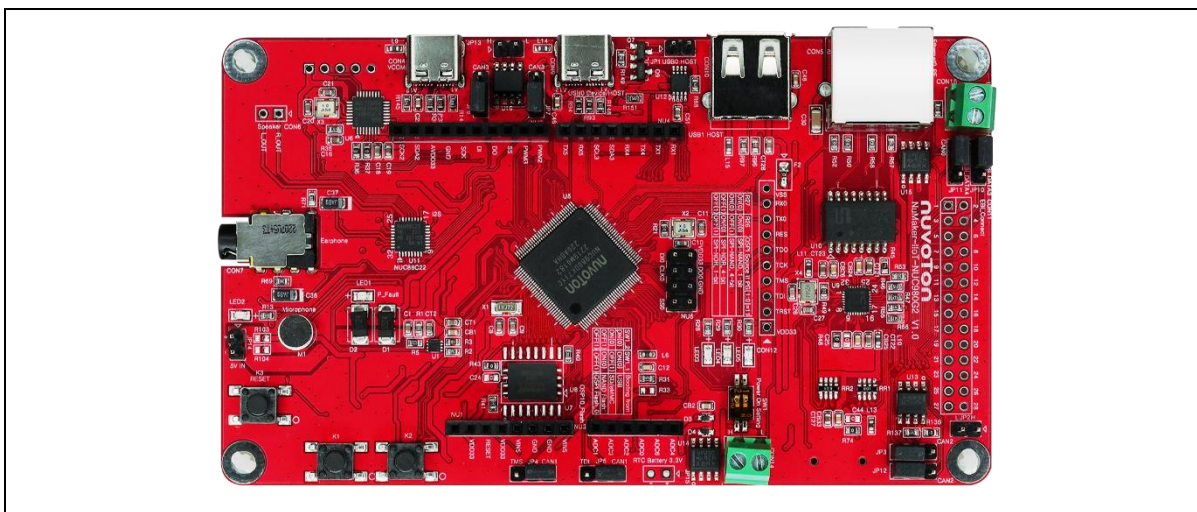


Figure 1-2 NuMaker-IIoT-NUC980G2 Evaluation Board

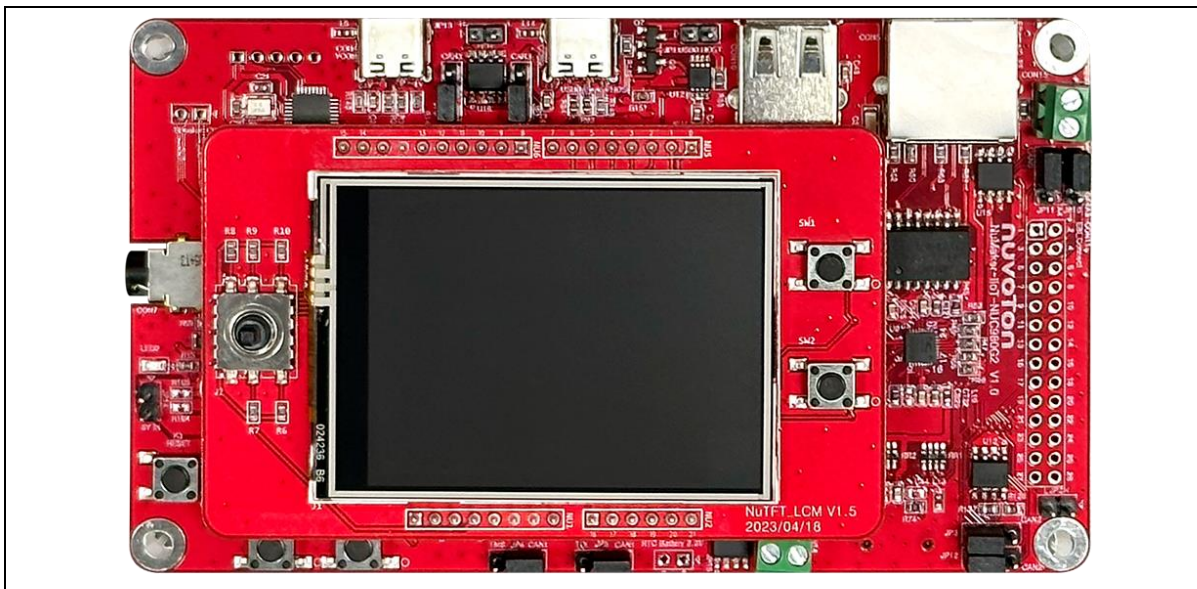


Figure 1-3 NuMaker-IIoT-NUC980G2D Evaluation Board



## 2 FEATURES

### 2.1 NuTFT Kit Board Features

- 16 M bits (2 MB) SPI Flash \* 1 (W25Q16CV)
- 2.4" (320x240) LCD Panel with 4-Wire ADC Touch Function (LCD Driver is Ili9341)
- Five Direction Joystick \* 1
- Push Button \* 2
- Arduino UNO compatible extension connectors
- Switch \* 2

### 2.2 NuMaker-IIoT-NUC980G2 Features

- NUC980DK71YC: LQFP128 pin MCP package with DDR2 (128 MB), which can run up to 300 MHz operating speed
- SPI Flash: Quad mode system booting or data storage, using W25N01GVZEIG SPI-NAND (128 MB)
- SD1/eMMC1: User SD/eMMC memory card for system booting, data storage or SDIO (Wi-Fi) device
- UART0: Connected to Virtual COM port for system development, debug message output
- Arduino Uno compatible interface connectors (NU1, NU2, NU3, NU4 and NU5)
- JTAG interface provided for software development
- RJ45 port with Ethernet 10/100 Mbps MAC (Ethernet0)
- EBI interface with pin header
- Microphone input and Earphone/Speaker output with 24-bit stereo Audio Codec (NAU88C22) for I<sup>2</sup>S interface
- 4 sets of CAN transceiver and header connector
- 3 sets of LED for status indication
- 2 sets of user-configurable push buttons
- USB port-0 that can be used as Device/HOST and USB port-1 that can be used as HOST
- Supports pen drives, keyboards, mouse and printers
- Provides over-voltage and over current protection
- 3.3V I/O power, 1.8V Memory power and 1.2V core power



### 3 NUTFT KIT BOARD HARDWARE CONFIGURATION

#### 3.1 Front View

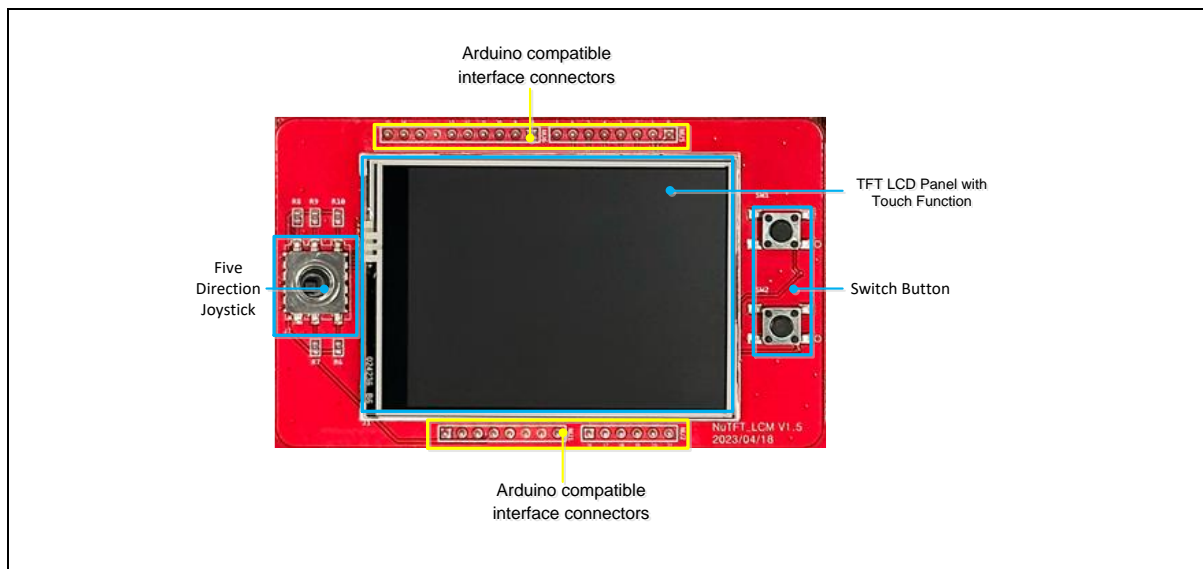


Figure 3-1 Front View of NuTFT Kit Board

Figure 3-1 shows the main components and connectors from the front side of NuTFT Kit Board. The following lists components and connectors from the front view:

- Five Direction Joystick (J3)
  - There are five dimensions: up, down, left, right and middle in five-direction joystick.
- Push Button (SW7 and SW8)
- 2.4" (320x240) LCD Panel with 4-Wire ADC Touch Function (LCD Driver is Ili9341) (J7)
  - This TFT LCD panel with 4-wire ADC touch function is equipped with driver IC, ILI9341.
  - LCD Control Interface: SPI
  - Touch Function Interface: ADC \* 4
  - Panel Size: 2.4"
  - Panel Resolution: 320x240
- Arduino UNO Compatible Interface Connectors (NU1, NU2, NU5 and NU6)

### 3.2 Rear View

shows the main components and connectors from the rear side of NuTFT Kit Board.

The following lists components and connectors from the rear view:

- 16 Mbits (2 MB) SPI Flash \* 1 (W25Q16CV) (U1)
- Arduino UNO Compatible Interface Connectors (NU1, NU2, NU5, NU6 and NU7)
- Switch (SW3, SW4)

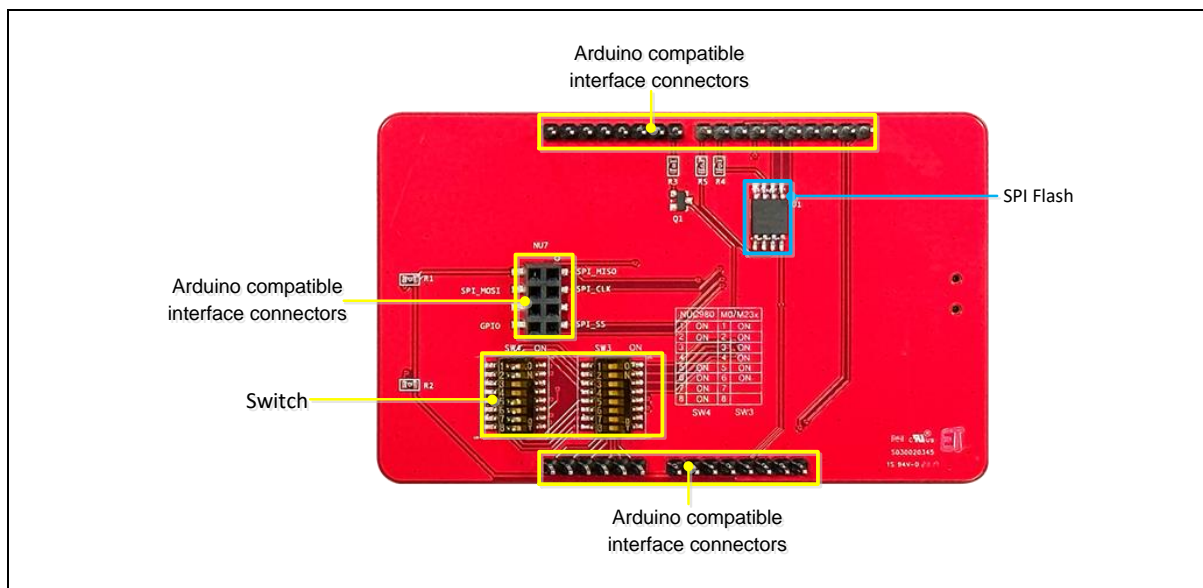


Figure 3-2 Rear View of NuTFT Kit Board

### 3.3 Extension Connectors

#### 3.3.1 Pin Assignment for Extension Connectors

NuTFT Kit Board is equipped with one SPI flash, one LCD panel with touch function, one five direction joystick and two push buttons for developing and verifying some special feature. Besides, the pin arrangement of NuTFT Kit Board is compatible with Arduino UNO.

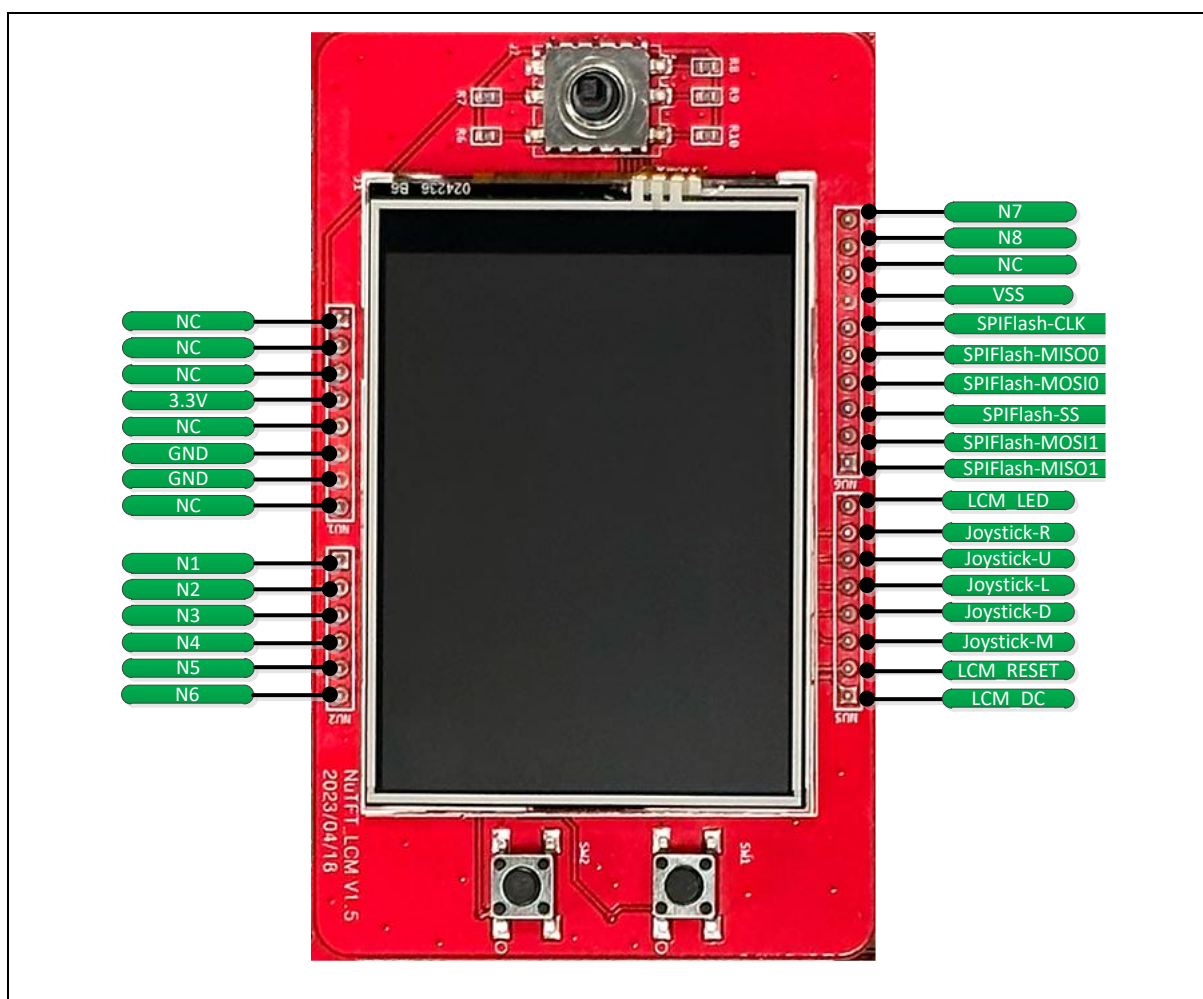


Figure 3-3 NuTFT Kit Board Extension Connectors – Front View

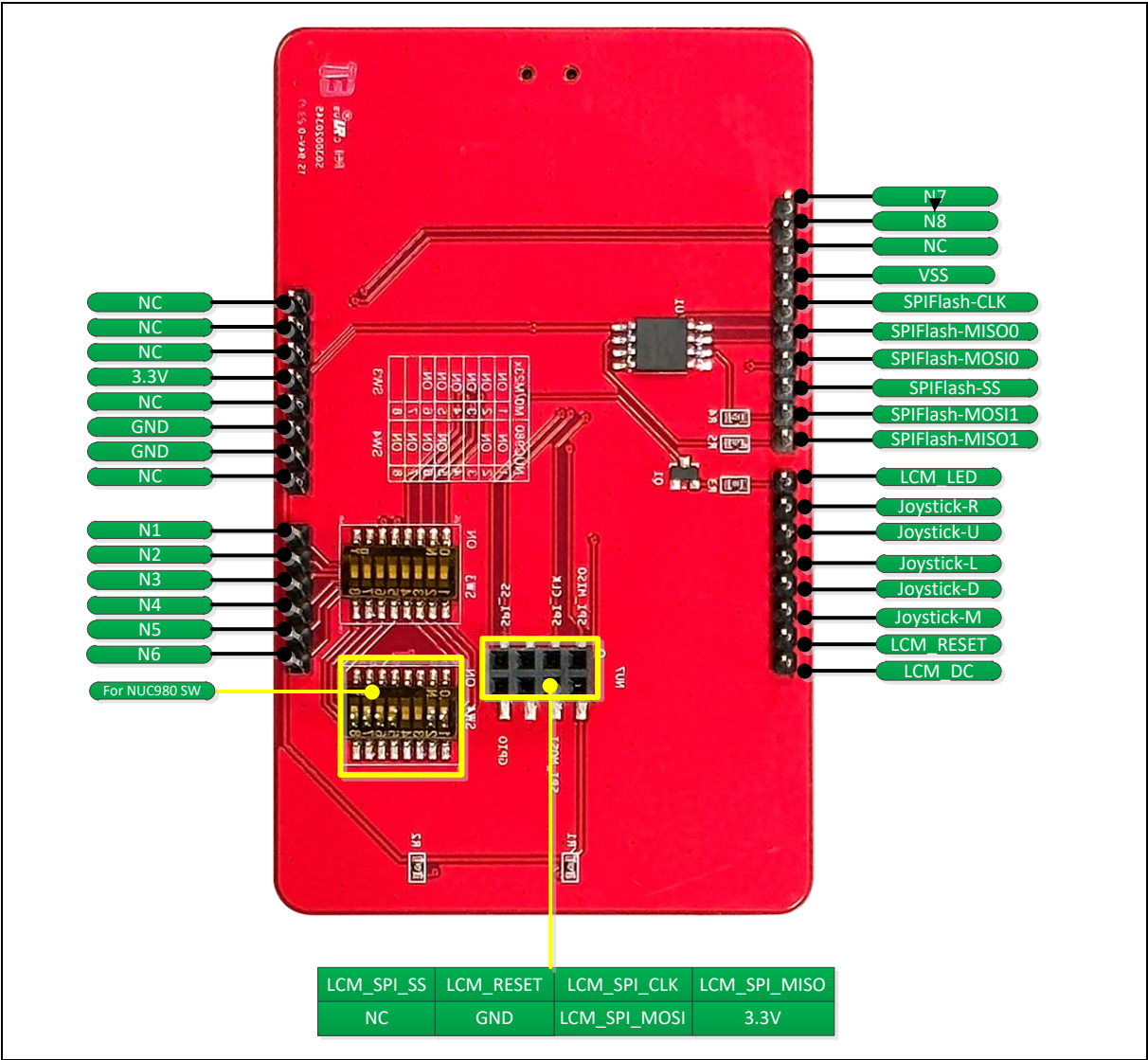


Figure 3-4 NuTFT Kit Board Connectors – Rear View

### 3.3.2 Pin Function Description

There is few different between NuMaker pin define and Arduino UNO pin define. The pin define of NU7 of Arduino is same as D11, D12, D13 (SPI function) and there is 1 set of SPI. The pin define of NU7 of NuMaker is another SPI function and there are 2 sets of SPI. If user wants to operate the NuTFT Kit Board on Arduino UNO, because it share one SPI, it recommends that control Flash and panel by switching the SPI Flash – SS0(D10) and LCM\_SPI\_SS.

Header	Arduino UNO Pin Define	NuMaker-IIoT-NUC980G2 Pin Define	NuTFT Kit Board Function
NU1	NC	NC	NC
	IOREF	3.3V	NC
	RESET	RESET	NC
	3.3V	3.3V	Power for NuTFT Kit Board
	5V	Vin	NC
	GND	VSS	GND
	GND	VSS	GND
	Vin	Vin	NC
NU2	A0	PB1	N1
	A1	PB3	N2
	A2	PB2	N3
	A3	PB0	N4
	A4	TX7	N5
	A5	RX7	N6
NU5	D0	PF9	LCM_DC
	D1	PF10	LCM_RESET
	D2	TX4	Five direction joystick – Middle
	D3	RX4	Five direction joystick – Down
	D4	PD15	Five direction joystick – Left
	D5	PD14	Five direction joystick – Up
	D6	PG6	Five direction joystick – Right
	D7	PG7	LCM_EN (PWM)

Table 3-1 NuTFT Kit Board Pin Function Description – NU1, NU2 and NU5

<i>Header</i>	<i>Arduino UNO Pin Define</i>	<i>NuMaker-IIoT-NUC980G2 Pin Define</i>	<i>NuTFT Kit Board Function</i>
<b>NU6</b>	D8	PF7	SPI Flash – MISO1
	D9	PF8	SPI Flash – MOSI1
	D10	PG11 (SPI1)	SPI Flash – SS0
	D11	RX5 (SPI0)	SPI Flash – MOSI0
	D12	TX5 (SPI0)	SPI Flash – MISO0
	D13	PG12 (SPI0)	SPI Flash – CLK0
	GND	VSS	GND
	AREF	VD33_ADC	NC
	SDA	PB7	N8
	SCL	PB5	N7
<b>NU7</b>	D12	PD11 (SPI0)	LCM_SPI_MISO
	5V	VDD33	3.3V
	D13	PD9 (SPI0)	LCM_SPI_CLK
	D11	PD10 (SPI0)	LCM_SPI_MOSI
	RESET	--	LCM Panel Reset
	GND	VSS	GND
	--	PD8 (SPI0)	LCM_SPI_SS
	--	--	--

Table 3-2 NuTFT Kit Board Pin Function Description – NU6 and NU7

<b>NuTFT Kit Board net name</b>	<b>NuTFT Kit Board Function</b>
N1	SW1
N2	SW2
N3	N3
N4	N4
N5	Touch_XL
N6	Touch_YU
N7	Touch_YD
N8	Touch_XR

Table 3-3 NuTFT Kit Board Pin Function Description –Switch for NUC980

4 NUMAKER-IIOT-NUC980G2 BOARD HARDWARE CONFIGURATION

4.1 Front View

Figure 4-1 shows the main components and connectors from the front side of NuMaker-IIoT-NUC980G2D.

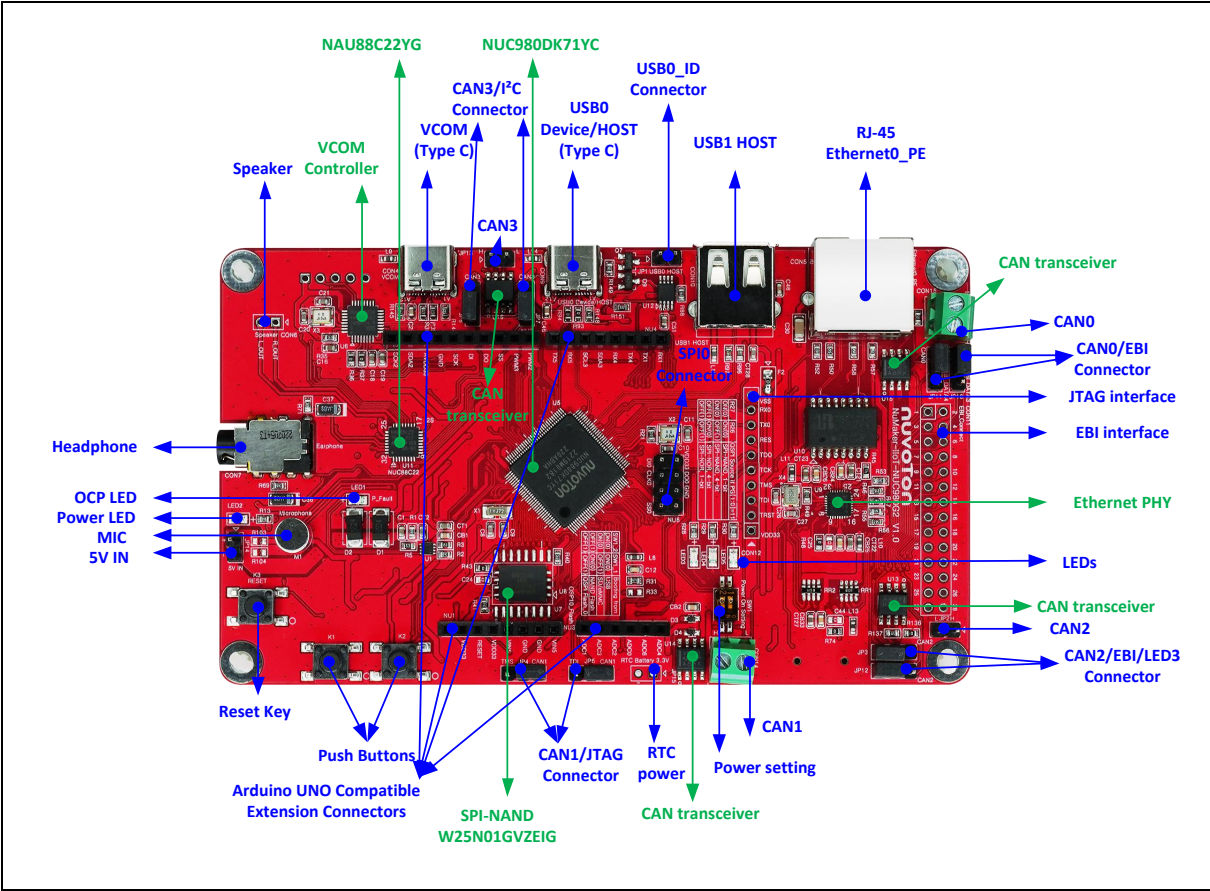


Figure 4-1 Front View of NuMaker-IIoT-NUC980G2D

- +5V In (JP14): Power adaptor 5V input

Power Model	CON4 USB Port (Type C)	CON9 USB Port (Type C)	JP14
Model 1	Connect to PC	-	-
Model 2	-	Connect to PC	-
Model 3	-	-	VDD5V Input

- Power indication LEDs (LED1, LED2):

LED	Color	Descriptions
LED1	Red	The system power will be terminated and LED1 lighting when the input



		voltage is over 5.7V or the current is over 1.7A.
LED2	Green	Power normal state.

- RTC Battery (JP15): External Battery supply for RTC 3.3V powered
  - JP15.1: Positive (+)
  - JP15.2: Negative (-)
- System Reset (K2): System will be reset if the K2 button is pressed
- Virtual COM (CON4, U6): NUC123ZD4AN0 microcontroller (U6), USB type C connector (CON4) to PC, for debug message output
- User indication LEDs (LED3, LED4, LED5):

LED	Color	GPIO pin of NUC980
LED3	Yellow	PB8
LED4	Green	PG15
LED5	Red	PB13

- SPI NAND Flash (U7, U8): Use Winbond W25N01GVZEIG 128MB (U8) for system booting; only one (U7 or U8) SPI Flash can be used, supporting dual / quad mode
- JTAG interface and UART0 (CON12)

Connector	GPIO pin of NUC980	Function
CON12.1	-	VDD33
CON12.2	GPG15	nTRST
CON12.3	GPG14, JP5(1-2)	TDI
CON12.4	GPG13, JP4(1-2)	TMS
CON12.5	GPG12	TCK
CON12.6	GPG11	TDO
CON12.7	-	nRESET
CON12.8	GPF12	UART0_TXD
CON12.9	GPF11	UART0_RXD
CON12.10	-	VSS

Note: TMS, TDI need jumper to switch JP4(1-2), JP5(1-2)

- User Key SWs (K1 and K2)

Key	GPIO pin of NUC980
K1	GPE10
K2	GPE12

- Arduino UNO compatible interface (NU1, NU2, NU3, NU4 and NU5)

Connector	GPIO pin of NUN980	Function
NU1.1	-	-
NU1.2	-	VDD33
NU1.3	-	nRESET
NU1.4	-	VDD33
NU1.5	-	VIN
NU1.6	-	VSS
NU1.7	-	VSS
NU1.8	-	VIN

Connector	GPIO pin of NUN980	Function
NU2.1	GPF7	PWM2
NU2.2	GPF8	PWM3
NU2.3	GPG11	SPI1_SS
NU2.4	GPG14	SPI1_DO
NU2.5	GPG13	SPI1_DI
NU2.6	GPG12	SPI1_CLK
NU2.7	-	VSS
NU2.8	-	ADC VDD33
NU2.9	GPB7	I2C2_SDA
NU2.10	GPB5	I2C2_SCL

Connector	GPIO pin of NUN980	Function
NU3.1	GPB1	UART9_TXD
NU3.2	GPB3	UART9_RXD
NU3.3	GPB2	ADC_AIN[2]
NU3.4	GPB0	ADC_AIN[0]
NU3.5	GPB6	UART7_TXD
NU3.6	GPB4	UART7_RXD

Connector	GPIO pin of NUN980	Function
NU4.1	GPF9	UART1_RXD

NU4.2	GPF10	UART1_TXD
NU4.3	GPD12	UART4_TXD
NU4.4	GPD13	UART4_RXD
NU4.5	GPD15	I2C3_SDA
NU4.6	GPD14	I2C3_SCL
NU4.7	GPG6	UART5_RXD
NU4.8	GPG7	UART5_TXD

Connector	GPIO pin of NUN980	Function
NU5.1	GPD11	SPI0_DI
NU5.2	-	VDD33
NU5.3	GPD9	SPI0_CLK
NU5.4	GPD10	SPI0_DO
NU5.5	-	-
NU5.6	-	VSS
NU5.7	GPD8	SPI0_SS
NU5.8	-	-

- EBI port for use (CON11)

Connector	GPIO pin of NUN980	Function
CON11.1	GPC0	EBI_DATA0
CON11.2	GPC1	EBI_DATA1
CON11.3	GPC2	EBI_DATA2
CON11.4	GPC3, JP10(1-2)	EBI_DATA3
CON11.5	GPC4, JP11(1-2)	EBI_DATA4
CON11.6	GPC5	EBI_DATA5
CON11.7	GPC6	EBI_DATA6
CON11.8	GPC7	EBI_DATA7
CON11.9	GPC8	EBI_DATA8
CON11.10	GPC9	EBI_DATA9
CON11.11	GPC10	EBI_DATA10
CON11.12	GPC11	EBI_DATA11
CON11.13	GPC12	EBI_DATA12

CON11.14	GPC13	EBI_DATA13
CON11.15	GPC14	EBI_DATA14
CON11.16	GPC15	EBI_DATA15
CON11.17	GPA7	EBI_nWE
CON11.18	GPA8	EBI_nRE
CON11.19	GPA9	EBI_nCS0
CON11.20	GPA12	EBI_ADDR8
CON11.21	GPA11	EBI_ADDR9
CON11.22	GPA10	EBI_ADDR10
CON11.23	GPB0	ADC_AIN[0]
CON11.24	GPB2	ADC_AIN[2]
CON11.25	GPB4	ADC_AIN[4]
CON11.26	GPB6	ADC_AIN[6]
CON11.27	-	VDD33
CON11.28	-	VSS

Note: EBI\_DATA3 and EBI\_DATA4 need jumper to switch JP10(1-2), JP11(1-2)

- SD1/eMMC1 (CON8): Use Micro SD/eMMC memory card for system booting, data storage or SDIO (Wi-Fi) device
- Power on setting (SW1, R24~R27)

Switch	Status	Function	GPIO pin of NUC980
SW1.2/SW1.1	ON/ON	Boot from USB	GPG1/GPG0
SW1.2/SW1.1	ON/OFF	Boot from SD/eMMC	GPG1/GPG0
SW1.2/SW1.1	OFF/ ON	Boot from NAND Flash	GPG1/GPG0
SW1.2/SW1.1	OFF/OFF	Boot from QSPI0 Flash	GPG1/GPG0

Resistance	Status	Function	GPIO pin of NUC980
R24	Solder R	Watchdog Timer OFF	GPG3
R24	Remove	Watchdog Timer ON	GPG3

Resistance	Status	Function	GPIO pin of NUC980
R25	Solder R	UART0 debug message ON	GPG5

R25	Remove	UART0 debug message OFF	GPG5
-----	--------	-------------------------	------

Resistance	Status	Function	GPIO pin of NUC980
R27/R26	Solder R/ Solder R	SPI-NAND Flash boot with 1-bit mode	GPG9/GPG8
R27/R26	Solder R/ Remove	SPI-NAND Flash boot with 4-bit mode	GPG9/GPG8
R27/R26	Remove/ Solder R	SPI-NOR Flash boot with 4-bit mode	GPG9/GPG8
R27/R26	Remove/ Remove	SPI-NOR Flash boot with 1-bit mode	GPG9/GPG8

- Audio Codec (U11, M1, CON6, CON7, CN1): Nuvoton NAU88C22YG (U11) connects to NUC980 using I<sup>2</sup>S interface
  - Microphone (M1): Through the NAU88C22YG chip sound input
  - Speaker output (CON6): Through the NAU88C22YG chip sound output

Connector	Pin Name	Functions
CON6.1	SPKOUT_R	NAU88C22YG BTL Speaker Positive Output or Right high current output.
CON6.2	SPKOUT_L	NAU88C22YG BTL Speaker Negative Output or Left high current output.

- Earphone output (CON7): Through the NAU88C22YG chip sound output
- USB0 Device/HOST (CON9, JP1): USB0 Device/HOST Type C connector, By JP1 status or defined by the ID pin of the USB type C cable
- USB1 HOST (CON10): USB1 for USB HOST with type-A connector
- Ethernet0\_PE (CON5, U9): For Ethernet port, the NUC980 support RMII interface which add one Ethernet PHY IP101GR to RJ45 connector with LED indicator
- SOC CPU: NUC980DK71YC (U5)
- CAN: CAN0, CAN1, CAN2, CAN3 transceiver (U15, U14, U13, U16) and Connector (CON15, CON14, JP2, JP13, JP10, JP11, JP4, JP5, JP3, JP12, JP7, JP8)

Connector	Pin Name
CON15.1	CAN0_H
CON15.2	CAN0_L
CON14.1	CAN1_H
CON14.2	CAN1_L
JP2.1	CAN2_H

JP2.2	CAN2_L
JP13.1	CAN3_H
JP13.2	CAN3_L
JP10.1	EBI_DATA3
JP10.2	PC3
JP10.3	CAN0_RXD
JP11.1	EBI_DATA4
JP11.2	PC4
JP11.3	CAN0_TXD
JP4.1	TMS
JP4.2	PG13
JP4.3	CAN1_RXD
JP5.1	TDI
JP5.2	PG14
JP5.3	CAN1_TXD
JP3.1	LED_YELLOW
JP3.2	PB8
JP3.3	CAN2_RXD
JP12.1	EBI_DATA0
JP12.2	PC0
JP12.3	CAN2_TXD
JP7.1	SDA0
JP7.2	PA0
JP7.3	CAN3_RXD
JP8.1	SCL0
JP8.2	PA1
JP8.3	CAN3_TXD

Note: CAN2 and CAN3 need jumper to switch JP3(2-3), JP12(2-3), JP7(2-3), JP8(2-3)

## 4.2 Rear View

Figure 4-2 shows the main components and connectors from the rear side of NuMaker-IIoT-NUC980G2D.

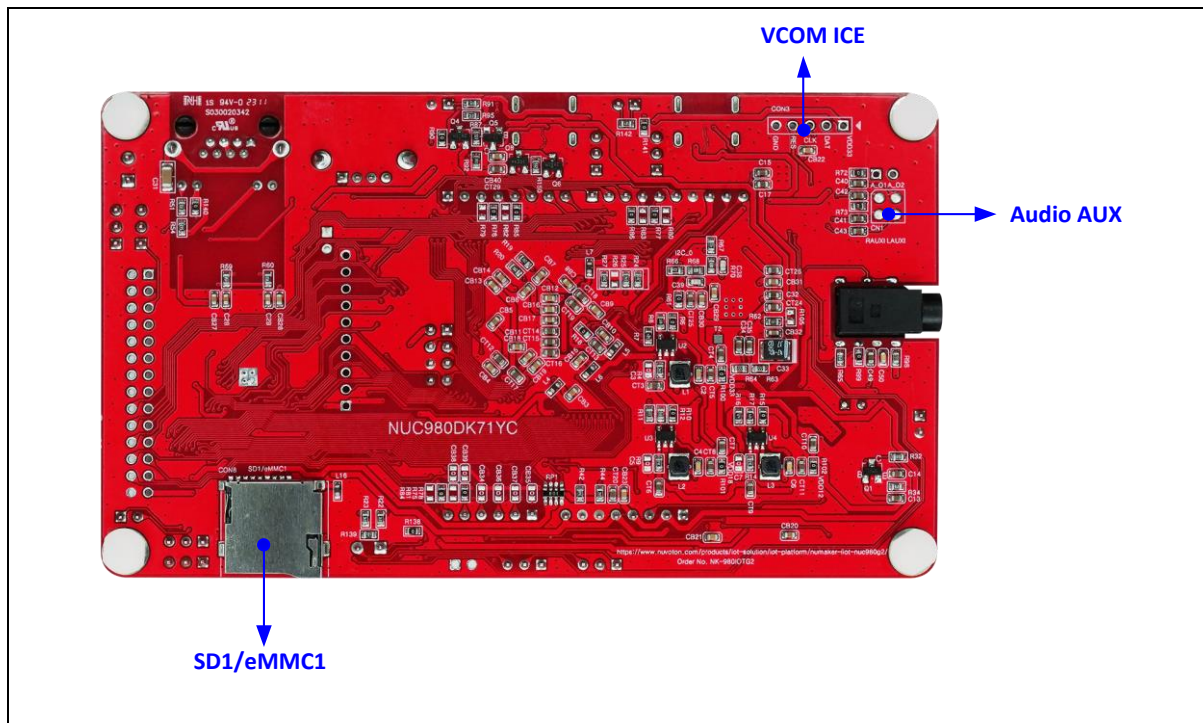


Figure 4-2 Rear View of NuMaker-IIoT-NUC980G2D

- VCOM ICE interface: ICE Controller NUC123ZD4AN0 (U6) connects CON3 to PC Host

Connector	Pin Name	Functions
CON3.1	VDD33	DC 3.3V
CON3.2	ICE_DAT	Serial Wired Debugger Data
CON3.3	ICE_CLK	Serial Wired Debugger Clock
CON3.4	RST#	VCOM Chip Reset, Active Low.
CON3.5	VSS	Power Ground

- Audio Codec (U11, M1, CON6, CON7, CN1): Nuvoton NAU88C22YG (U11) connects to NUC980 using I<sup>2</sup>S interface
  - Auxiliary Input and Output(CN1)

Connector	Pin Name	Functions
CN1.1	AUXOUT1	Mono Mixed Output / Line Output
CN1.2	AUXOUT2	Line Output
CN1.3	AUXINR	Right Auxiliary Input
CN1.4	AUXINL	Left Auxiliary Input

- MicroSD Card Slot: T-Flash slot (CON8)



## 5 QUICK START

### 5.1 Nuvoton CDC Driver Installation

The USB serial port function is used to print some messages on PC API, such as SecureCRT, through the standard UART protocol to help user to debug program.

Download and install the latest Nuvoton CDC driver:

- [https://www.nuvoton.com/resource-download.jsp?tp\\_GUID=SW1020160914071736](https://www.nuvoton.com/resource-download.jsp?tp_GUID=SW1020160914071736)

The installation is presented in Figure 5-1 and Figure 5-2.

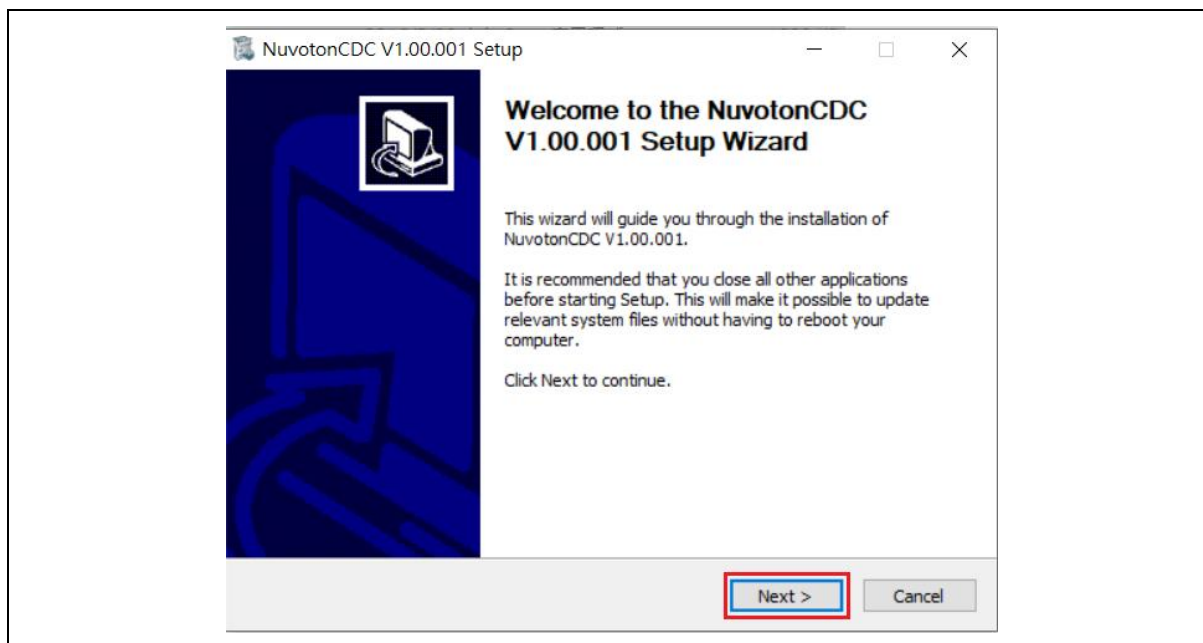
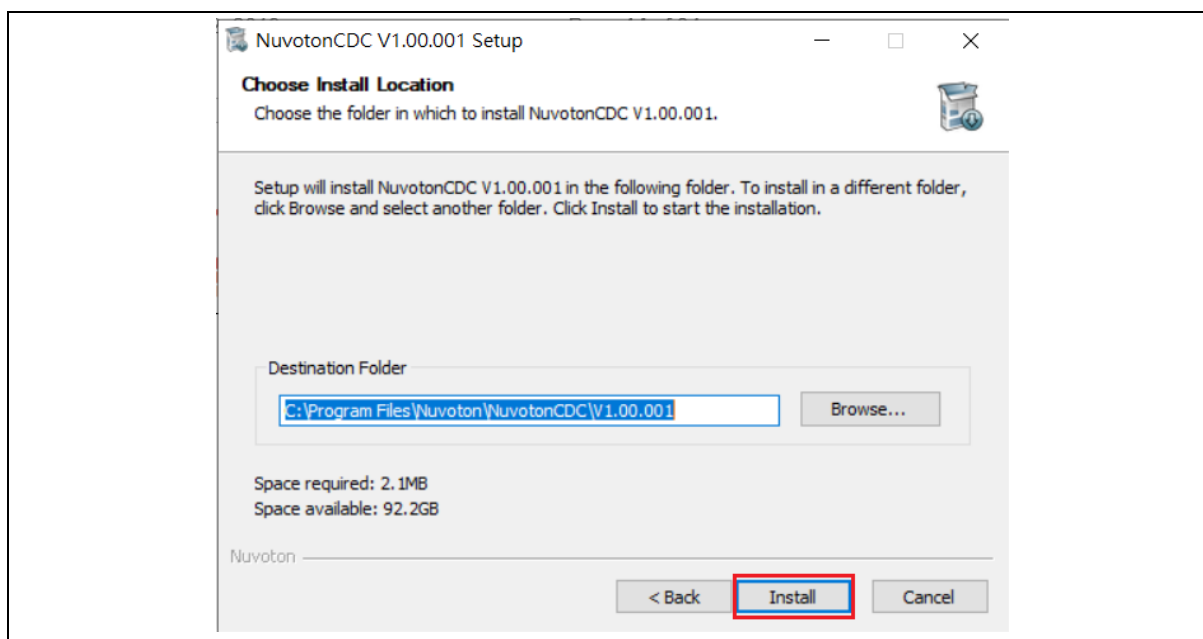


Figure 5-1 CDC Driver Installation Setup



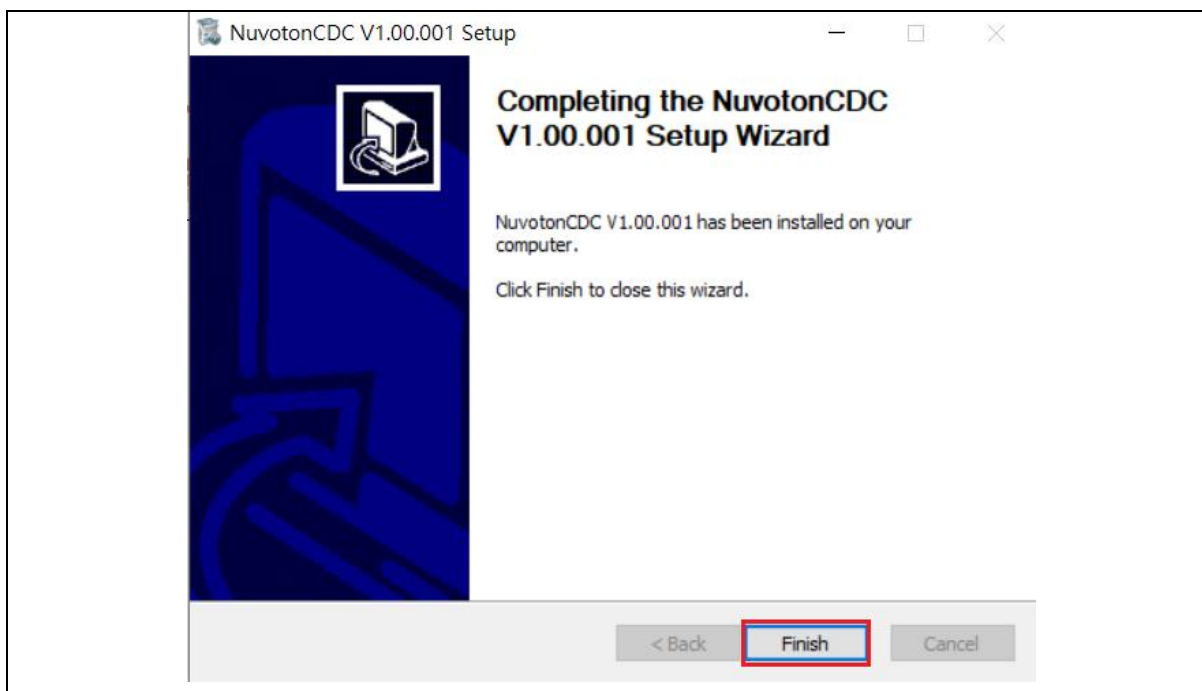


Figure 5-2 CDC Driver Installation

## 5.2 Nuvoton Virtual COM Driver Installation

The firmware programming tool **NuWriter** requires a NuWriter driver to be installed on PC first. Please follow the steps below to install the driver.

Download and install the latest Nuvoton Virtual COM driver:

- [https://github.com/OpenNuvoton/NUC980\\_NuWriter/tree/master/Driver](https://github.com/OpenNuvoton/NUC980_NuWriter/tree/master/Driver)

The installation is presented in Figure 5-3 and Figure 5-4

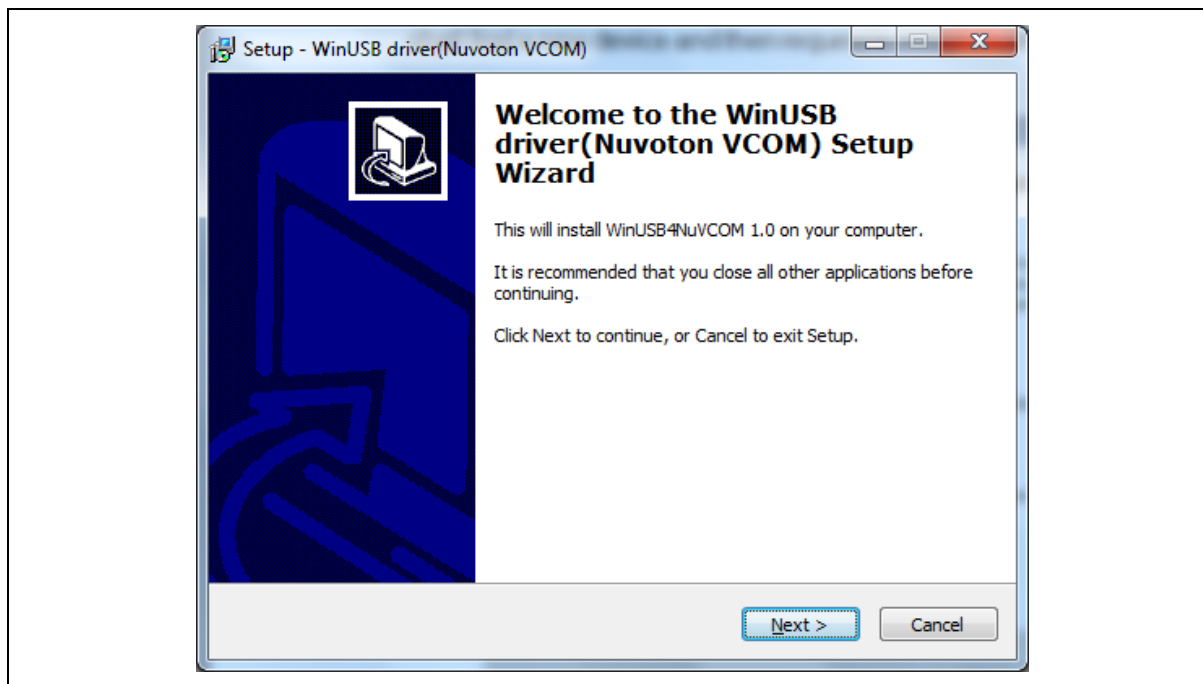
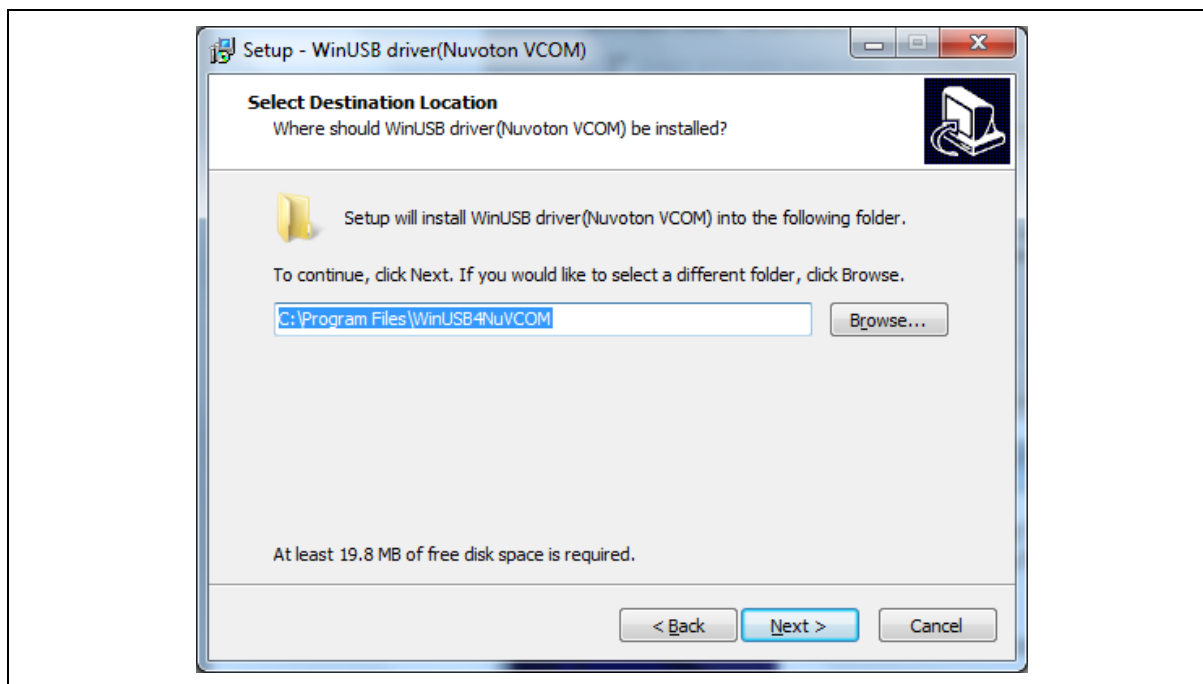
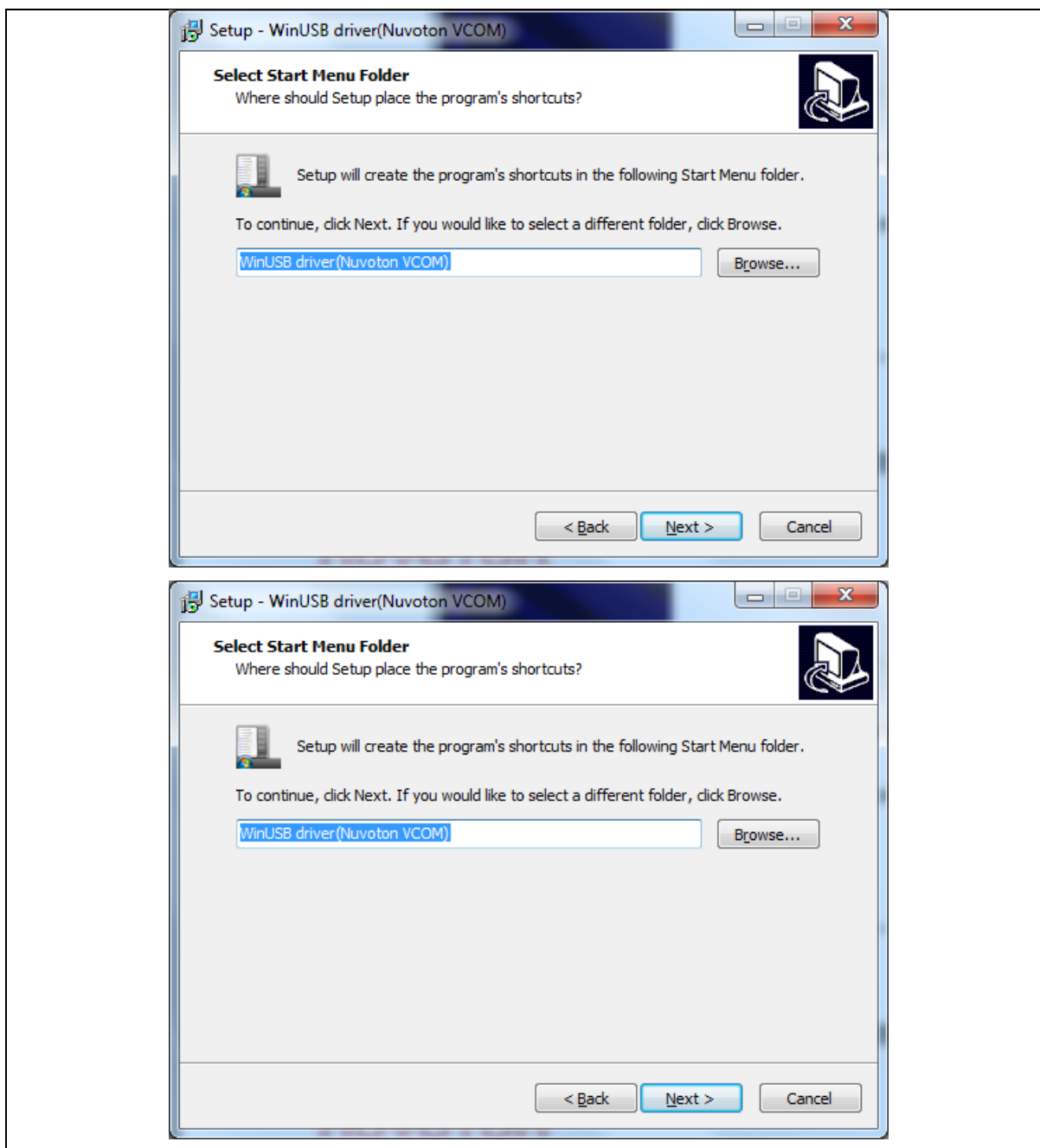


Figure 5-3 VCOM Driver Installation Setup





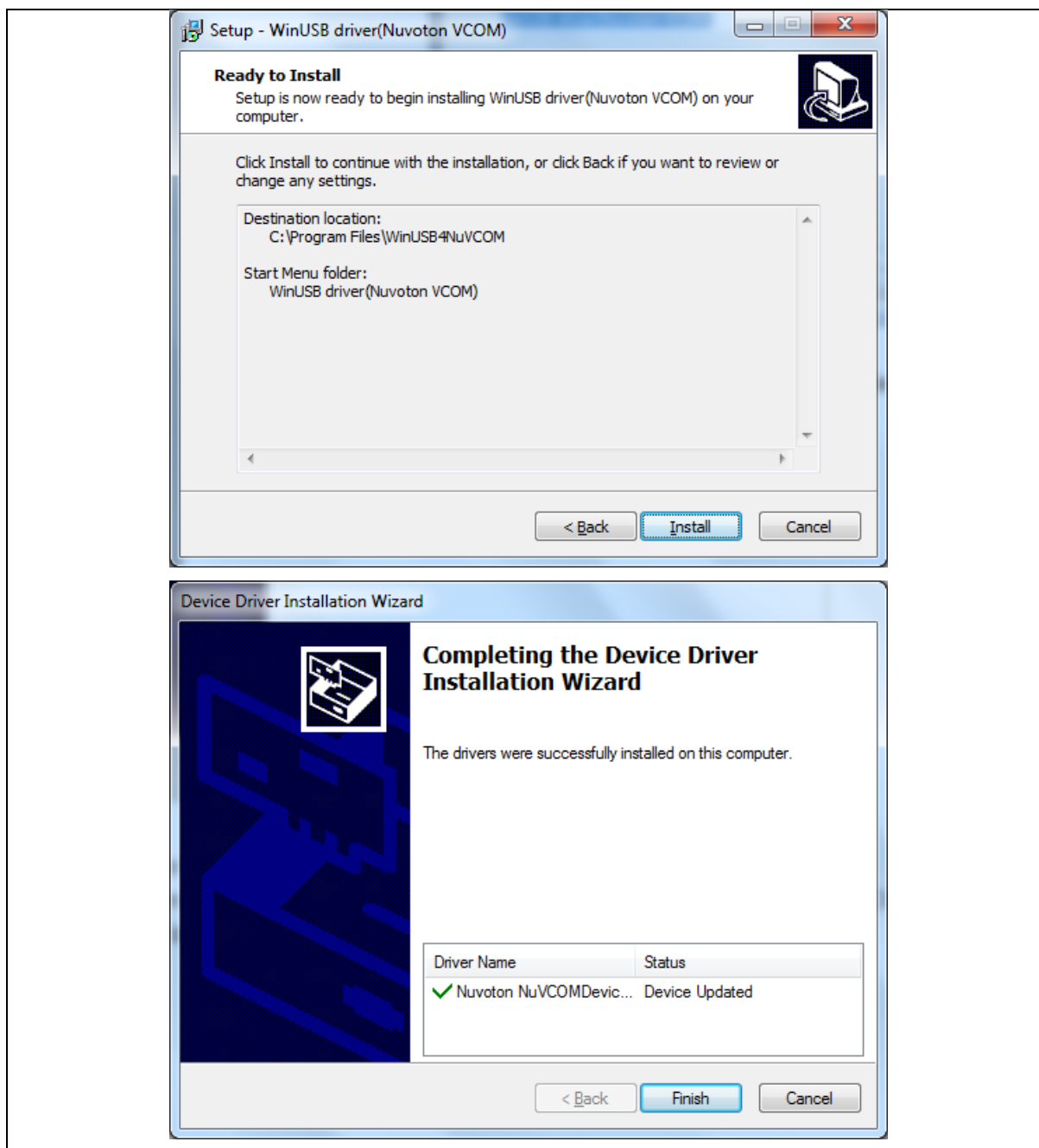


Figure 5-4 VCOM Driver Installation

## 5.3 BSP Firmware Download

NUC980 Linux BSP provides cross compilation tools based on Linux. This BSP has been tested in different x86 Linux distributions, including Ubuntu, CentOS, and Debian, etc. Since there are many distributions out there with different system configuration, sometimes it is necessary to change system setting or manually install some missing components for cross compilation.

Linux development environment could either be native, or install in a virtual machine executed on top of other operating system.

BSP download locations:

### Official website:

<https://www.nuvoton.com/products/iot-solution/iot-platform/numaker-iiot-nuc980g2d/>

- VMware Linux Virtual machine image
  - An UBUNTU18.04 VMware Image with NUC980 toolchain and Buildroot
- VMware Linux Virtual machine image User Manual
  - Introduction of NUC980 Buildroot usage and how to compile firmware for NUC980
- Hardware
  - Schematics and Gerber files
- NUC980 Linux V5.10 BSP
  - Linux BSP, NuWriter tool and documents

### Github:

<https://github.com/OpenNuvoton/MPU-Family>

For more details about NUC980 Linux 5.10 BSP, please refer to [NUC980 Linux 5.10 BSP User Manual](#).

For more details about NUC980 Linux 4.4 BSP, please refer to *NUC980 Linux 4.4 BSP User Manual* in the "/Documents" directory from [NUC980 Linux 4.4 BSP](#).

5.4 Hardware Setup

The NuMaker-IIoT-NUC980G2D provides jumpers to select boot-up conditions. To select USB ISP mode, the statuses of SW1.1 and SW1.2 are ON. For other boot selections, refer to Figure 5-5 and Table 5-1.

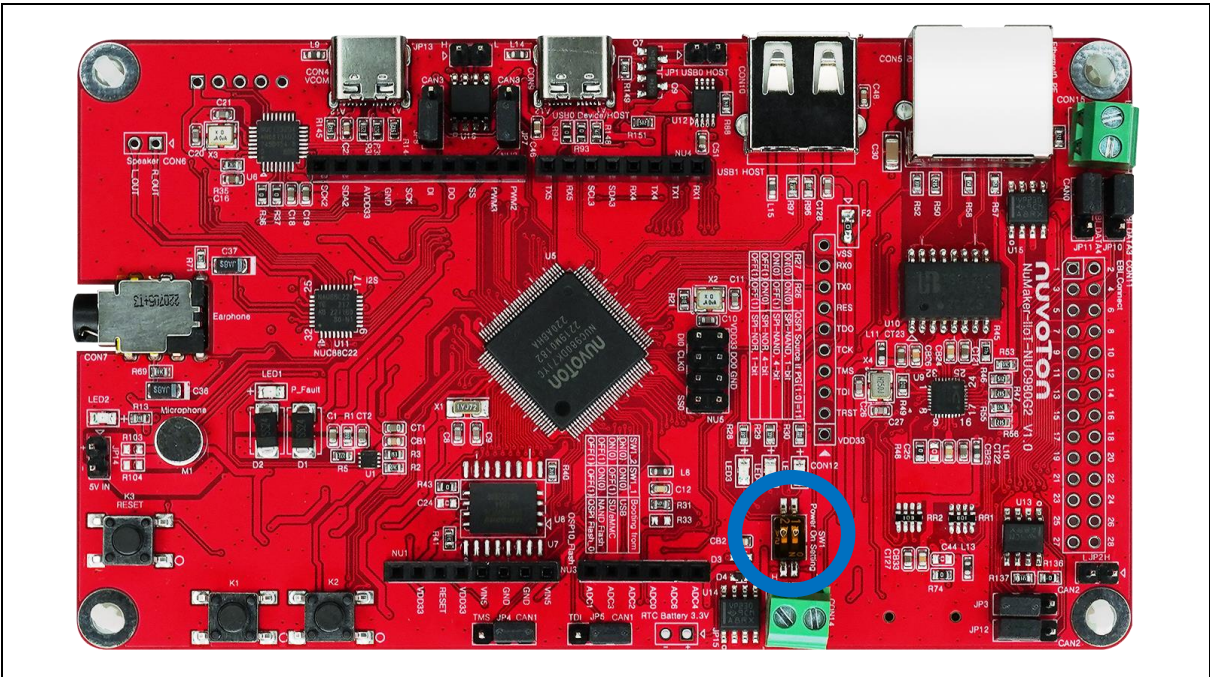


Figure 5-5 Boot Source Selection

Power-on setting	SW1.2	SW1.1
USB ISP	ON	ON
Boot from eMMC/SD	ON	OFF
Boot from NAND	OFF	ON
Boot from SPI	OFF	OFF

Table 5-1 Boot Source Selection Table

Refer to the following steps to use the debug console:

1. Connect USB-Serial connector shown in Figure 5-6 to the PC USB port through a USB type C cable.



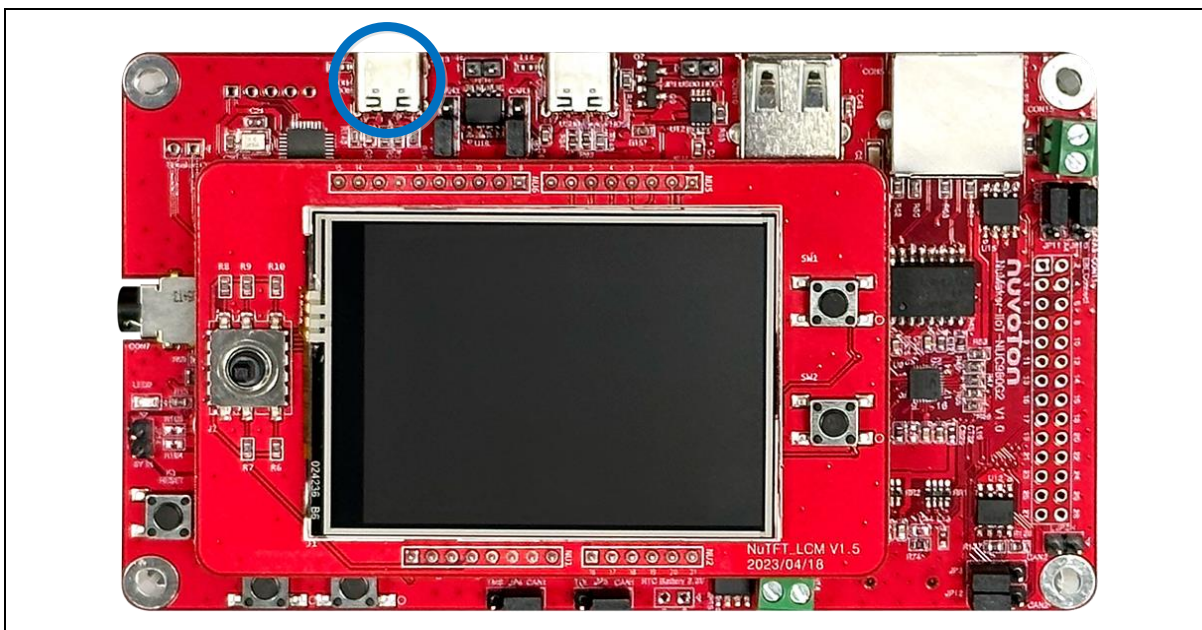


Figure 5-6 USB-Serial Debug Port

2. Find the "Nuvoton Virtual COM Port" on the Device Manger as Figure 5-7.

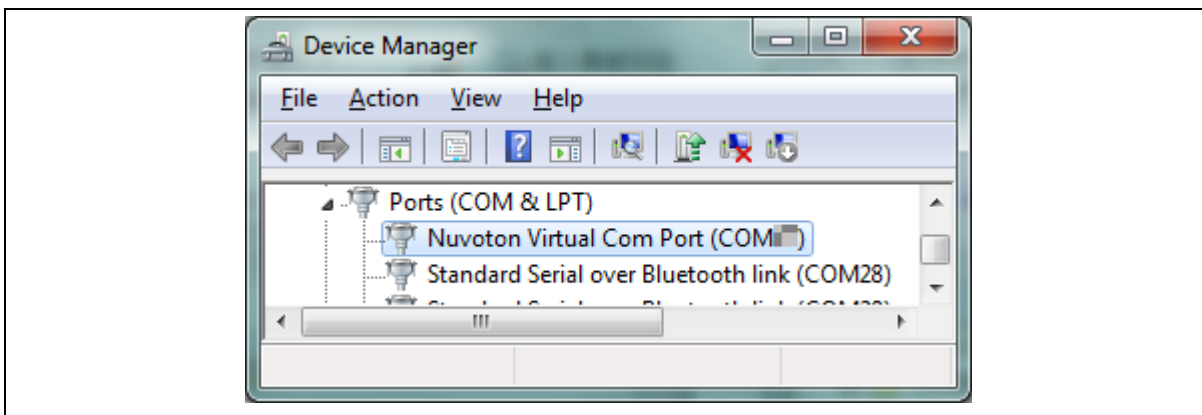


Figure 5-7 Device Manger

3. Open a serial port terminal, PuTTY for example, to print out debug message. Set the speed to 115200.

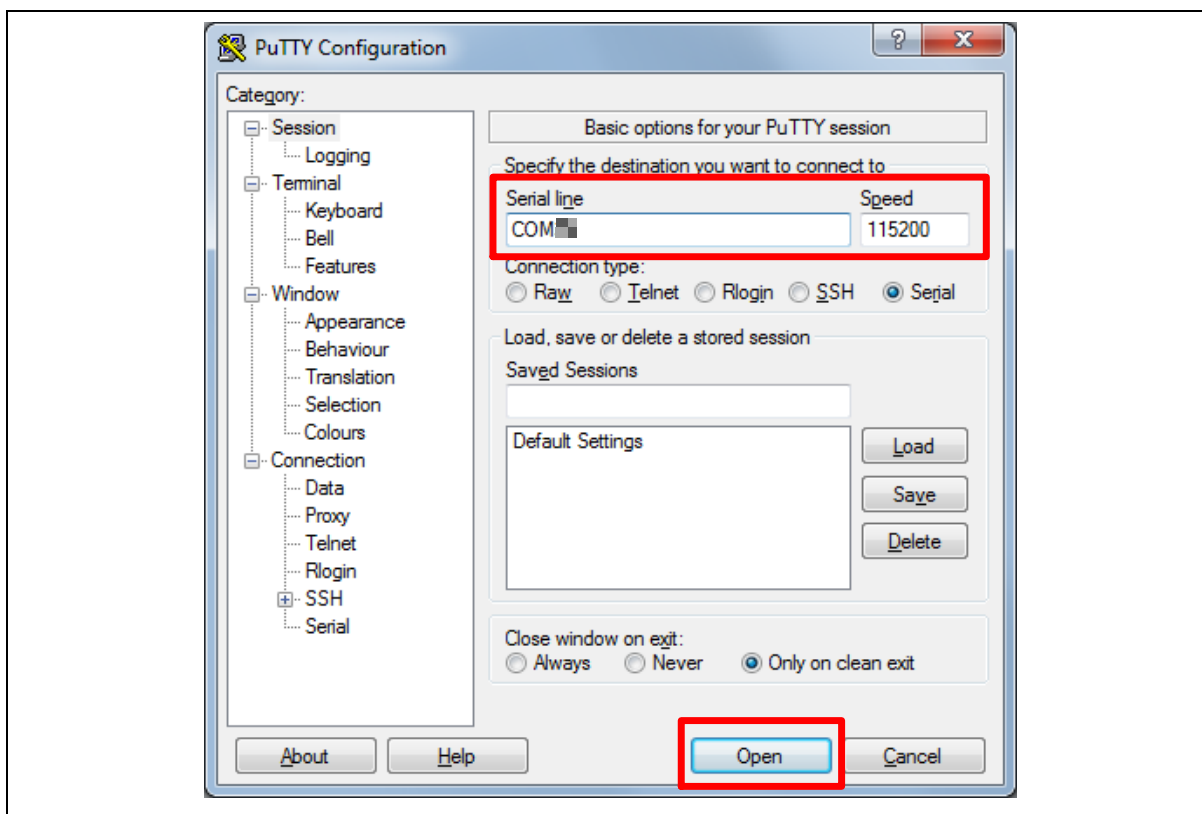


Figure 5-8 PuTTY Session Setting

Figure 5-9 is the log after booting from SPI NAND.

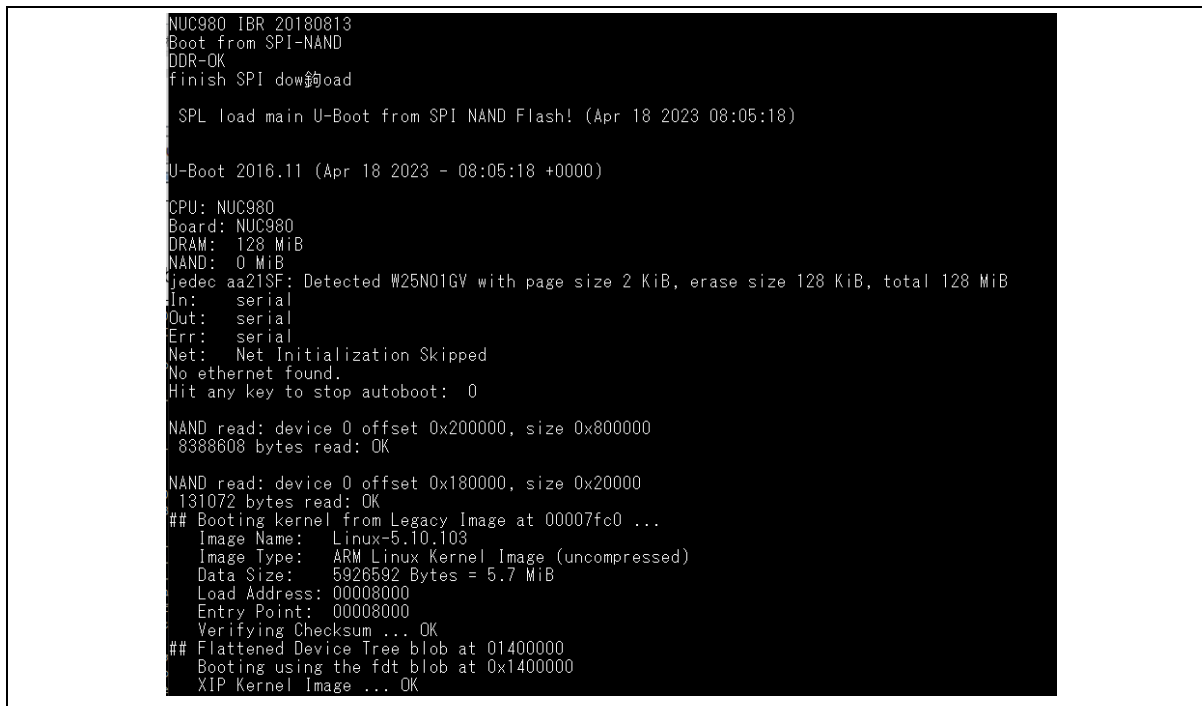


Figure 5-9 Booting Log

## 5.5 NuWriter Tool

### 5.5.1 NuWriter Setup

1. Refer to section 5.3 to install NuWriter tool.
2. Connect USB connector to the PC USB port through a USB type C cable.

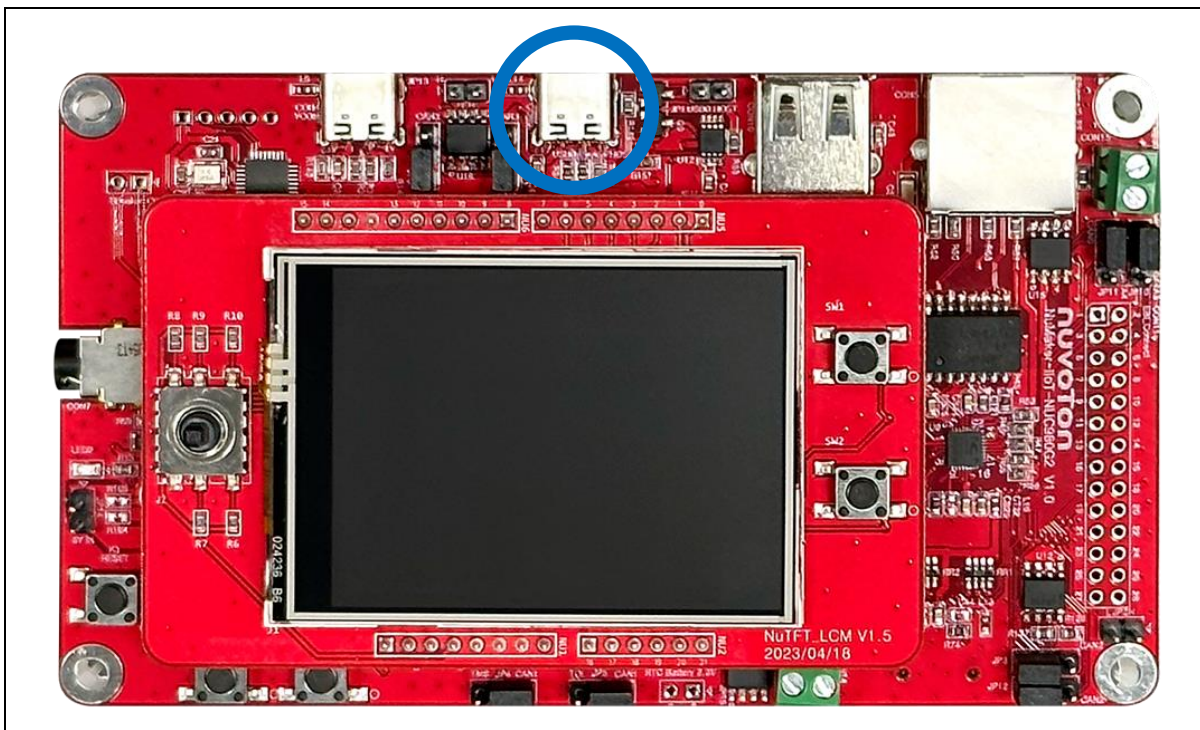


Figure 5-10 USB Port

3. Boot NuMaker-IIoT-NUC980G2D from USB ISP mode.
4. Find the "WinUSB driver (Nuvoton VCOM)" on the Device Manager as Figure 5-11.

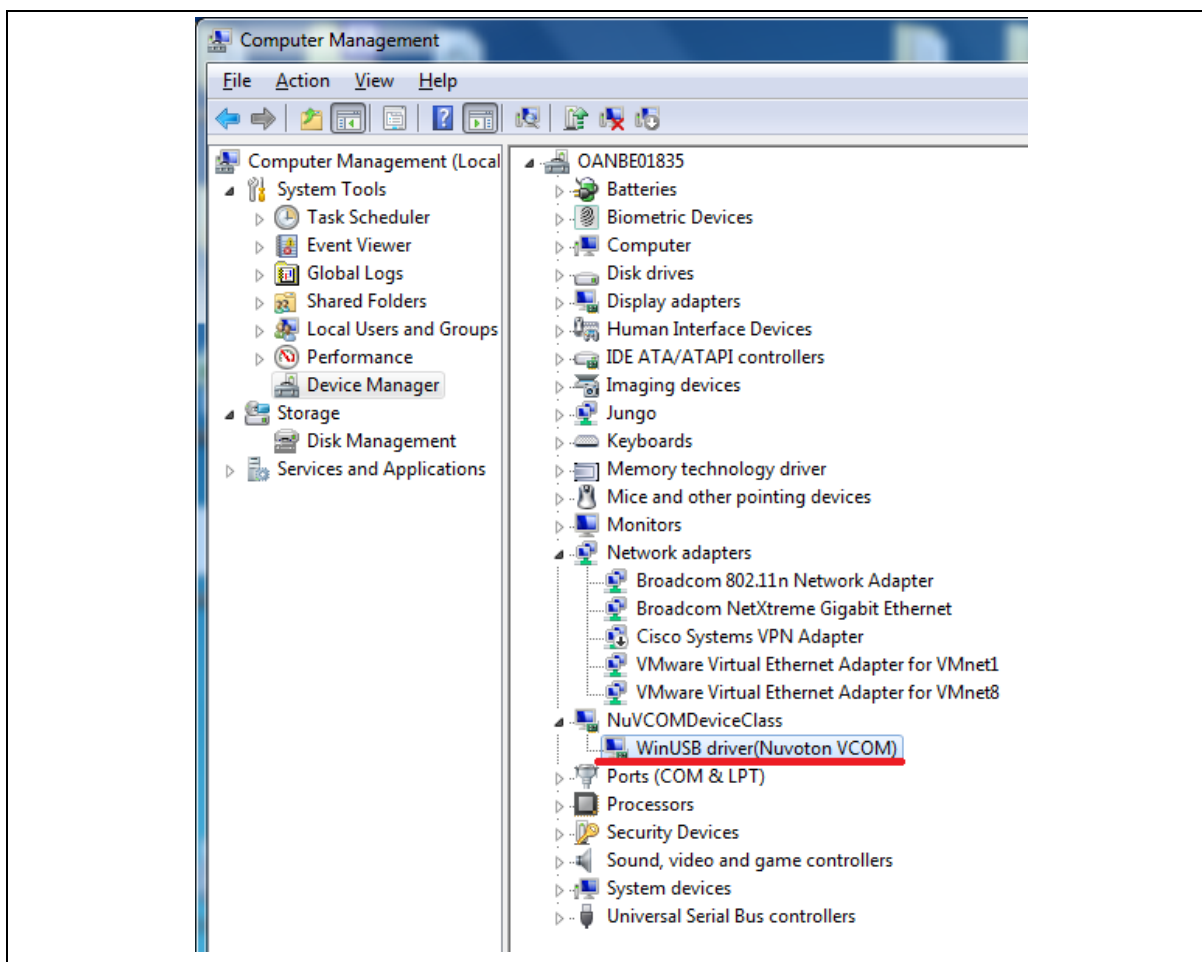


Figure 5-11 Device Manger(2)

Power on the NuMaker-IIoT-NUC980G2D, and then open the programming tool “*NuWriter.exe*” on the PC. Note that the tool cannot work if the “**WinUSB4NuVCOM**” driver is not found.

First, double-click “*NuWriter.exe*” on PC. NuWriter will start and a window appears. Select target chip as NUC980 series and select DDR parameter as DDR initial files.

DDR initial is available to set register values. For example, the default GPIO output mode is push-pull. If you want to switch to open-drain mode, it is helpful to set register values via DDR initial files.

After selecting DDR parameter, click “**Continue**” to use NuWriter tool.

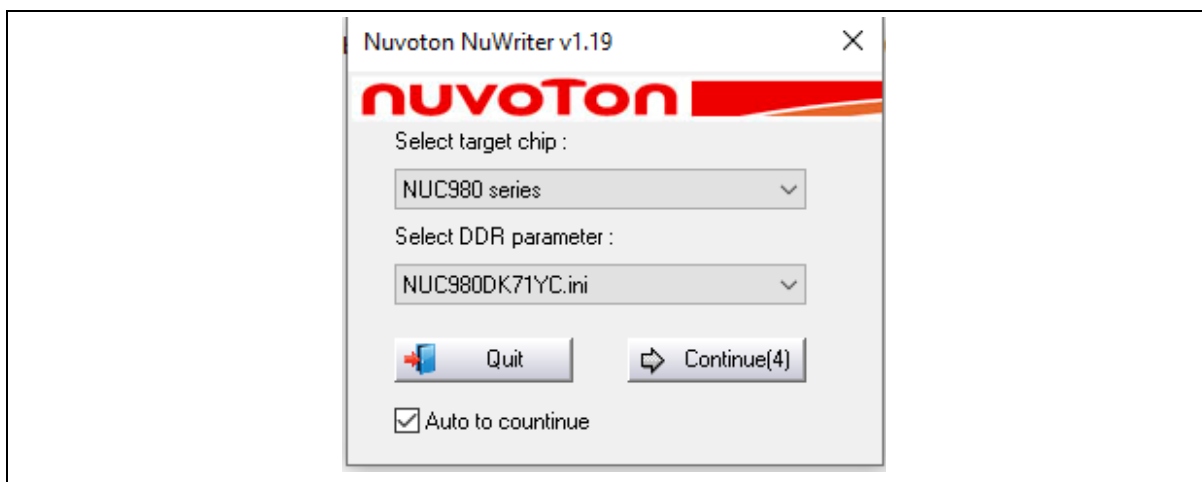


Figure 5-12 NuWriter Chip Setting

NuWriter provides 7 types of images to be downloaded including DDR/SRAM, SPI, NAND, eMMC/SD, SPI NAND, PACK and Mass Production. This chapter will guide you to download images to SPI NAND flash. If you want to choose others types to download images. For more details about NUC980 Linux BSP, please refer to *NUC980 NuWriter User Manual* in the “BSP/Documents” directory.

### 5.5.2 SPI NAND Mode

This mode can write a new image to SPI NAND flash and specify the type of the image. These types can be recognized by uboot or Linux. The Image type is set Loader, Data, Environment or Pack.

The NuMaker-IIoT-NUC980G2D default firmware consists of five images:

- u-boot-spl
- u-boot
- dtb
- ulmage
- environment variables

Please refer to *NuMaker NUC980 Linux Environment on VMware User Manual* to generate these firmware images.

#### u-boot-spl

For the Linux system, Loader Type is used to boot the Linux kernel. To compile NUC980 U-Boot to get Main U-Boot and SPL U-Boot. The SPL U-Boot is a small binary file, which will move Main U-Boot into DDR execution. The SPL U-Boot is only for NAND/SPI NAND boot. The default link address of SPL U-Boot is 0x200. For the detailed introduction of Loader Type format, refer to *NUC980 NuWriter User Manual* in the “BSP/Documents” directory

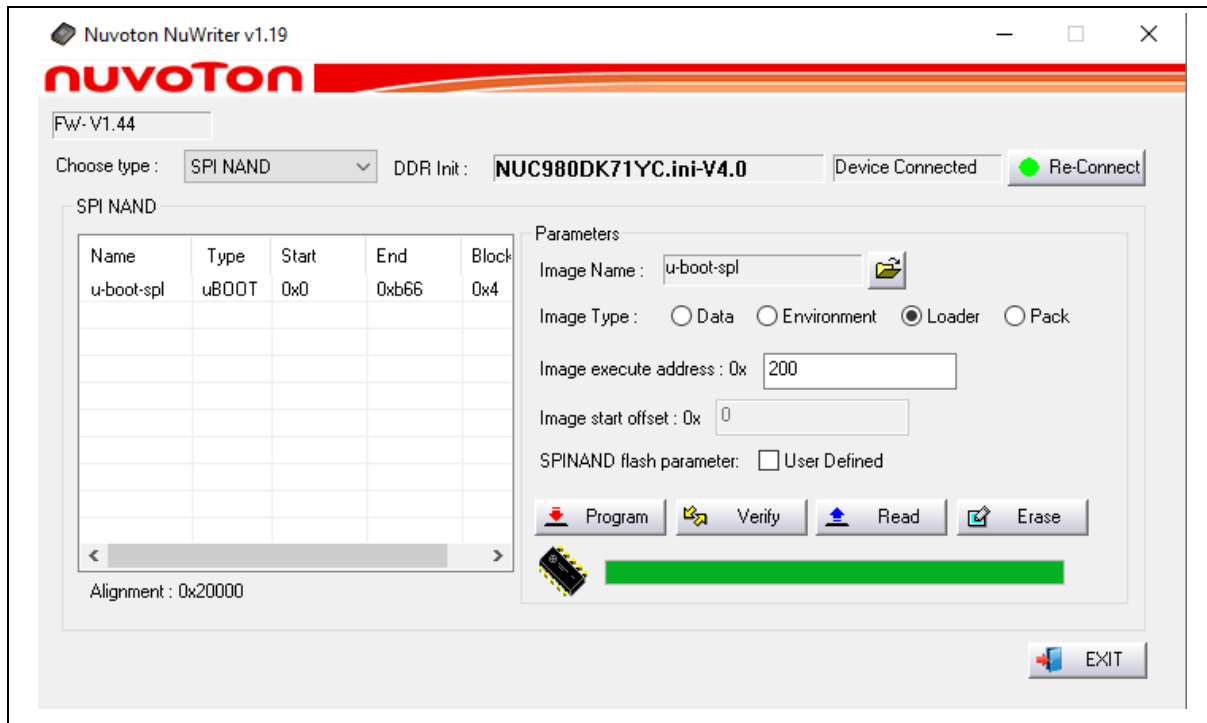


Figure 5-13 Download u-boot-spl to SPI NAND

### u-boot

For the Linux system, Loader Type is used to boot the Linux kernel. Compile NUC980 U-Boot to get Main U-Boot and SPL U-Boot. The Main U-Boot is a fully featured version of U-Boot. In this case, the Main U-Boot need to set the address at 0x100000.

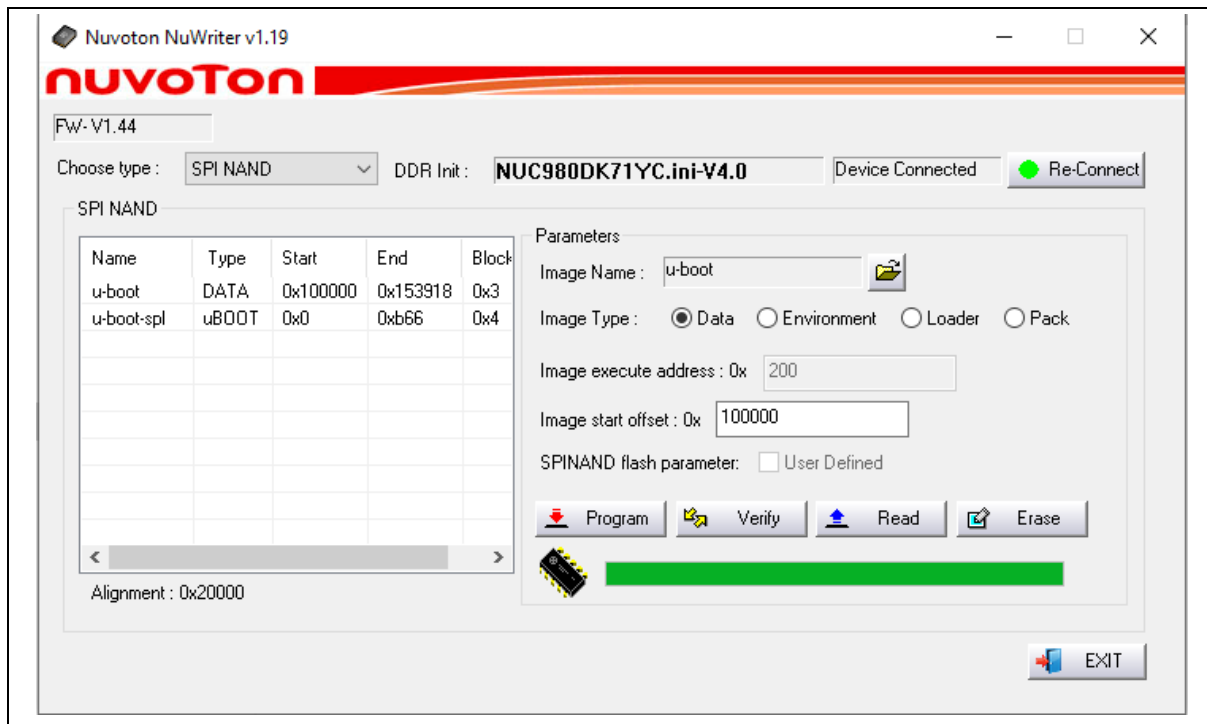


Figure 5-14 Download u-boot to SPI NAND



### dtb

Linux 5.10 need to download the dtb into SPI NAND Flash at the specified address, depending on the value of image start offset (aligned on block size boundary, block size is based on SPI NAND specifications). If dtb start offset is equal to 0x180000, download the dtb into SPI NAND Flash at the address 0x180000.

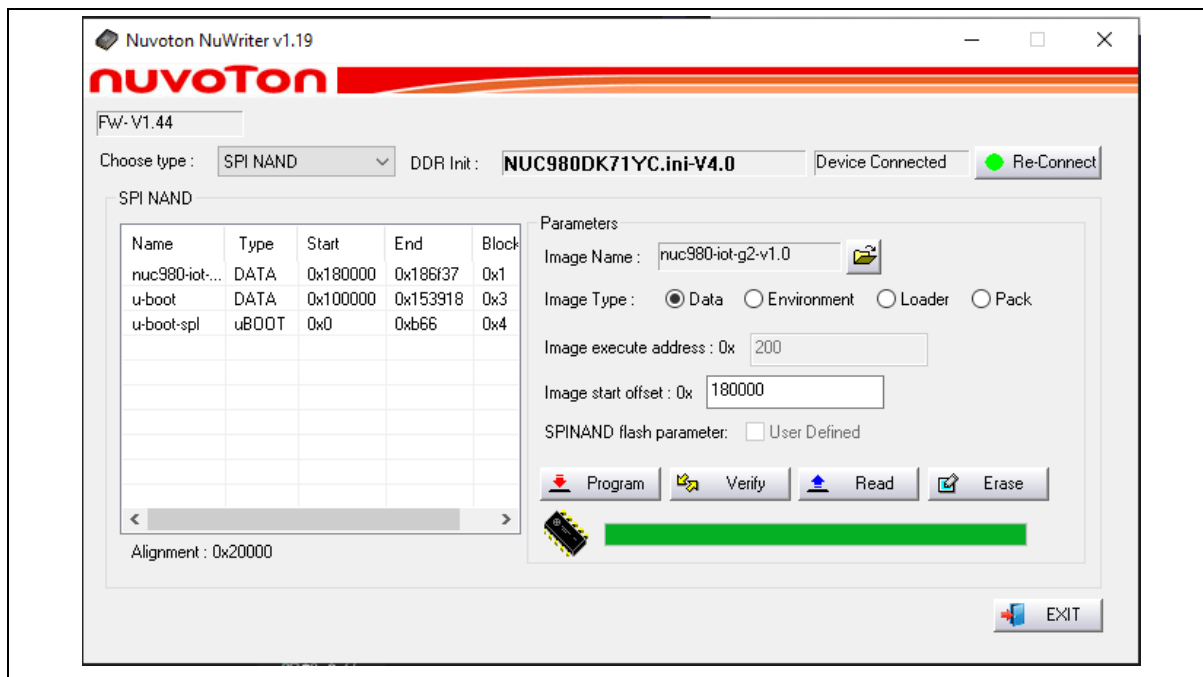


Figure 5-15 Download dtb to SPI NAND

### ulmage

Download the image of data type into SPI NAND Flash at the specified address, depending on the value of image start offset (aligned on block size boundary, block size is based on SPI NAND specifications). If image start offset is equal to 0x200000, download the image of data into SPI NAND Flash at the address 0x200000.

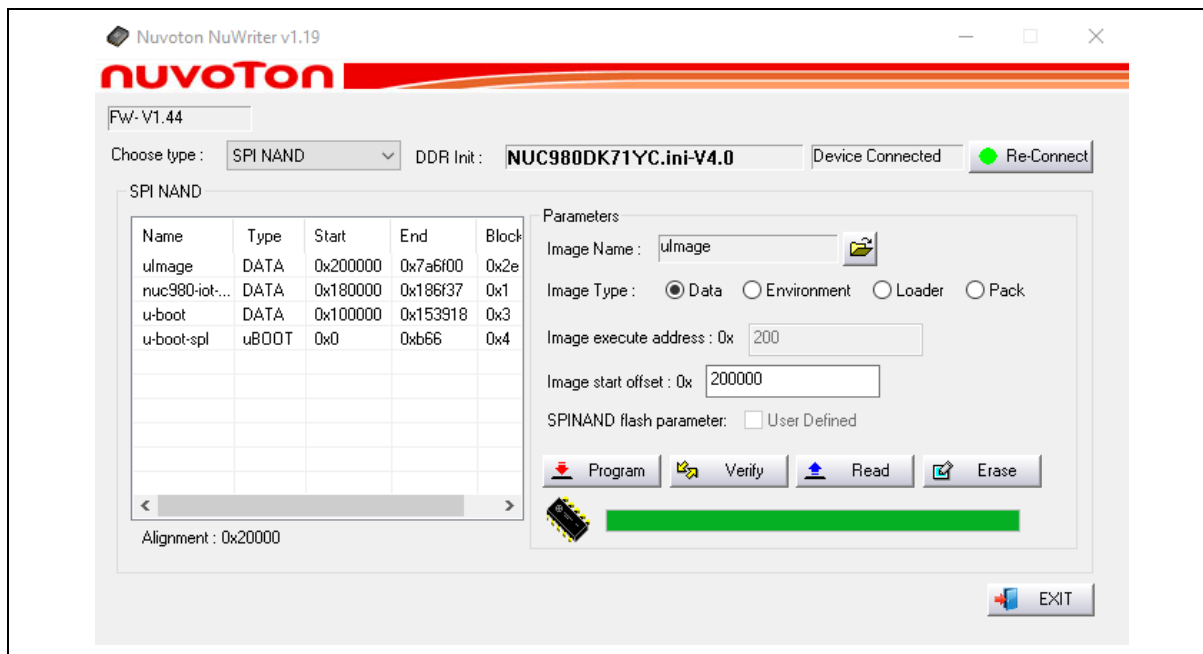




Figure 5-16 Download ulmage to SPI NAND

### environment

Loader Type is set uboot environment variables, the image of environment type into SPI NAND Flash in the specified address. U-Boot reads environment variables file to set the environment. If image start offset is equal to 0x80000, download the image of data into SPI NAND Flash at the address 0x80000.

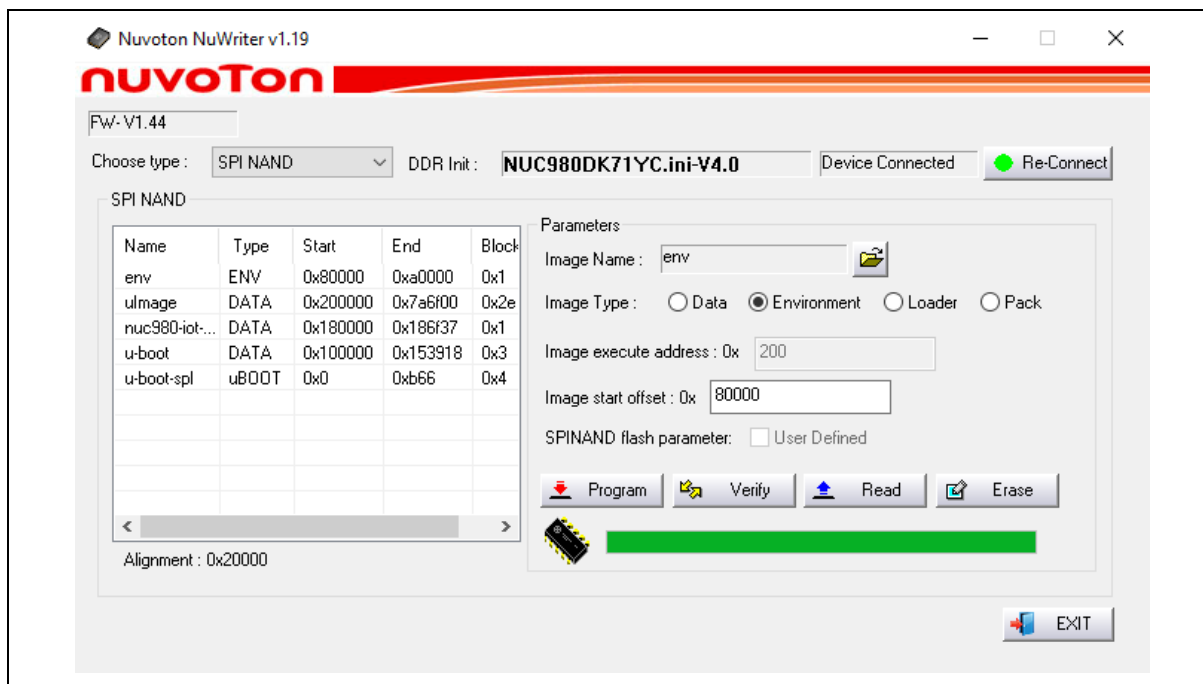


Figure 5-17 Download Environment to SPI NAND

You can create a TXT file extension and add contents. NuWriter will transform *env.txt* to an environment image and download the image to SPI NAND.

Here is an example for NuMaker-IIoT-NUC980G2D environment variables:

```
baudrate=115200
bootdelay=1
stderr=serial
stdin=serial
stdout=serial
loadkernel=nand read 0x7fc0 0x200000 0x800000
loaddtb=nand read 0x1400000 0x180000 0x20000
bootcmd=run loadkernel;run loaddtb;bootm 0x07fc0 - 0x1400000
```

## 6 NUTFT KIT BOARD SCHEMATICS

### 6.1 NuTFT Kit Board

Figure 6-1 shows the NuTFT Kit Board circuit.

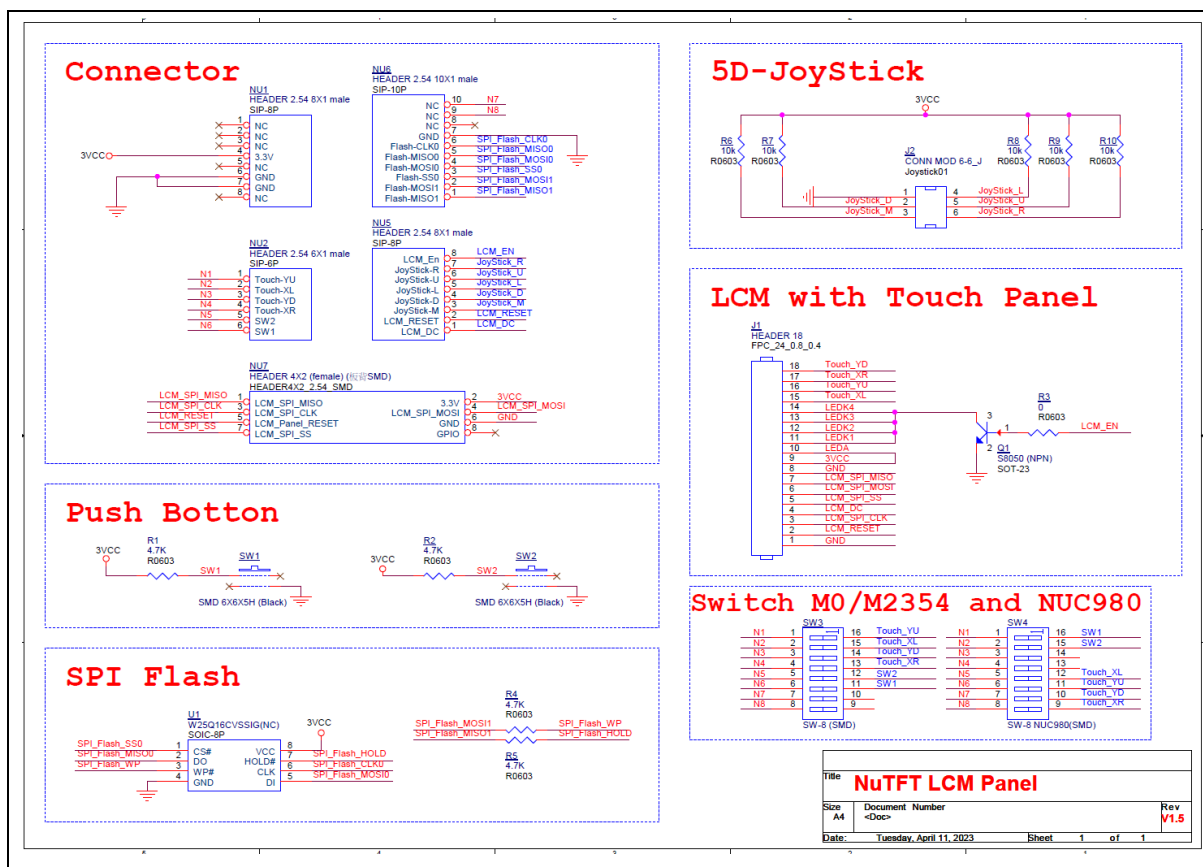
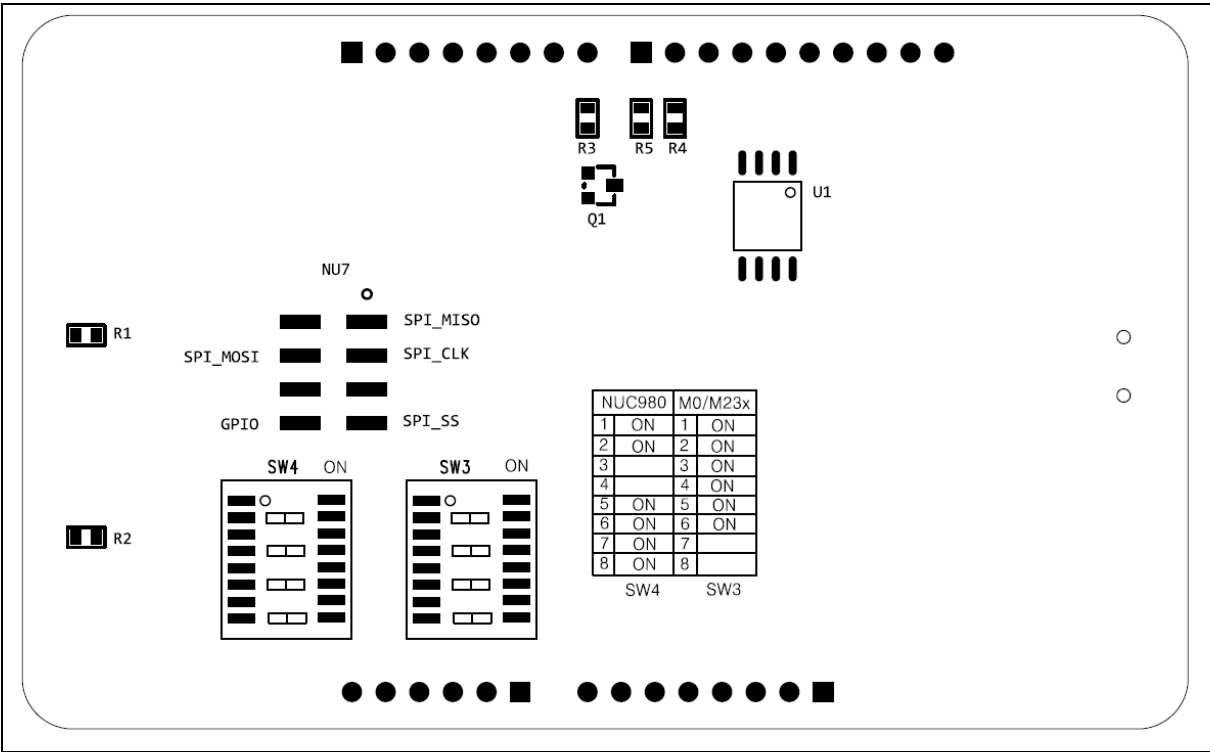
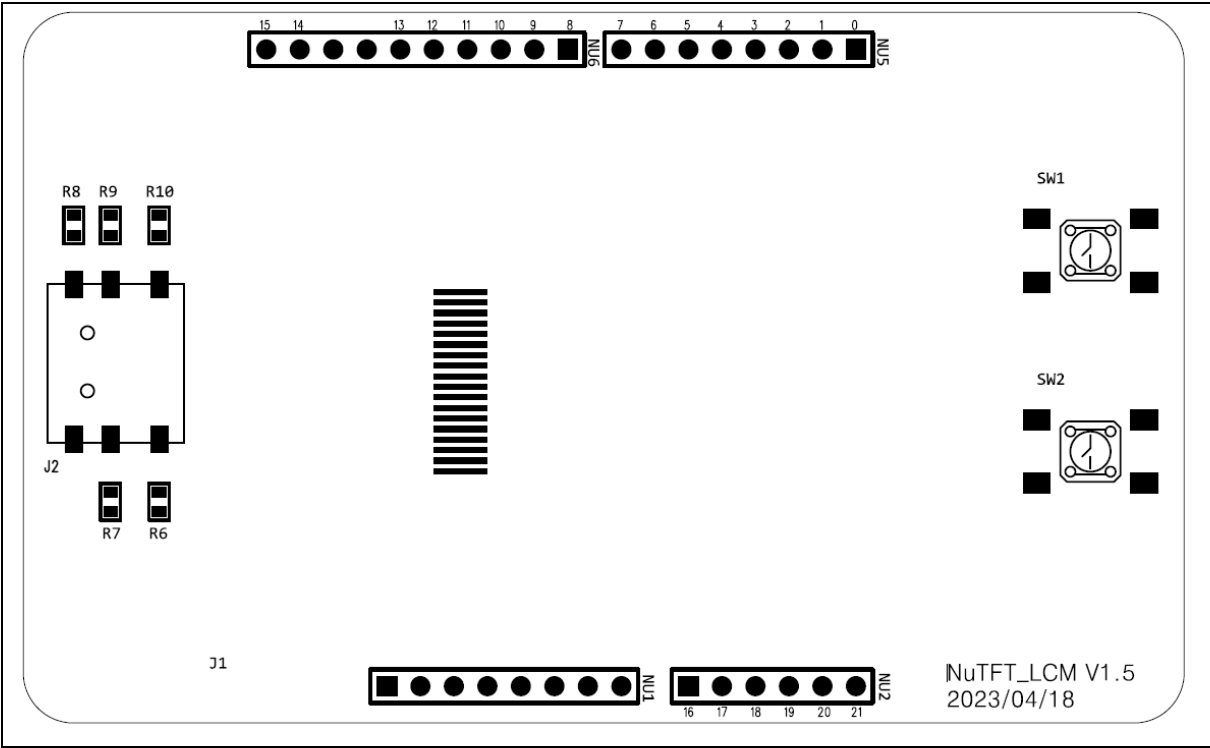


Figure 6-1 NuTFT Kit Board Circuit

6.2 PCB Placement

show the front and rear placement of NuTFT Kit Board.



## 7 NUMAKER-IIOT-NUC980G2D SCHEMATICS

### 7.1 Block Diagram Schematic

Figure 7-1 shows the Block Diagram of the NuMaker-IIoT-NUC980G2D board.

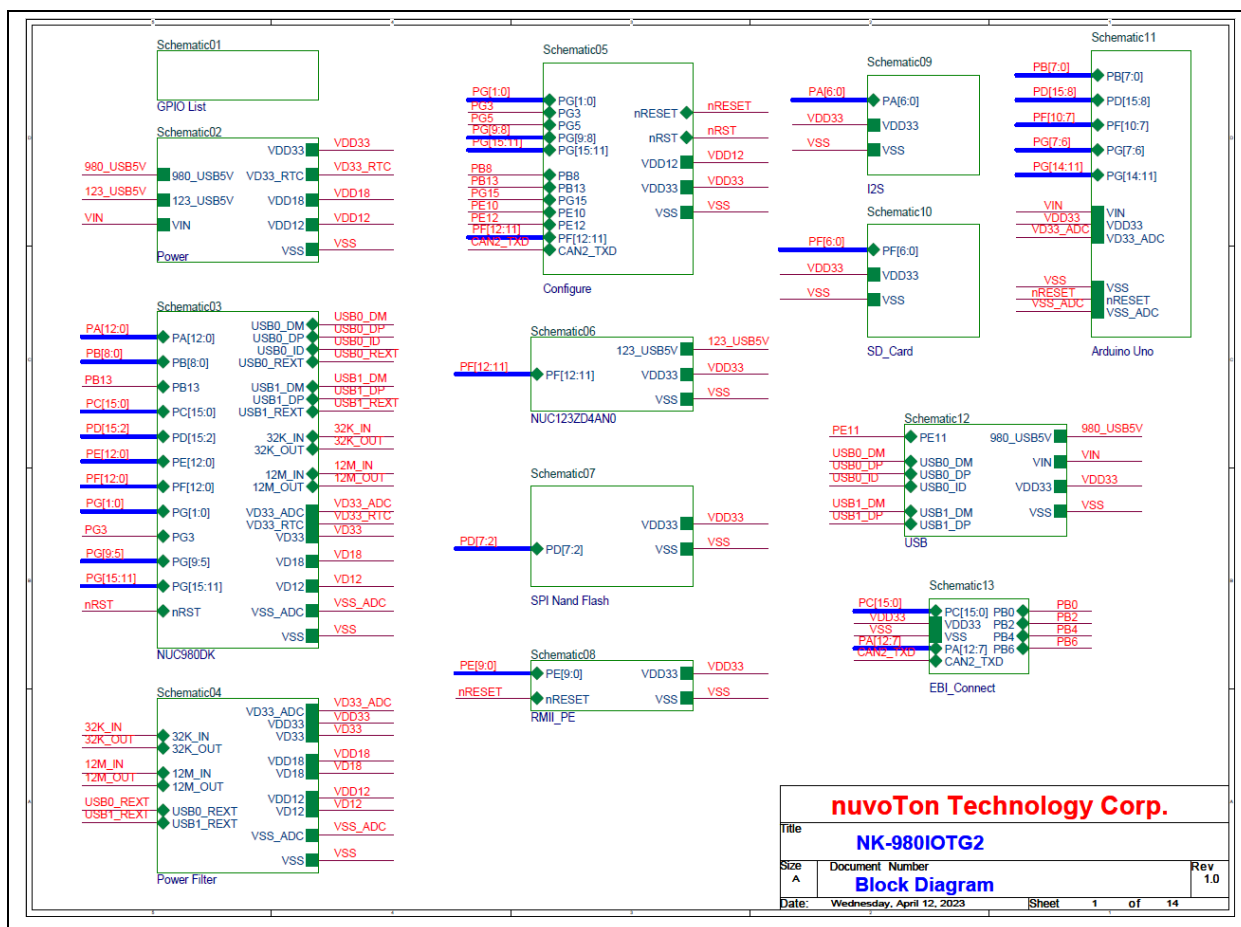


Figure 7-1 NuMaker-IIoT-NUC980G2D Board Block Diagram

## 7.2 GPIO List

Figure 7-2 shows the GPIO List of the NuMaker-IIoT-NUC980G2D board.

PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION
PA0	I2C0_SDA CAN3_RXD	PB0	ADC_AIN[0]	PC0	EBI_DATA0 CAN2_TXD	PD2	QSPI0_SS0	PE0	RMII0_RXERR	PF0	SD1_CMD eMMC1_CMD	PG0	CFG[0]
PA1	I2C0_SCL CAN3_TXD	PB1	ADC_AIN[1] UART9_TXD	PC1	EBI_DATA1	PD3	QSPI0_CLK	PE1	RMII0_CRSRV	PF1	SD1_CLK eMMC1_CLK	PG1	CFG[1]
PA2	I2S_LRCK	PB2	ADC_AIN[2]	PC2	EBI_DATA2	PD4	QSPI0_DO	PE2	RMII0_RXD1	PF2	SD1_DATA0 eMMC1_DATA0	PG3	CFG[3]
PA3	I2S_BCLK	PB3	ADC_AIN[3] UART9_RXD	PC3	EBI_DATA3 CAN0_RXD	PD5	QSPI0_DI	PE3	RMII0_RXD0	PF3	SD1_DATA1 eMMC1_DATA1	PG5	CFG[5]
PA4	I2S_DI	PB4	I2C1_SCL ADC_AIN[4] UART7_RXD	PC4	EBI_DATA4 CAN0_TXD	PD6	QSPI0_D2	PE4	RMII0_REFCLK	PF4	SD1_DATA2 eMMC1_DATA2	PG6	UART5_RXD PWM10
PA5	I2S_DO	PB5	I2C2_SCL ADC_AIN[5]	PC5	EBI_DATA5	PD7	QSPI0_D3	PE5	RMII0_TXEN	PF5	SD1_DATA3 eMMC1_DATA3	PG7	UART5_TXD PWM11
PA6	I2S_MCLK	PB6	I2C1_SDA ADC_AIN[6] UART7_TXD	PC6	EBI_DATA6	PD8	SPI0_SS0	PE6	RMII0_TXD1	PF6	SD1_nCD	PG8	CFG[8]
PA7	EBI_nWE	PB7	I2C2_SDA ADC_AIN[7]	PC7	EBI_DATA7	PD9	SPI0_CLK	PE7	RMII0_TXD0	PF7	PWM02	PG9	CFG[9]
PA8	EBI_nRE	PB8	LED_Y CAN2_RXD	PC8	EBI_DATA8	PD10	SPI0_DO UART6_TXD	PE8	RMII0_MDIO	PF8	PWM03	PG11	JTAG0_TDO SPI1_SS0 PWM10
PA9	EBI_nCS0	PB9	LED_R	PC9	EBI_DATA9	PD11	SPI0_DI UART6_RXD	PE9	RMII0_MDC	PF9	UART1_RXD PWM10	PG12	JTAG0_TCK SPI1_CLK PWM11
PA10	EBI_ADDR10 (LCD_RS)	PB10		PC10	EBI_DATA10	PD12	UART4_TXD PWM00	PE10	Key1	PF10	UART1_TXD PWM11	PG13	JTAG0_TMS SPI1_DO PWM12 UART5_RXD CAN1_RXD
PA11	EBI_ADDR9 (LCD_RESET)	PB11		PC11	EBI_DATA11	PD13	UART4_RXD PWM01	PE11	USB0_VBUSVLD	PF11	UART0_RXD	PG14	JTAG0_TDI SPI1_DI PWM13 UART5_TXD CAN1_TXD
PA12	EBI_ADDR8 (LCD_BL)	PB12		PC12	EBI_DATA12	PD14	I2C3_SCL PWM02	PE12	Key2	PF12	UART0_TXD	PG15	JTAG0_NTRST LED_G
				PC13	EBI_DATA13	PD15	I2C3_SDA PWM03						
				PC14	EBI_DATA14								
				PC15	EBI_DATA15								

**nuvoTon Technology Corp.**

Title: **NK-980IOTG2**

Size: A Document Number: **GPIO List** Rev: 1.0

Date: Monday, March 20, 2023 Sheet: 2 of 14

Figure 7-2 GPIO List

### 7.3 Power

Figure 7-3 shows the power circuit of the NuMaker-IIoT-NUC980G2D board.

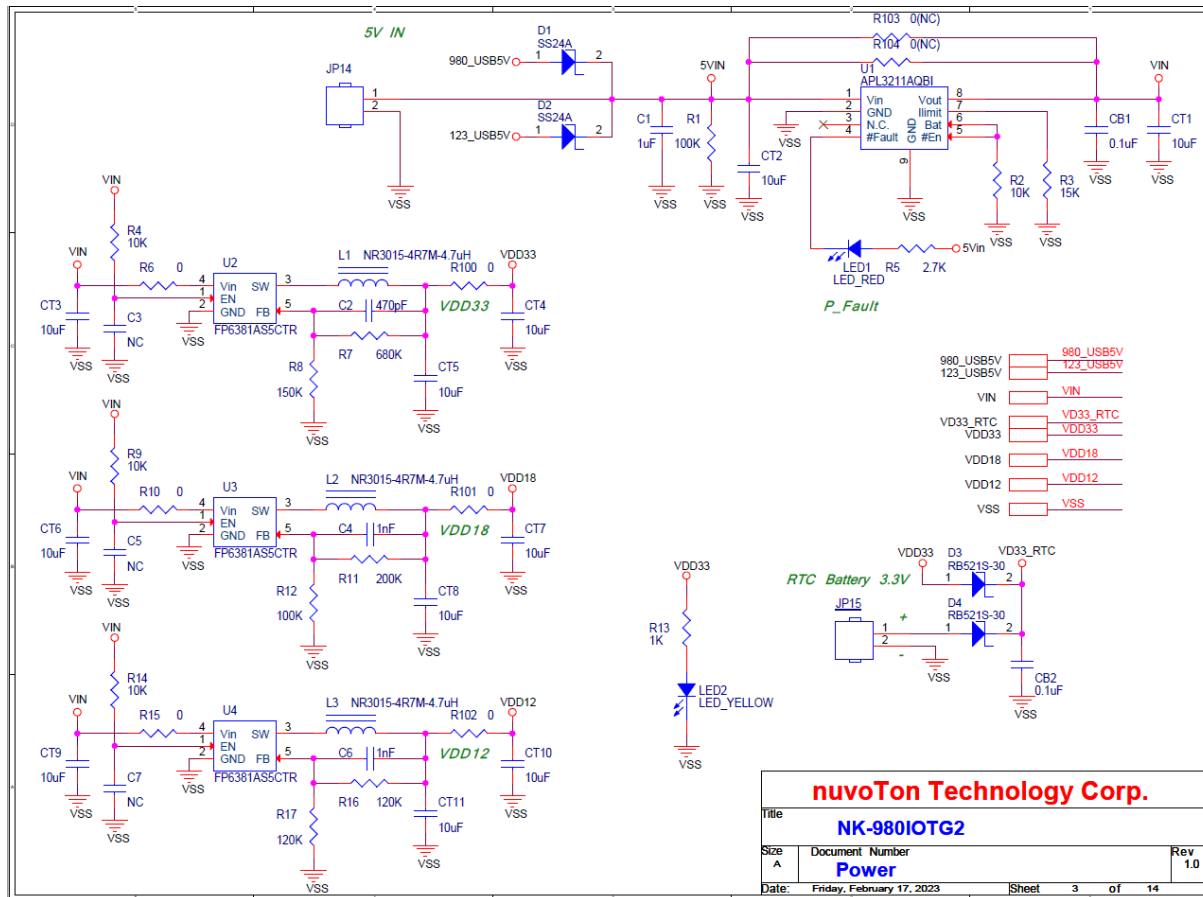


Figure 7-3 Power





7.5 Power Filter

Figure 7-5 shows the power filter of the NuMaker-IIoT-NUC980G2D board.

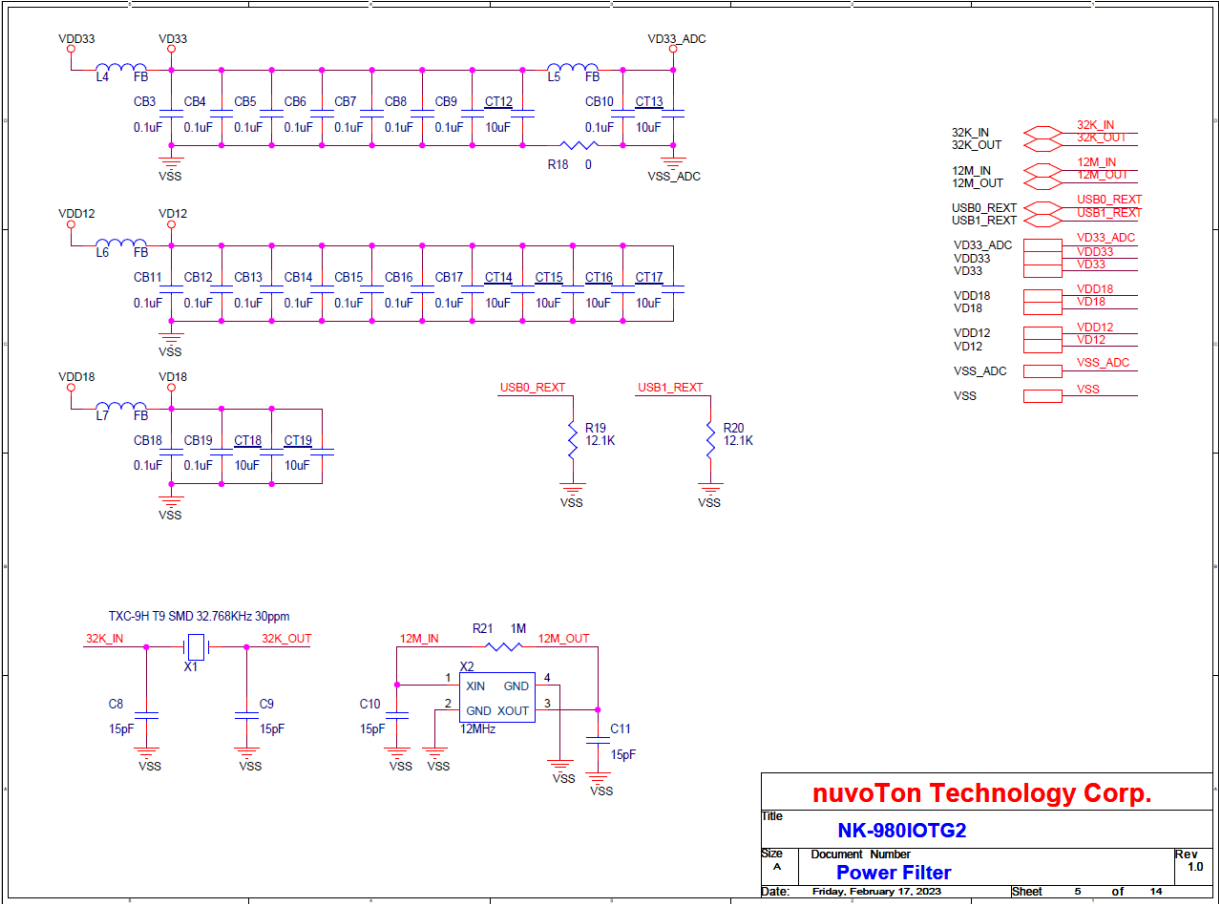


Figure 7-5 Power Filter

## 7.6 Power-on Setting, CAN\_1 and CAN\_2

Figure 7-6 shows the power-on setting, CAN\_1 and CAN\_2 circuit of the NuMaker-IIoT-NUC980G2D board.

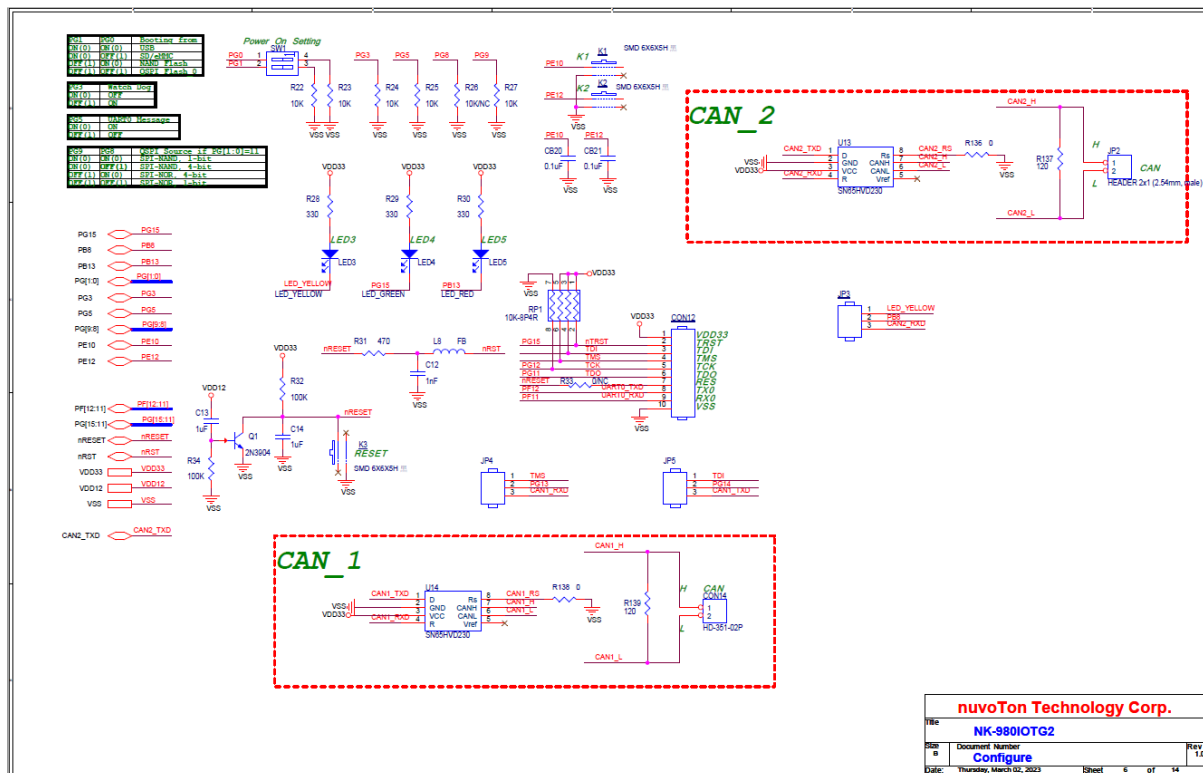


Figure 7-6 Power-on Setting

## 7.7 NUC123ZD4AN0

Figure 7-7 shows the NUC123 VCOM circuit of the NuMaker-IIoT-NUC980G2D board.

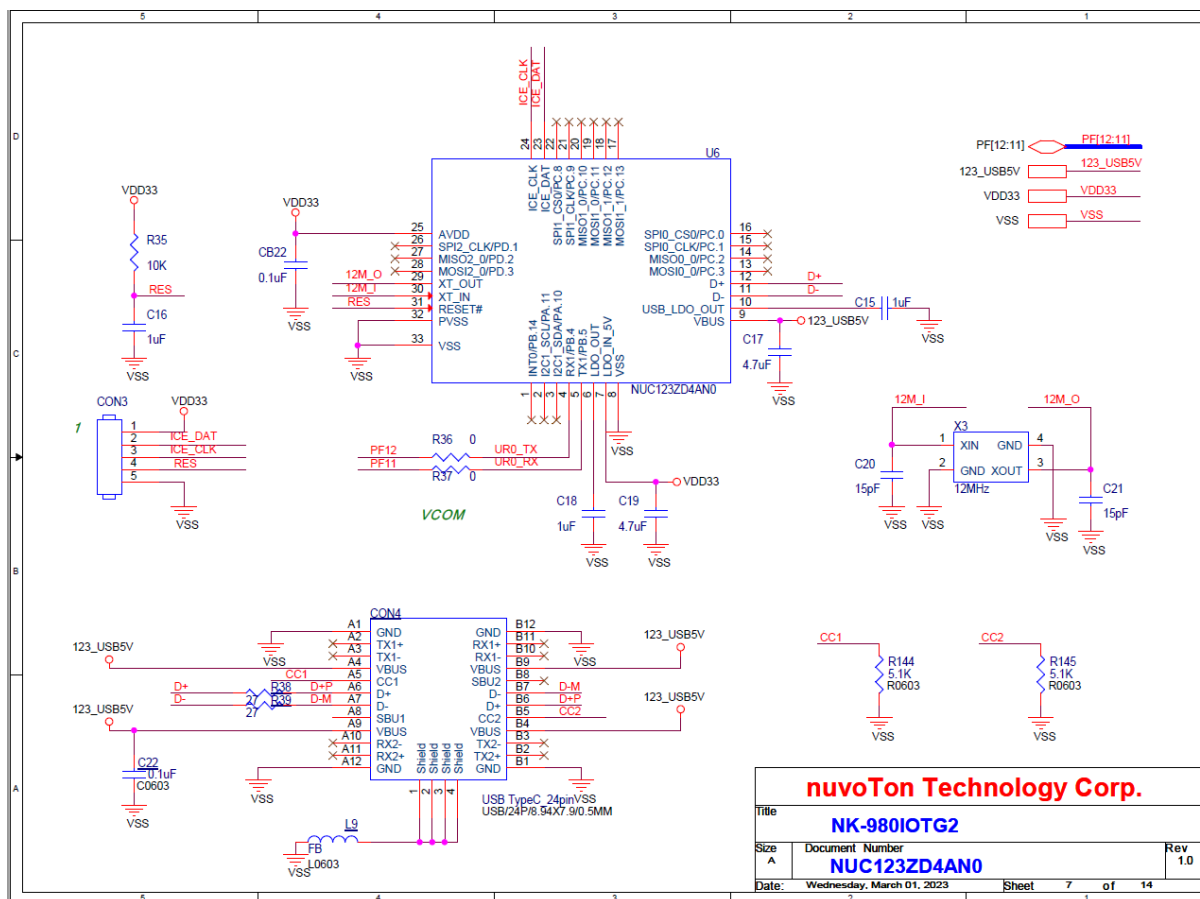


Figure 7-7 NUC123ZD4AN0

## 7.8 Memory

Figure 7-8 shows the QSPI0 (only SPI NAND Flash device mounted) circuit of the NuMaker-IIoT-NUC980G2D board.

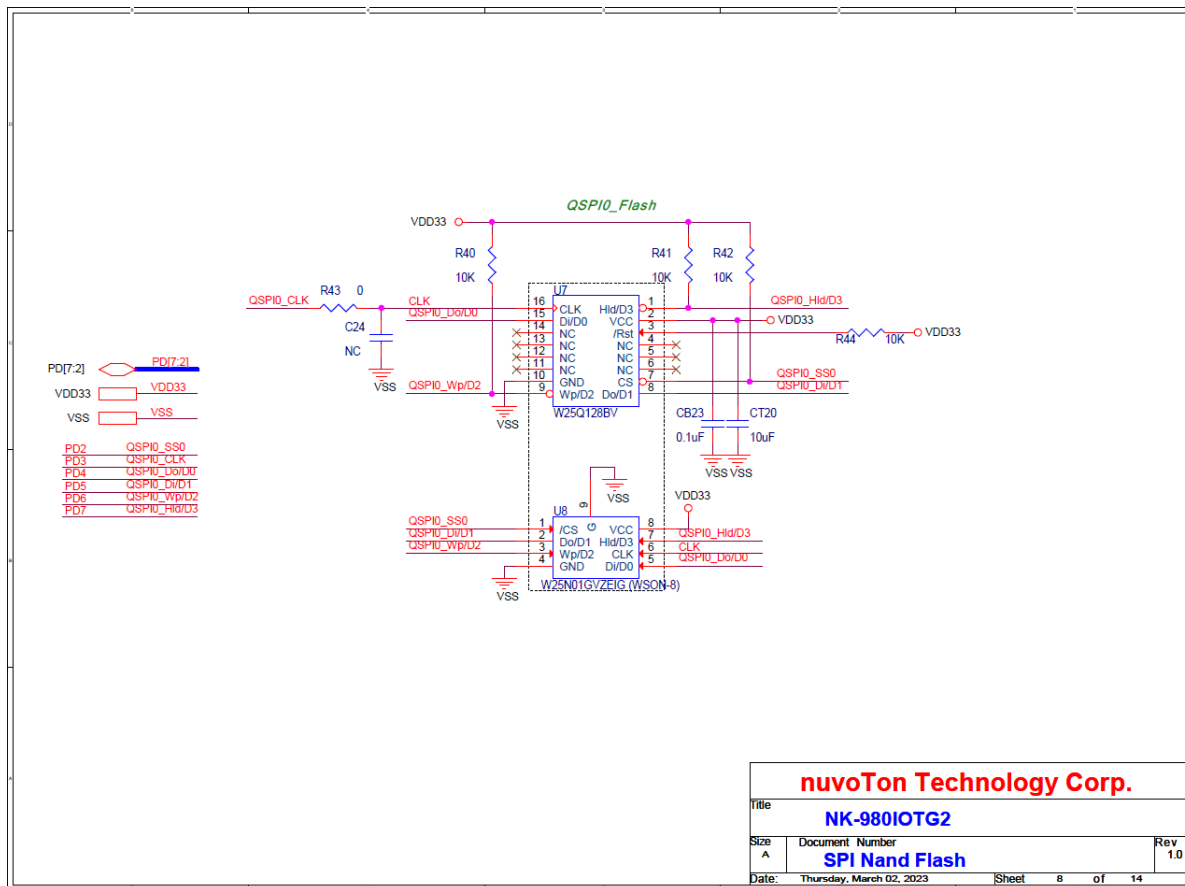


Figure 7-8 Memory

## 7.9 RMII\_PE

Figure 7-9 shows the RMII\_PE circuit of the NuMaker-IIoT-NUC980G2D board.

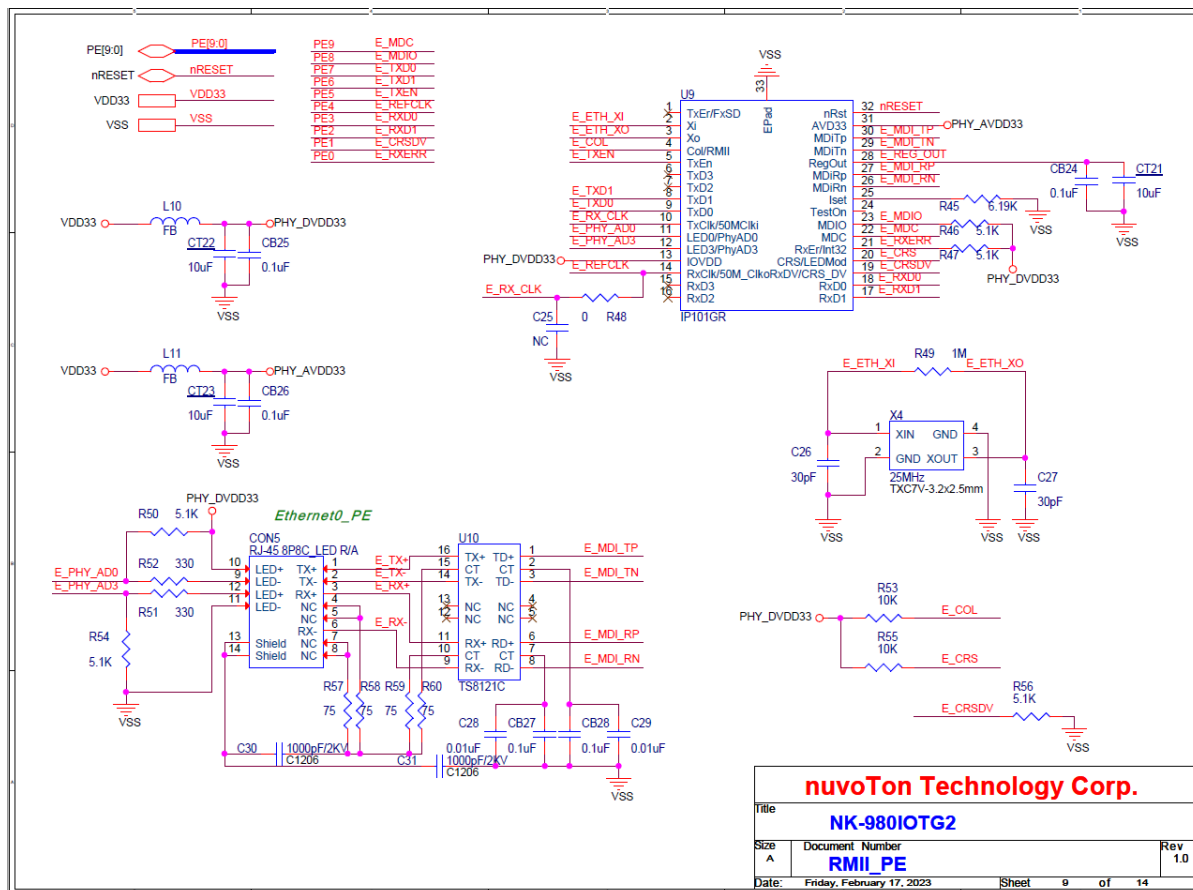


Figure 7-9 RMII\_PE

Figure 7-10 shows the NAU88C22YG Audio Codec and CAN\_3 circuit of the NuMaker-IloT-NUC980G2D board.

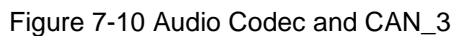


Figure 7-11 shows the SD1 card slot circuit of the NuMaker-IIoT-NUC980G2D board.

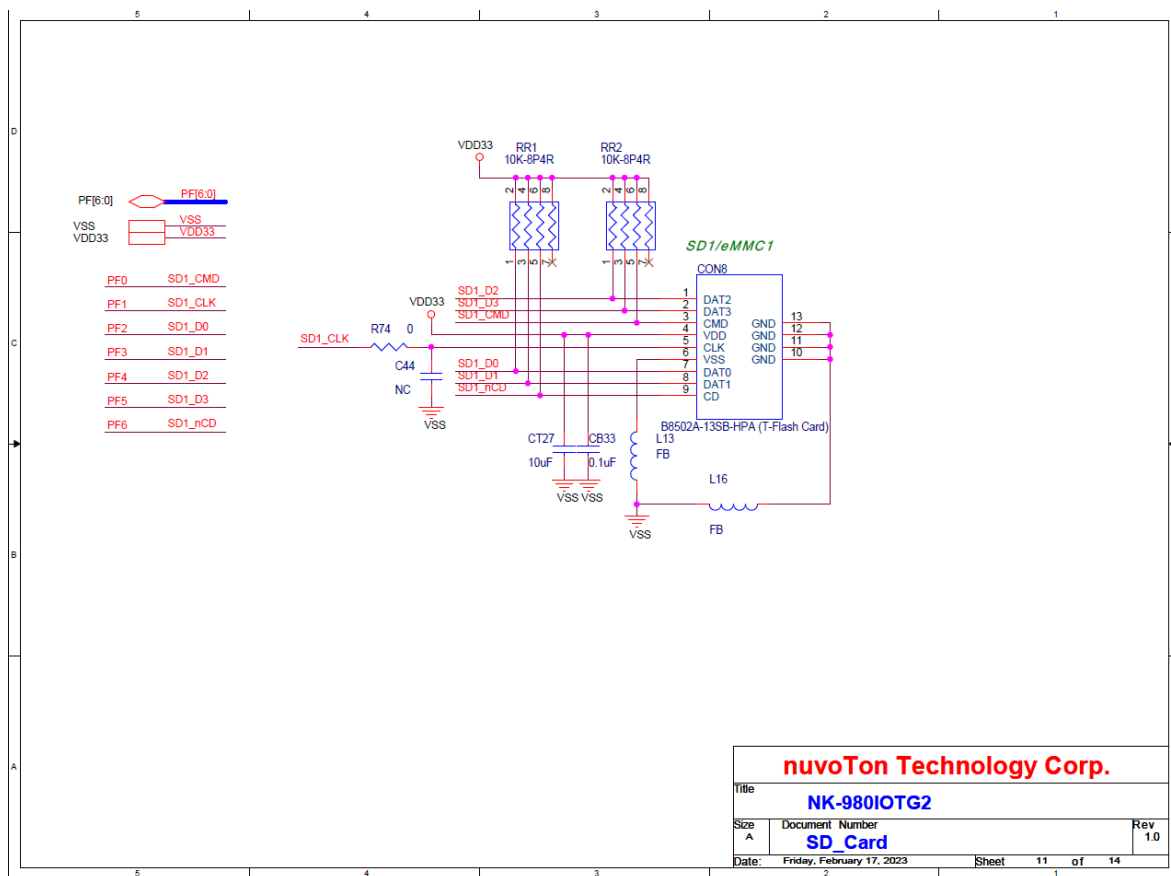


Figure 7-11 SD1/eMMC1



## 7.12 Arduino Uno Interface

Figure 7-12 shows the Arduino Uno interface of the NuMaker-IIoT-NUC980G2D board.

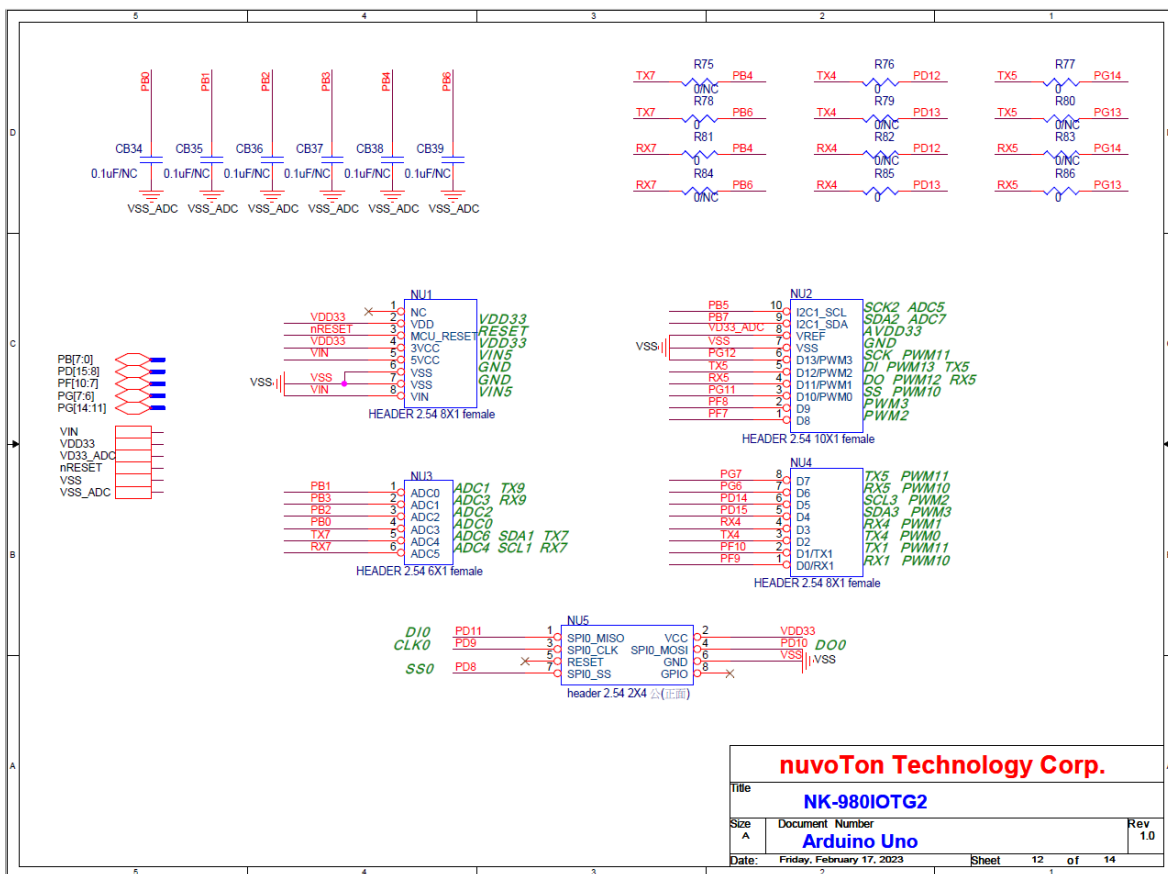


Figure 7-12 Arduino Uno interface

Figure 7-13 shows the USB 0/1 circuit of the NuMaker-IIoT-NUC980G2D board.

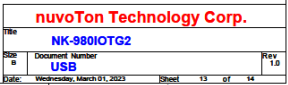


Figure 7-13 USB

Figure 7-14 shows the Expand EBI Interface and CAN\_0 circuit of the NuMaker-IIoT-NUC980G2D board.



Figure 7-14 Expand EBI Interface and CAN\_0

## 7.15 PCB Placement

Figure 7-15 and Figure 7-16 show the front and rear placement of NuMaker-IIoT-NUC980G2D.

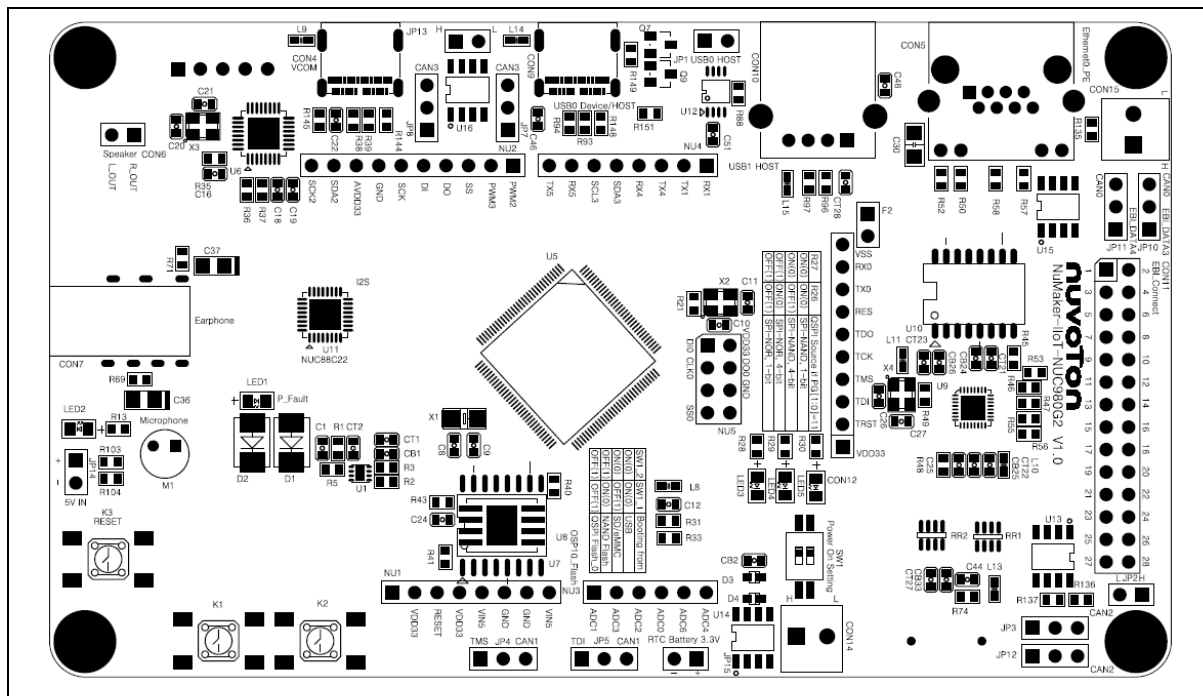


Figure 7-15 Front Placement

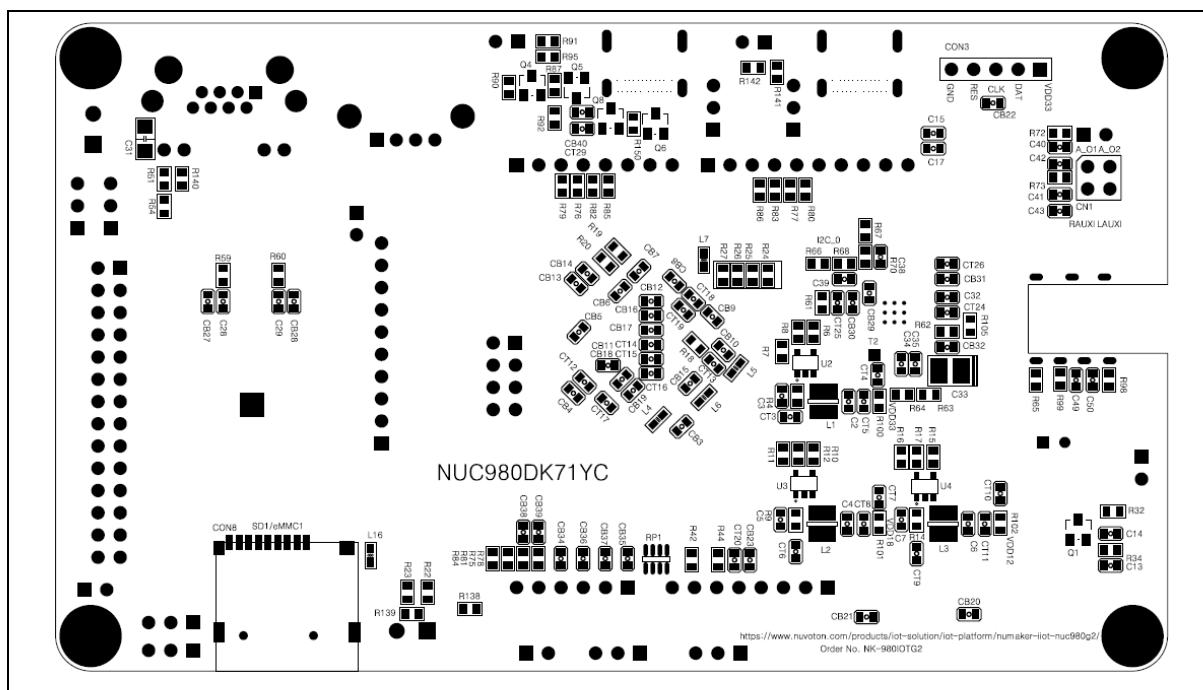


Figure 7-16 Rear Placement

## 8 REVISION HISTORY

Date	Revision	Description
2023.08.22	1.00	Initial version.

### Important Notice

Nuvoton Products are neither intended nor warranted for usage in systems or equipment, any malfunction or failure of which may cause loss of human life, bodily injury or severe property damage. Such applications are deemed, "Insecure Usage".

Insecure usage includes, but is not limited to: equipment for surgical implementation, atomic energy control instruments, airplane or spaceship instruments, the control or operation of dynamic, brake or safety systems designed for vehicular use, traffic signal instruments, all types of safety devices, and other applications intended to support or sustain life.

All Insecure Usage shall be made at customer's risk, and in the event that third parties lay claims to Nuvoton as a result of customer's Insecure Usage, customer shall indemnify the damages and liabilities thus incurred by Nuvoton.

---

*Please note that all data and specifications are subject to change without notice.  
All the trademarks of products and companies mentioned in this datasheet belong to their respective owners.*