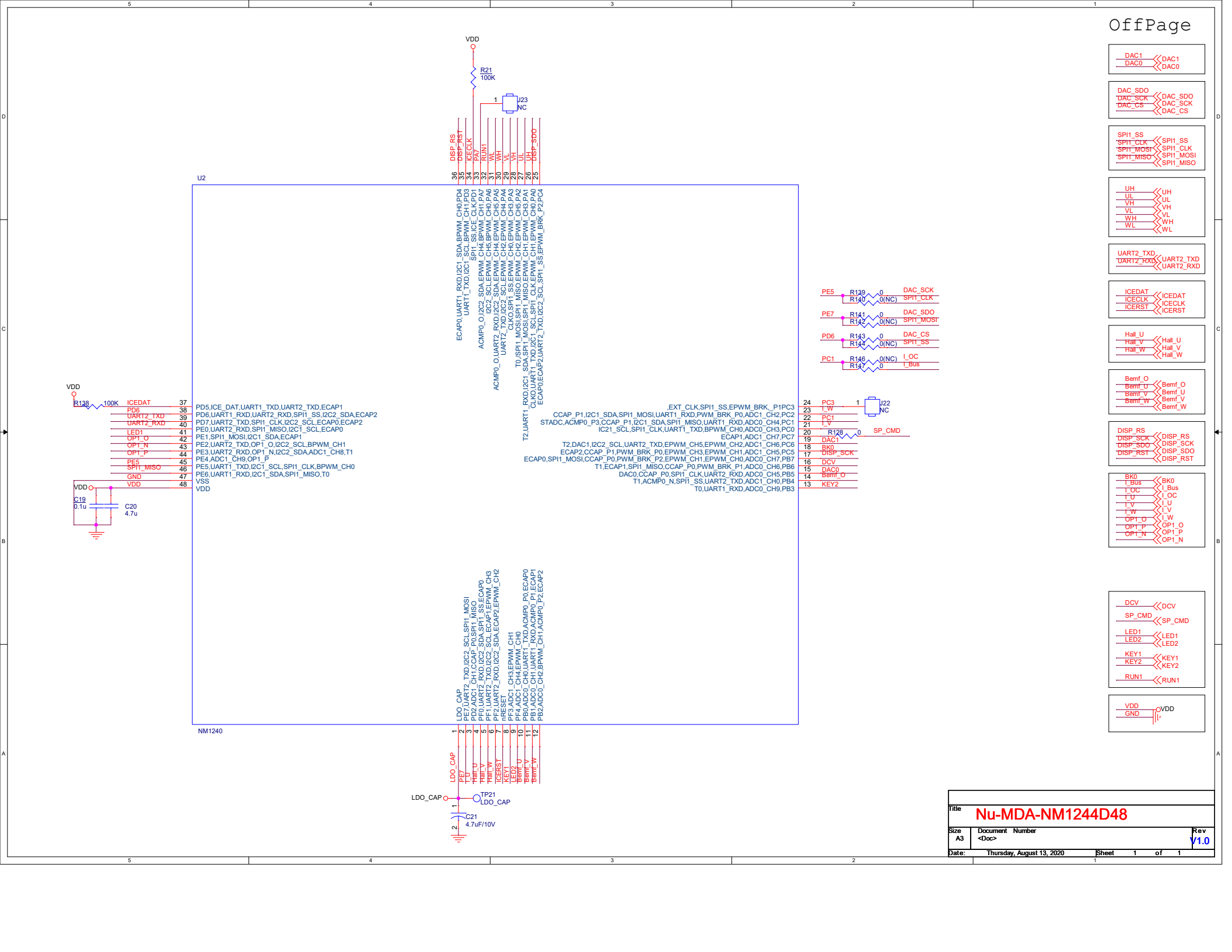
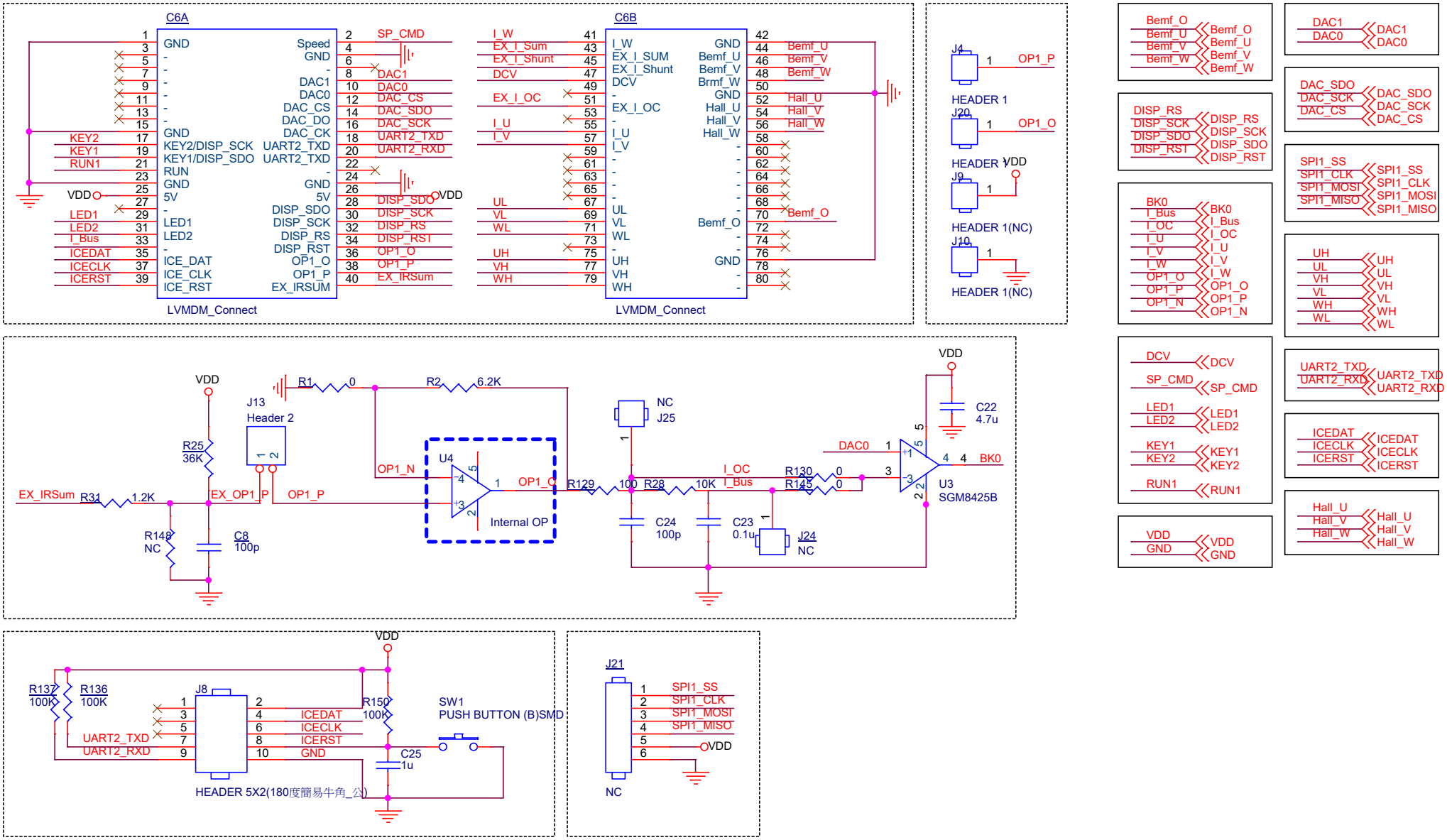

OffPage
VDD
R21
100K
U23
NC
DISP_RS
DISP_RST
ICECLK
PA7
RUN1
WL
WH
VH
VL
UL
UH
DISP_SDO
U2
ECAP0,UART1_RXD,I2C1_SDA,BPWM_CH0,PD4
UART1_TXD,I2C1_SCL,BPWM_CH1,PD3
SPI1_SS,I2C2_SDA,BPWM_CH4,PD7
ACMP0_O,I2C2_SDA,EPWM_CH4,BPWM_CH0,PA6
I2C2_SCL,EPWM_CH5,BPWM_CH0,PA6
ACMP0_O,UART2_RXD,I2C2_SDA,EPWM_CH4,EPWM_CH5,PA5
UART2_TXD,I2C2_SCL,EPWM_CH2,EPWM_CH4,PA4
CLKO,SPI1_SS,EPWM_CH0,EPWM_CH3,PA3
T0,/SPI1_MOSI,SPI1_MISO,EPWM_CH2,EPWM_CH5,PA2
T2,UART1_RXD,I2C1_SDA,SPI1_MOSI,SPI1_MISO,EPWM_CH1,EPWM_CH3,PA1
CLKO,UART1_TXD,I2C1_SCL,SPI1_CLK,EPWM_CH1,EPWM_CH0,PA0
ECAP0,ECAP2,UART2_TXD,I2C2_SCL,SPI1_SS,EPWM_BRK_P2,PC4
PE5
R139
R140
0
0(NC)
DAC_SCK
SPI1_CLK
PE7
R141
R142
DAC_SDO
SPI1_MOSI
PD6
R143
R144
DAC_CS
SPI1_SS
PC1
R146
R147
I_OC
I_Bus
VDD
R138
100K
ICEDAT
PD6
UART2_TXD
UART2_RXD
LED1
OP1_O
OP1_N
OP1_P
PE5
SPI1_MISO
GND
VDD
37
38
39
40
41
42
43
44
45
46
47
48
PD5,ICE_DAT,UART1_TXD,UART2_TXD,ECAP1
PD6,UART1_RXD,UART2_RXD,SPI1_SS,I2C2_SDA,ECAP2
PD7,UART2_TXD,SPI1_CLK,I2C2_SCL,ECAP0,ECAP2
PE0,UART2_RXD,SPI1_MISO,I2C1_SCL,ECAP0
PE1,SPI1_MOSI,I2C1_SDA,ECAP1
PE2,UART2_TXD,OP1_O,I2C2_SCL,BPWM_CH1
PE3,UART2_RXD,OP1_N,I2C2_SDA,ADC1_CH8,T1
PE4,ADC1_CH9,OP1_P
PE5,UART1_TXD,I2C1_SCL,SPI1_CLK,BPWM_CH0
PE6,UART1_RXD,I2C1_SDA,SPI1_MISO,T0
VSS
VDD
VDD
C19
0.1u
C20
4.7u
,EXT_CLK,SPI1_SS,EPWM_BRK_,P1PC3
CCAP_P1,I2C1_SDA,SPI1_MOSI,UART1_RXD,PWM_BRK_P0,ADC1_CH2,PC2
STADC,ACMP0_P3,CCAP_P1,I2C1_SDA,SPI1_MISO,UART1_RXD,ADC0_CH4,PC1
IC21_SCL,SPI1_CLK,UART1_TXD,BPWM_CH0,ADC0_CH3,PC0
ECAP1,ADC1_CH7,PC7
T2,DAC1,I2C2_SCL,UART2_TXD,EPWM_CH5,EPWM_CH2,ADC1_CH6,PC6
ECAP2,CCAP_P1,PWM_BRK_P0,EPWM_CH3,EPWM_CH1,ADC1_CH5,PC5
ECAP0,SPI1_MOSI,CCAP_P0,PWM_BRK_P2,EPWM_CH1,EPWM_CH0,ADC0_CH7,PB7
T1,ECAP1,SPI1_MISO,CCAP_P0,PWM_BRK_P1,ADC0_CH6,PB6
DAC0,CCAP_P0,SPI1_CLK,UART2_RXD,ADC0_CH5,PB5
T1,ACMP0_N,SPI1_SS,UART2_TXD,ADC1_CH0,PB4
T0,UART1_RXD,ADC0_CH9,PB3
24
23
22
21
20
19
18
17
16
15
14
13
PC3
I_W
PC1
I_V
R128
0
SP_CMD
DAC1
BK0
DISP_SCK
DCV
DAC0
Bemf_O
KEY2
U22
NC
LDO_CAP
PE7/UART2_TXD,I2C2_SCL,SPI1_MOSI
PE6/UART2_RXD,I2C2_SDA,SPI1_MISO
PE0,UART2_RXD,I2C2_SDA,SPI1_SS,ECAP0
PF1,UART2_TXD,I2C2_SCL,ECAP1,EPWM_CH3
PF2,UART2_RXD,I2C2_SDA,ECAP2,EPWM_CH2
nRESET
PF3,ADC1_CH3,EPWM_CH1
PF4,ADC1_CH4,EPWM_CH0
PB0,ADC0_CH0,UART1_TXD,ACMP0_P0,ECAP0
PB1,ADC0_CH1,UART1_RXD,ACMP0_P1,ECAP1
PB2,ADC0_CH2,BPWM_CH1,ACMP0_P2,ECAP2
NM1240
1
2
3
4
5
6
7
8
9
10
11
12
LDO_CAP
PE7
I_U
Hall_U
Hall_V
Hall_W
ICERST
KEY2
LED2
Bemf_U
Bemf_V
Bemf_W
LDO_CAP
TP21
LDO_CAP
C21
4.7uF/10V
DAC1
DAC0
DAC_SDO
DAC_SCK
DAC_CS
SPI1_SS
SPI1_CLK
SPI1_MOSI
SPI1_MISO
UH
UL
VH
VL
WH
WL
UART2_TXD
UART2_RXD
ICEDAT
ICECLK
ICERST
Hall_U
Hall_V
Hall_W
Bemf_O
Bemf_U
Bemf_V
Bemf_W
DISP_RS
DISP_SCK
DISP_SDO
DISP_RST
BK0
I_Bus
I_OC
I_U
I_V
I_W
OP1_O
OP1_P
OP1_N
DCV
SP_CMD
LED1
LED2
KEY1
KEY2
RUN1
VDD
GND
Title
Nu-MDA-NM1244D48
Size
A3
Document Number
<Doc>
Rev
V1.0
Date:
Thursday, August 13, 2020
Sheet
1
of
1


C6A
LVMDM_Connect
C6B
LVMDM_Connect
J4
HEADER 1
J20
HEADER 1
J9
HEADER 1(NC)
J10
HEADER 1(NC)
J13
Header 2
U4
Internal OP
U3
SGM8425B
J25
NC
J24
NC
J8
HEADER 5X2(180度簡易牛角_公)
SW1
PUSH BUTTON (B)SMD
J21
NC
Nu-MDA-NM1244D48_Connect
Date: Tuesday, June 03, 2025