



Title	Nu-Link-Me		
Size	A4	Document Number	1
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Rev	3.0		

PIN37 37 PC5,ADC1_CH5,PWM_BRK_P0,CCAP_P1
 PIN38 38 PC6,ADC1_CH6,DAC1,T2,I2C2_SCL,UART2_TxD
 PIN39 39 PC7,ADC1_CH7,T3,ECAP1,QEI_B
 PIN40 40 PE0,OP0_N
 PIN41 41 PE1,OP0_N
 PIN42 42 PE2,OP1_N
 PIN43 43 PE3,OP1_P
 PIN44 44 PE4,OP1_N
 PIN45 45 PE5,OP2_O,I2C1_SCL,SP1_CLK,UART1_TxD
 PIN46 46 PE6,OP2_I,I2C1_SDA,SP1_MISO,UART1_RXD
 PIN47 47 PE7,OP2_N,I2C2_SCL,SP1_MOSI,UART2_TxD
 TICECLK PIN48 48 PD1,ICE_CLK,ACMP1_P2,I2C0_SCL,SP1_SS,UART0_TxD

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 TICEDAT PIN1 1 PD2,ICE_DAT,ADC1_CH1,CCAP_P0,I2C_SDA,SP10_SS,UART0_RXD
 PIN2 2 PD3,PWM,CH1,UART1_TxD
 PIN3 3 PD4,PWM,CH0,UART1_RXD,ECAP0_QEI_A
 PIN4 4 PD5,UART0_RXD,ECAP1_QEI_B
 PIN5 5 PD6,UART0_RXD,ECAP2_IDX
 PIN6 6 PD7,I2C0_TxD,ECAP2_IDX
 PIN7 7 PD8,I2C1_SDA,UART2_TxD,ECAP2_IDX
 FA0_EPWM_Chip1,I2C1_SDA,SP1_SS,SP1_CLK,UART1_TxD,CLK0
 FA1_EPWM_Chip1,I2C2_SDA,SP1_MISO,SP1_MOSI,UART1_RXD
 FA2_EPWM_Chip2,I2C2_SDA,SP1_MISO,SP1_MOSI,UART0_RXD
 FA3_EPWM_Chip3,I2C2_SDA,SP1_MISO,SP1_MOSI,UART0_RXD
 FA4_EPWM_Chip4,XTIN
 FA5_EPWM_Chip5,ACMP0_O_XT_OUT

OP0_O,ECAP0_I2C2_SCL,UART1_RXD,ECAP2_IDX,PC4
 ADC1_O,OPA_O,SPI0_CLK,SP1_SS,PC3
 STADC,ADC0_P3,ADC1_CH2,PC2
 ADC1_CH3,EPWM_CH0,PF4
 ADC1_CH4,EPWM_CH0,PF4
 ADC1_CH5,EPWM_CH1,PF3
 ADC1_CH6,EPWM_CH2,PF2
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P2,ADC0_CH7,PF7
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH6,PF6
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH5,PF5
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH4,PF4
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH3,PF3
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH2,PF2
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH1,PF1
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH0,PF0

OP1_O,ECAP1_I2C1_SDA,UART1_RXD,ECAP2_IDX,PC4
 ADC1_O,OPA_O,SPI0_SS,SP1_CLK,PC3
 STADC,ADC0_P3,ADC1_CH2,PC2
 ADC1_CH3,EPWM_CH0,PF4
 ADC1_CH4,EPWM_CH0,PF4
 ADC1_CH5,EPWM_CH1,PF3
 ADC1_CH6,EPWM_CH2,PF2
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P2,ADC0_CH7,PF7
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH6,PF6
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH5,PF5
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH4,PF4
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH3,PF3
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH2,PF2
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH1,PF1
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH0,PF0

QEI_A,ACMP0_P0,ECAP0,ADC0_CH0,PF0
 ADC1_CH4,EPWM_CH0,PF4
 ADC1_CH5,EPWM_CH1,PF3
 IDX,I2C2_SDA,SP1_MISO,UART2_RXD,ECAP2_EPWM_CH2,PF2
 QEI_A,I2C2_SDA,SP1_SS,UART2_RXD,ECAP1_EPWM_CH3,PF1
 QEI_A,I2C2_SDA,SP1_SS,UART2_RXD,ECAP0_EPWM_CH0,PF0

ACMP0_O,I2C0_SDA,SP1_SS,UART0_RXD,EPWM_CH4,PA1
 I2C0_SCL,SP1_MOSI,UART0_TxD,EPWM_CH5,PA6
 nRESET

OP0_O,ECAP0_I2C2_SCL,UART1_RXD,ECAP2_IDX,PC4
 ADC1_O,OPA_O,SPI0_CLK,SP1_SS,PC3
 STADC,ADC0_P3,ADC1_CH2,PC2
 ADC1_CH3,EPWM_CH0,PF4
 ADC1_CH4,EPWM_CH0,PF4
 ADC1_CH5,EPWM_CH1,PF3
 ADC1_CH6,EPWM_CH2,PF2
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P2,ADC0_CH7,PF7
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH6,PF6
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH5,PF5
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH4,PF4
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH3,PF3
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH2,PF2
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH1,PF1
 CCAP_PU,ECAP0_QEI_A,PPWM_BRK_P1,ADC0_CH0,PF0

OP1_O,ECAP1_I2C1_SDA,UART1_RXD,ECAP2_IDX,PC4
 ADC1_O,OPA_O,SPI0_SS,SP1_CLK,PC3
 STADC,ADC0_P3,ADC1_CH2,PC2
 ADC1_CH3,EPWM_CH0,PF4
 ADC1_CH4,EPWM_CH0,PF4
 ADC1_CH5,EPWM_CH1,PF3
 ADC1_CH6,EPWM_CH2,PF2
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P2,ADC0_CH7,PF7
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH6,PF6
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH5,PF5
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH4,PF4
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH3,PF3
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH2,PF2
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH1,PF1
 CCAP_PU,ECAP1_QEI_A,PPWM_BRK_P1,ADC0_CH0,PF0

QEI_A,ACMP0_P0,ECAP0,ADC0_CH0,PF0
 ADC1_CH4,EPWM_CH0,PF4
 ADC1_CH5,EPWM_CH1,PF3
 IDX,I2C2_SDA,SP1_MISO,UART2_RXD,ECAP2_EPWM_CH2,PF2
 QEI_A,I2C2_SDA,SP1_SS,UART2_RXD,ECAP1_EPWM_CH3,PF1
 QEI_A,I2C2_SDA,SP1_SS,UART2_RXD,ECAP0_EPWM_CH0,PF0

ACMP0_O,I2C0_SDA,SP1_SS,UART0_RXD,EPWM_CH4,PA1
 I2C0_SCL,SP1_MOSI,UART0_TxD,EPWM_CH5,PA6
 nRESET

