

Single N-channel MOSFET

KFK9B0463ZL Datasheet

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1. GENERAL DESCRIPTION

Single N-channel MOSFET for automotive.

2. FEATURES

- Drain-source On-state Resistance: RDS(on) typ = $11 \text{ m}\Omega (VGS = 10 \text{ V})$
- CSP (Chip Size Package)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL: Level 1)
- AEC-Q101 Qualified

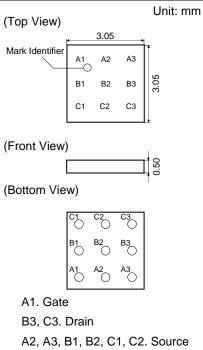
3. MARKING SYMBOL: UH

4. PACKAGING

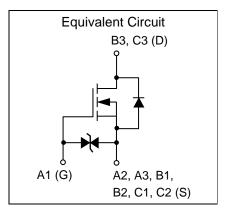
Embossed type (Thermo-compression sealing): 5,000 pcs / reel (standard)

5. ABSOLUTE MAXIMUM RATINGS Ta = $25 \degree$ C

Parameter		Symbol	Rating	Unit
Drain-source Voltage		VDS	40	V
Gate-source Voltage		VGS	+ 20 / - 10	V
	DC *1	ID1	7.5	
Drain Current	DC *2	ID2	10.8	٨
	DC *3	ID3	12.9	A
	Pulsed *4	IDp	86.4	
	DC *1	PD1	0.86	
Total Power Dissipation	DC *2	PD2	1.75	W
	DC *3	PD3	2.50	
Operating Junction and Storage Temperature Range		Tj, Tstg	- 55 to + 150	°C



Package MLGA009-W-3030-FA



6. THERMAL CHARACTERISTICS Ta = 25 °C

Parameter	Symbol	Rating	Unit
	Rth1 *1	145	
Thermal Resistance (ch-a)	Rth2 ^{*2}	72	°C / W
	Rth3 *3	50	

Note *1 Mounted on FR4 board (25.4 mm x 25.4 mm x t1.0 mm).

 $\label{eq:FR4} FR4 \mbox{ board partially covered with copper pad (79.9 \mbox{ mm}^2 \mbox{ area, 36 } \mu m \mbox{ thickness}).$

FR4 board fully covered with copper pad (616 mm² area, 36 μ m thickness).

*3 Mounted on ceramic board (70 mm x 70 mm x t1.0 mm).

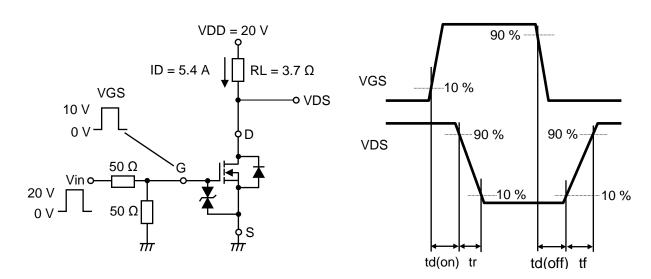
*4 t = 10 $\mu s,~Duty~Cycle \leq$ 1 %.

7. ELECTRICAL CHARACTERISTICS Ta = $25 \degree C \pm 3 \degree C$

Parameter	Symbol	Conditions	Min	Тур	Мах	Unit	
Drain-source Breakdown Voltage	VDSS	ID = 1 mA, VGS = 0 V	40			V	
Zero Gate Voltage Drain Current	IDSS	VDS = 40 V, VGS = 0 V			1	μA	
	IGSS	VGS = + 16 V, VDS = 0 V			10		
Gate-source Leakage Current	1622	VGS = - 8 V, VDS = 0 V			- 10	μA	
Gate-source Threshold Voltage	Vth	ID = 0.99 mA, VDS = 10 V	1.2		3.0	V	
Drain-source On-state Resistance	RDS(on)1	ID = 2 A, VGS = 10 V	7	11	15		
Drain-source On-state Resistance	RDS(on)2	ID = 2 A, VGS = 4.5 V	10	16	27	mΩ	
Body Diode Forward Voltage	VF(s-d)	IF = 2 A, VGS = 0 V		0.7	1.0	V	
Input Capacitance ^{*1}	Ciss			5000			
Output Capacitance *1	Coss	VDS = 20 V, VGS = 0 V f = 1 MHz		500		pF	
Reverse Transfer Capacitance *1	Crss			200			
Turn-on Delay Time *1, *2	td(on)	VDD = 20 V, VGS = 0 to 10 V		32			
Rise Time ^{*1, *2}	tr	ID = 5.4 A		70			
Turn-off Delay Time *1, *2	td(off)	VDD = 20 V, VGS = 10 to 0 V		320		ns	
Fall Time *1, *2	tf	ID = 5.4 A		120			
	0.51	VDD = 20 V, VGS = 4.5 V		30			
Total Gate Charge ^{*1}	Qg1	ID = 10.8 A	30				
	Qg2			70		nC	
Gate-source Charge *1	Qgs	VDD = 20 V, VGS = 10 V ID = 10.8 A		18			
Gate-drain Charge *1	Qgd	ID = 10.6 A		10			

Note Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors. *1 Guaranteed by design, not subject to production testing.

*2 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time.

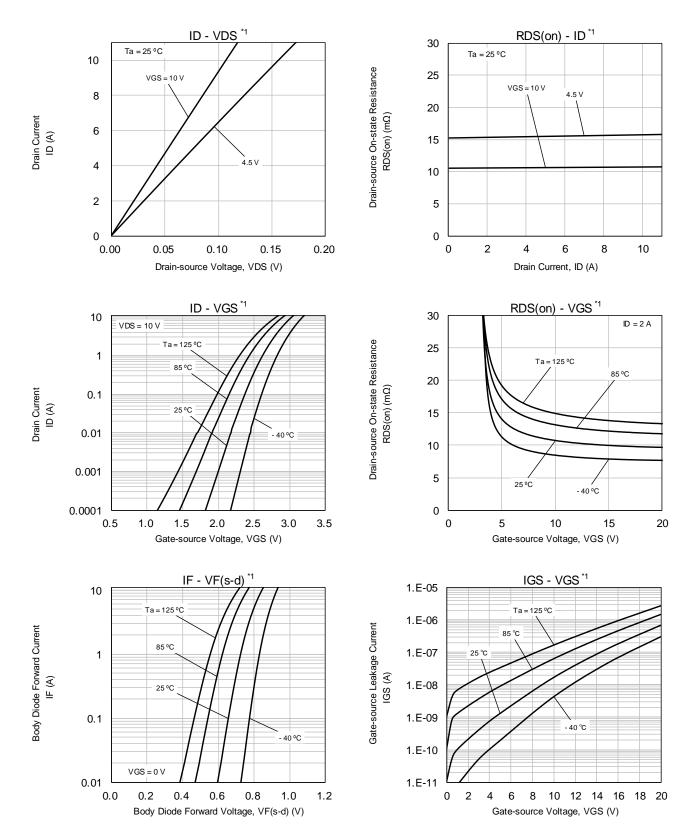


8. ELECTROSTATIC DISCHARGE CHARACTERISTIC Ta = 25 °C ± 3 °C

Standard	Test Type	Symbol	Conditions	Class	Value	Unit
AEC-Q101-001	Human Body Model	HBM	$C = 100 \text{ pF}, R = 1.5 \text{ k}\Omega$	H2	> 2 to \leq 4	kV

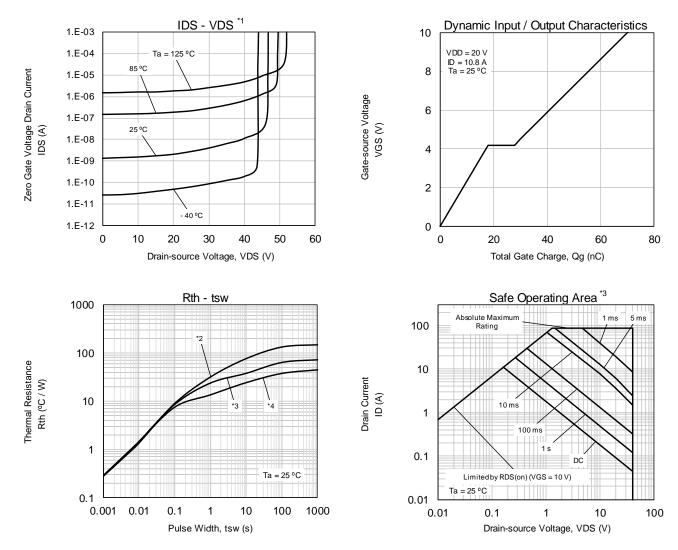
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9. TECHNICAL DATA (Reference)



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TECHNICAL DATA (Reference)



Note

*1 Pulse measurement.

*2 Mounted on FR4 board (25.4 mm × 25.4 mm × t1.0 mm).

FR4 board partially covered with copper pad (79.9 mm² area, 36 µm thickness).
*3 Mounted on FR4 board (25.4 mm × 25.4 mm × t1.0 mm).

FR4 board fully covered with copper pad (616 \mbox{mm}^2 area, 36 $\mbox{\mu}\mbox{m}$ thickness).

*4 Mounted on ceramic board (70 mm \times 70 mm \times t1.0 mm).

Unit: mm

10. OUTLINE

(Top View)

(Front View)

(Bottom View)

Mark Identifier

3.05±0.03

11. LAND & STENCIL PATTERN (Reference)

1.05

(0.475) 1.05

Unit: mm

Important notice:

Solder Mask Defined (SMD) pattern is strongly recommended for pad design. Please check the information in the Nuvoton WL-CSP Application Notes about mounting process.

12. REVISION HISTORY

Date	Revision	Description
2021.4.26	1.00	1. Initially issued.
2021.9.29	2.00	 Changed document name from Product Standards to Datasheet.
		2. Changed ID1, ID2, ID3, IDp, RDS(on)1, RDS(on)2 rating.
		3. Added electrostatic discharge
		4. Added important notice in Land Pattern.
		5. Added special attention and precautions notes.

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