

Single P-channel MOSFET

KFJ9B0466ZL Datasheet

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1. GENERAL DESCRIPTION

Single P-channel MOSFET for automotive.

2. FEATURES

- Drain-source On-state Resistance: RDS(on) typ = 27 m Ω (VGS = -10 V)
- CSP (Chip Size Package)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL: Level 1)
- AEC-Q101 Qualified

3. MARKING SYMBOL: UK

4. PACKAGING

Embossed type (Thermo-compression sealing): 5,000 pcs / reel (standard)

5. ABSOLUTE MAXIMUM RATINGS Ta = 25 °C

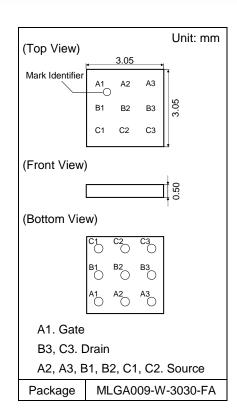
Parameter	Symbol	Rating	Unit		
Drain-source Voltage		VDS	- 40	V	
Gate-source Voltage		VGS	- 20 / + 10	V	
	DC *1	ID1	- 4.9	А	
Drain Current	DC *2	ID2	- 7.0		
	DC *3	ID3	- 8.4		
	Pulsed*4	IDp	- 56.0		
	DC *1	PD1	0.86		
Total Power Dissipation	DC *2	PD2	1.75	W	
	DC *3	PD3	2.50		
Operating Junction and Storage Temperature Range		Tj, Tstg	- 55 to + 150	°C	

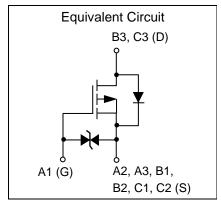
6. THERMAL CHARACTERISTICS Ta = 25 °C

Parameter	Symbol	Rating	Unit
	Rth1 *1	145	°C/W
Thermal Resistance (ch-a)	Rth2 *2	72	
	Rth3 *3	50	

Note *1 Mounted on FR4 board (25.4 mm x 25.4 mm x t1.0 mm). FR4 board partially covered with copper pad (79.9 mm² area, 36 µm thickness).

- *2 Mounted on FR4 board (25.4 mm x 25.4 mm x t1.0 mm). FR4 board fully covered with copper pad (616 mm² area, 36 µm thickness).
- *3 Mounted on ceramic board (70 mm x 70 mm x t1.0 mm).
- *4 $t = 10 \mu s$, Duty Cycle $\leq 1 \%$.





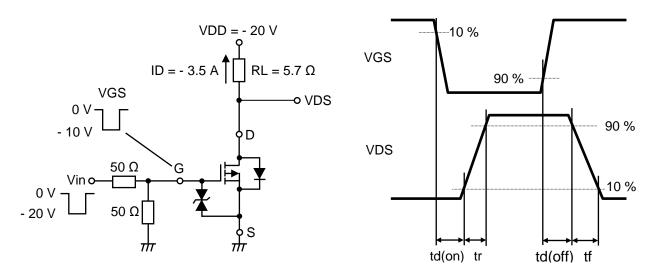


7. ELECTRICAL CHARACTERISTICS Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
Drain-source Breakdown Voltage	VDSS	ID = - 1 mA, VGS = 0 V	- 40			V	
Zero Gate Voltage Drain Current	IDSS	VDS = - 40 V, VGS = 0 V			- 1	μA	
Cata agurag Logkaga Current	IGSS	VGS = - 16 V, VDS = 0 V			- 10	μA	
Gate-source Leakage Current		VGS = + 8 V, VDS = 0 V			10		
Gate-source Threshold Voltage	Vth	ID = - 16.8 mA, VDS = - 10 V	- 1	- 2	- 3	V	
Drain agures On state Desistance	RDS(on)1	ID = - 2 A, VGS = - 10 V	19	27	35	0	
Drain-source On-state Resistance	RDS(on)2	ID = - 2 A, VGS = - 4.5 V	21	30	49	mΩ	
Body Diode Forward Voltage	VF(s-d)	IF = - 2 A, VGS = 0 V		- 0.8	- 1.0	V	
Input Capacitance *1	Ciss	VDC 20 V VCC 0 V		7500			
Output Capacitance *1	Coss	VDS = - 20 V, VGS = 0 V f = 1 MHz		500		pF	
Reverse Transfer Capacitance *1	Crss	I = I IVIMZ		450			
Turn-on Delay Time *1, *2	td(on)	VDD = - 20 V, VGS = 0 to - 10 V		40			
Rise Time *1, *2	tr	ID = - 3.5 A VDD = - 20 V, VGS = - 10 to 0 V		70		ns	
Turn-off Delay Time *1, *2	td(off)			580			
Fall Time *1, *2	tf	ID = - 3.5 A		200			
Total Gate Charge *1	Qg1	VDD = - 20 V, VGS = - 4.5 V	70				
		ID = - 7 A		70			
	Qg2	VDD - 20 V VCS - 10 V		140		nC	
Gate-source Charge *1	Qgs	VDD = - 20 V, VGS = - 10 V		20			
Gate-drain Charge *1	Qgd	ID = - I A		26			

Note Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

- *1 Guaranteed by design, not subject to production testing.
- *2 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time.

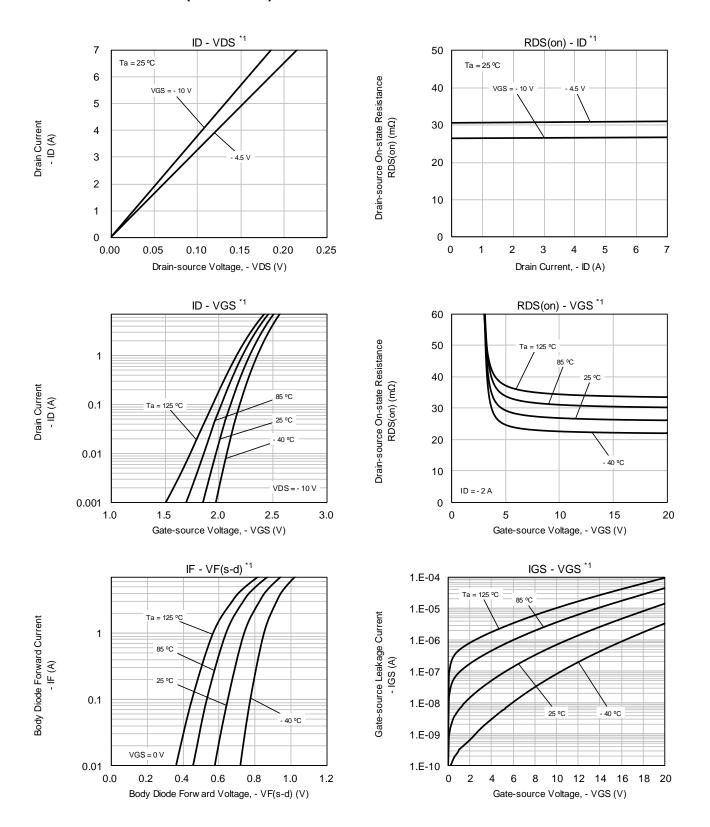


8. ELECTROSTATIC DISCHARGE CHARACTERISTIC Ta = 25 °C ± 3 °C

Standard	Test Type	Symbol	Conditions	Class	Value	Unit
AEC-Q101-001	Human Body Model	HBM	$C = 100 \text{ pF}, R = 1.5 \text{ k}\Omega$	НЗА	> 4 to ≤ 8	kV

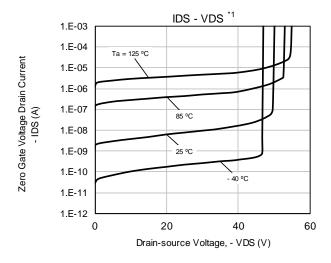


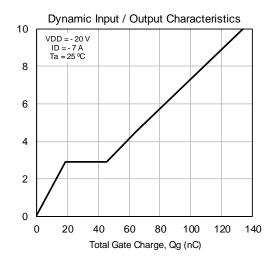
9. TECHNICAL DATA (Reference)





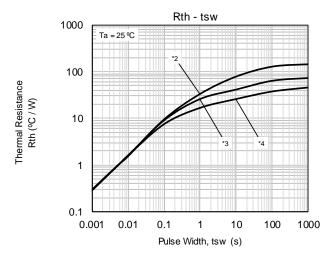
TECHNICAL DATA (Reference)

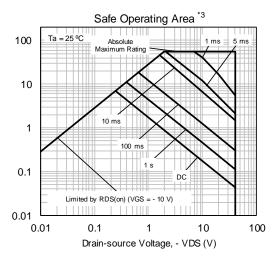




Gate-source Voltage -VGS (V)

Drain Current - ID (A)





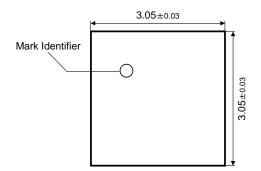
Note

- *1 Pulse measurement.
- *2 Mounted on FR4 board (25.4 mm x 25.4 mm x t1.0 mm). FR4 board partially covered with copper pad (79.9 mm² area, 36 μm thickness).
- *3 Mounted on FR4 board (25.4 mm x 25.4 mm x t1.0 mm). FR4 board fully covered with copper pad (616 mm² area, 36 μm thickness).
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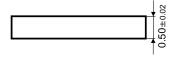


10. OUTLINE

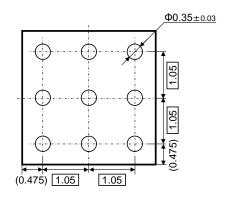
(Top View) Unit: mm



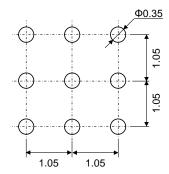
(Front View)



(Bottom View)



11. LAND & STENCIL PATTERN (Reference)



Unit: mm

Important notice:

Solder Mask Defined (SMD) pattern is strongly recommended for pad design.

Please check the information in the Nuvoton WL-CSP Application Notes about mounting process.



12. REVISION HISTORY

Date	Revision	Description
2021.4.16	1.00	1. Initially issued.
2021.11.15	1.01	Changed document name from Product Standards to Datasheet.
		2. Added electrostatic discharge
		3. Added important notice in Land Pattern.
		4. Added special attention and precautions notes.



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