SWLNA for 2.4GHz Band Applications

KA29223K Datasheet

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Support for industry standards and quality standards

Functional safety standards for automobiles ISO26262	No
AECQ-100	No
Market failure rate	50Fit

Disclaimer

- 1. When the application system is designed using this IC, please design the system at your own risk. Please read, consider, and apply appropriate usage notes and description in this standard.
- When designing your application system, please take into the consideration of break down and failure mode occurrence and possibility in semiconductor products. Measures on the systems such as, but not limited to, redundant design, mitigating the spread of fire, or preventing glitch, are recommended in order to prevent physical injury, fire, social damages, etc. in using the Nuvoton Technology Japan Corporation (hereinafter referred to as NTCJ) products.
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- 4. Please use this IC in compliance with all applicable laws, regulations and safety-related requirements that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. NTCJ shall not be held responsible for any damage incurred as a result of this IC being used not in compliance with the applicable laws, regulations and safety-related requirements.
- 5. This IC does not have any security functions using cryptographic algorithms, such as authentication, encryption, tampering detection.
- 6. Unless this IC is indicated by NTCJ to be used in applications as meeting the requirements of a particular industry standard (e.g., ISO 9001, IATF 16949, ISO 26262, etc.), this IC is neither designed nor intended for use in such environments for that applications. NTCJ shall not be held responsible for not meeting the requirements of a particular industry standard.
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KA29223K

SWLNA for 2.4 GHz Band Applications

Overview

- —KA29223K is a LNA (Low Noise Amplifier)-IC integrated SW(SP3T) for 2.4GHz Band applications .
- —Realizing high performance by using 0.3 µm CMOS process.
- —TX mode , BT mode , RX mode / High Gain , RX mode / Low Gain are controlled by integrated CMOS logic circuit .
- —Achieving miniaturization by using small size Chip Size Package with solder bump .

■ Features

—Low voltage operation	+3.3 V typ.
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Low current consumption
 11.5 mA typ. (RX mode / High Gain)
 100 uA typ. (RX mode / Low Gain)
 80 uA typ. (TX mode , BT mode)

25 uA typ. (ALL Off mode)

—High gain—Low noise figure12.5 dB typ. (RX mode/High Gain)1.7 dB typ. (RX mode/High Gain)

—Low Insertion Loss 0.55dB typ. (BT mode) 0.60dB typ. (TX mode)

Applications

-Wireless LAN / Bluetooth

■ Package

— 11Pin Chip Size Package with solder bump Size : $0.711 \text{ mm} \times 0.923 \text{ mm} \times 0.3 \text{ mm}$

■ Type

-CMOS IC



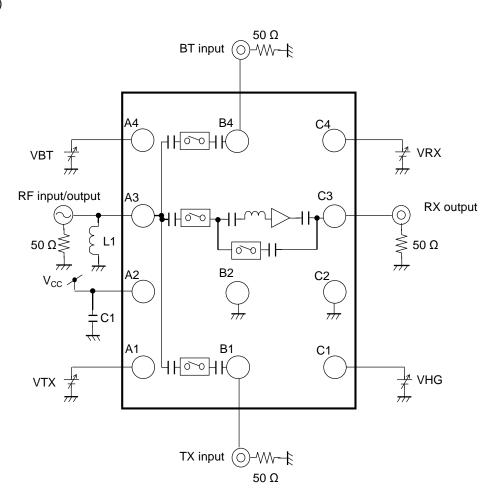
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■ Application Circuit Example (Block Diagram)

(Top View)



• External Components

Components	Size	Value	Part Number	Vendor
L1	L1 0603 2.5 nH		LQP03T2N5B04	Murata
C1	0603	0.1 uF	GRM033B30J104KE18	Murata

Notes)

- —This application circuit is an example. The operation of mass production set is not guaranteed. You should perform enough evaluation and verification on the design of mass production set. You are fully responsible for the incorporation of the above application circuit and information in the design of your equipment.
- —This block diagram is for explaining functions. The part of the block diagram may be omitted, or it may be simplified.

■ Pin Descriptions

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Pin No.	Pin name	Туре	Description
A1	VTX	Input	TX mode SW
A2	VCC	Power Supply	V _{cc}
А3	RFC	Input/Output	RF input/output
A4	VBT	Input	BT mode SW
B1	TX	Input	TX input
B2	GND	Ground	GND
B4	вт	Input	BT input
C1	VHG	Input	High Gain/Low Gain SW
C2	GND	Ground	GND
C3	RX	Output	RX output
C4	VRX	Input	RX mode SW



■ Absolute Maximum Ratings

Note) Absolute maximum ratings are limit values which do not result in damages to this IC, and IC operation is not guaranteed at these limit values.

A No.	Parameter	Symbol	Rating	Unit	Notes
1	Supply voltage	V _{CC}	3.7	V	*1
2	Supply current	I _{cc}	20	mA	_
3	Operating ambient temperature	T _{opr}	-40 to +85	°C	*2
4	Storage temperature	T _{stg}	-55 to +150	°C	*2

Notes) *1 : The values under the condition not exceeding the above absolute maximum ratings.

*2 : Except for the operating ambient temperature and storage temperature, all ratings are for $T_a = 25$ °C.

■ Operating supply voltage range

Parameter	Symbol	Range	Unit	Notes
Supply voltage range	V _{CC}	3.0 to 3.6	V	*

Note) *: The values under the condition not exceeding the above absolute maximum ratings.



■ Allowable Current and Voltage Range

Notes) — Allowable current and voltage ranges are limit ranges which do not result in damages to this IC, and IC operation is not guaranteed within these limit ranges.

- Voltage values, unless otherwise specified, are with respect to GND.
- Do not apply external currents or voltages to any pin not specifically mentioned.

Pin No.	Pin name	Range	Unit	Notes
A1	VTX	-0.3 to (Vcc + 0.3)	V	*4
A3	RFC	_	V	*1
A4	VBT	-0.3 to (Vcc + 0.3)	V	*4
B1	TX	_	V	*2
B4	ВТ	_	V	*2
C1	VHG	-0.3 to (Vcc + 0.3)	V	*4
СЗ	RX	_	V	*3
C4	VRX	-0.3 to (Vcc + 0.3)	V	*4

Note) *1 : RF signal input / output pin.

Maximum input / output power at TX mode and BT mode is 27 dBm.

Maximum input power at RX mode / High Gain is 0 dBm.

Maximum input power at RX mode / Low Gain is 10 dBm.

*2 :RF signal input pin.

Maximum input power at TX mode and BT mode is 27 dBm.

*3 : RF signal output pin.

*4: (VCC + 0.3) V must not be exceeded 3.7 V



■ Electrical Characteristics at V_{CC} = 3.3 V

Note) $T_a = 25^{\circ}C \pm 2^{\circ}C$ unless otherwise specified.

B No. Parameter	Demonstra		Test	0 11.1	Limits				Nata
	Parameter	Symbol	Circuit	Conditions	Min	Тур	Max	Unit	Note
DC elec	trical characteristics					-			
DC-1	Supply current RX mode / High Gain	I _{CC} RXHS	1	V _{CC} current at RX mode / High Gain No input signal		11.5	15	mA	_
DC-2	Supply current RX mode / Low Gain	I _{CC} RXLS	1	V _{CC} current at RX mode / Low Gain No input signal	_	100	150	uA	
DC-3	Supply current TX mode	I _{CC} TXS	1	V _{CC} current at TX mode No input signal	_	80	130	uA	_
DC-4	Supply current BT mode	I _{CC} BTS	1	V _{CC} current at BT mode No input signal	_	80	130	uA	_
DC-5	SW current (High Voltage)	IIHS	1	Current at VRX,VTX,VBT,VHG pin VIH = V _{CC}	_	13	26	uA	_
DC-6	SW Input Voltage (High Level)	VIHS	1	_	1.6	3.3	_	٧	_
DC-7	SW Input Voltage (Low Level)	VILS	1	_	_	0	0.3	V	_
DC-8	Supply current ALL Off mode	IccOFFS	1	V _{CC} current at ALL Off mode No input signal	_	25	50	uA	_



■ Electrical Characteristics (continued) at $V_{CC}=3.3~V$ Note) $T_a=25^{\circ}C\pm2^{\circ}C$, fRX = 2.442 GHz, PRX = -10 dBm, CW unless otherwise specified.

B No.	Parameter	Symbol	Test Conditions	Limits			Unit	Note		
D NO.	1 arameter	Symbol	Circuit	Conditions	Min	Тур	Max	Offic	Note	
AC ele	ctrical characteristics	RX Mode / High (Gain (VR	XX=3.3V, VHG=3.3V, V	ΓX=0V,	VBT=0V	')			
A-1	Power Gain	GRXHS	1	RFC to RX	10.0	12.5	15.0	dB	_	
AC ele	ctrical characteristics	RX Mode / Low G	ain (VR	X=3.3V, VHG=0V, VTX	=0V, VE	BT=0V)				
B-1	Power Gain	GRXLS	1	RFC to RX	-10.0	-8.0	-6.0	dB		
AC ele	ctrical characteristics	ΓX Mode (VRX=0	V, VHG	=0V, VTX=3.3V, VBT=0	OV)					
C-1	Insertion Loss	ILTXS	1	RFC to TX	_	0.60	0.90	dB	_	
AC ele	AC electrical characteristics BT Mode (VRX=0V, VHG=0V, VTX=0V, VBT=3.3V)									
D-1	Insertion Loss	ILBTS	1	RFC to BT	_	0.55	0.85	dB		



■ Electrical Characteristics (Reference values for design) at V_{CC} = 3.3 V

Note) $T_a = 25^{\circ}\text{C}\pm2^{\circ}\text{C}$, fRX = 2.442 GHz, PRX = -10 dBm, CW unless otherwise specified.

The characteristics listed below are reference values derived from the design of the IC and are not guaranteed by inspection. If a problem does occur related to these characteristics, we will respond in good faith to user concerns.

B No.	Parameter	Symbol	Test	Conditions	Reference values			Unit	Note
B No.	1 diameter	Symbol	Circuit	Conditions	Min	Тур	Max	Offic	Note
AC elec	trical characteristics	RX Mode / High	Gain (VF	RX=3.3V, VHG=3.3V, VT	X=0V, `	VBT=0\	/)		
E-1	Noise Figure	NFRXH	1	RFC to RX	_	1.7	2.2	dB	_
E-2	Input Power 1dB Compression	IP1dBRXH	1	RFC to RX	-11	-6.5	_	dBm	_
E-3	IIP3 +10 MHz offset	IIP3RXH	1	f1 = fRX + 10 MHz f2 = fRX + 20 MHz Input 2 signals (f1, f2)	2	4	_	dBm	_
E-4	Reverse Isolation	ISORXH	1	RX to RFC	23.5	26	_	dB	
E-5	Isolation RFC_BT	ISORXH_1	1	RFC to BT	14	17	_	dB	_
E-6	Isolation RFC_TX	ISORXH_2	1	RFC to TX	23.5	26.5	_	dB	
E-7	Isolation BT_RFC	ISORXH_3	1	BT to RFC	19	22	_	dB	
E-8	Isolation BT_RX	ISORXH_4	1	BT to RX	15	19	_	dB	_
E-9	Isolation BT_TX	ISORXH_5	1	BT to TX	27	30	_	dB	_
E-10	Isolation RX_BT	ISORXH_6	1	RX to BT	25	28	_	dB	_
E-11	Isolation RX_TX	ISORXH_7	1	RX to TX	29	33	_	dB	_
E-12	Isolation TX_RFC	ISORXH_8	1	TX to RFC	27	29.5	_	dB	_
E-13	Isolation TX_BT	ISORXH_9	1	TX to BT	26	29	_	dB	_
E-14	Isolation TX_RX	ISORXH_10	1	TX to RX	18	23	_	dB	_



 \blacksquare Electrical Characteristics (Reference values for design, continued) at $V_{CC} = 3.3 \text{ V}$

Note) $T_a = 25$ °C ± 2 °C, fRX = 2.442 GHz, PRX = -10 dBm, CW unless otherwise specified.

The characteristics listed below are reference values derived from the design of the IC and are not guaranteed by inspection.

If a problem does occur related to these characteristics, we will respond in good faith to user concerns.

B No.	Dorometer	Ourselval	Test	Reference values			l lmit	Note	
	Parameter	Symbol	Circuit	Conditions	Min	Тур	Max	- Unit	Note
AC elec	trical characteristics	RX Mode / Low	Gain (VF	RX=3.3V, VHG=0V, VTX=	=0V, VB	T=0V)			
F-1	Isolation RFC_BT	ISORXL_1	1	RFC to BT	22	25	_	dB	_
F-2	Isolation RFC_TX	ISORXL_2	1	RFC to TX	27	31	_	dB	_
F-3	Isolation BT_RX	ISORXL_3	1	BT to RX	22.5	24.5	_	dB	_
F-4	Isolation BT_TX	ISORXL_4	1	BT to TX	25	28	_	dB	
F-5	Isolation RX-TX	ISORXL_5	1	RX to TX	33.5	36		dB	



\blacksquare Electrical Characteristics (Reference Values for Design , continued) at V $_{\rm CC} = 3.3~{\rm V}$

Note) $T_a = 25^{\circ}C \pm 2^{\circ}C$, fRX = 2.442 GHz, PRX = -10 dBm, CW unless otherwise specified.

The characteristics listed below are reference values derived from the design of the IC and are not guaranteed by inspection.

If a problem does occur related to these characteristics, we will respond in good faith to user concerns.

B No.	Parameter	Symbol	Test Conditions —	Conditions	Reference values		lues	- Unit	Note
D NO.	raiametei	Symbol		Min	Min Typ	Max			
AC ele	ectrical characteristics	TX Mode (VRX:	=0V, VH	G=0V, VTX=3.3V, VBT=0	V)				
G-1	Input Power 0.1dB Compression	IP01dBTX	1	RFC to TX	27	31		dBm	_
G-2	Isolation RFC_BT	ISOTX_1	1	RFC to BT	26	29	_	dB	
G-3	Isolation RFC_RX	ISOTX_2	1	RFC to RX	25	35.5	38	dB	_
G-4	Isolation BT_RX	ISOTX_3	1	BT to RX	26	29		dB	_
G-5	Isolation BT_TX	ISOTX_4	1	BT to TX	30	35	_	dB	_
G-6	Isolation RX_TX	ISOTX_5	1	RX to TX	25	35	38	dB	_
AC ele	ectrical characteristics	BT Mode (VRX:	=0V, VH	G=0V, VTX=0V, VBT=3.3	V)				
H-1	Input Power 0.1dB Compression	IP01dBBT	1	RFC to BT	27	31		dBm	_
H-2	Isolation RFC_RX	ISOBT_1	1	RFC to RX	33	38	_	dB	_
H-3	Isolation RFC_TX	ISOBT_2	1	RFC to TX	28.5	31		dB	
H-4	Isolation BT_RX	ISOBT_3	1	BT to RX	30	34	_	dB	
H-5	Isolation BT_TX	ISOBT_4	1	BT to TX	30	34	_	dB	
H-6	Isolation RX_TX	ISOBT_5	1	RX to TX	35	40	_	dB	_

■ Control Pin Mode Table

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Note)See parameters B No. DC-6/ B No.DC-7 in the Electrical Characteristics for control voltage retention ranges.

Din No	Description	Pin voltage		Domorko
Pin No.	Description	Low	High	Remarks
A1	TX mode SW	_	TX Mode	_
A4	BT mode SW	_	BT Mode	_
C1	High Gain / Low Gain SW	Low Gain	High Gain	_
C4	RX mode SW	_	RX Mode	_

■ Truth Table

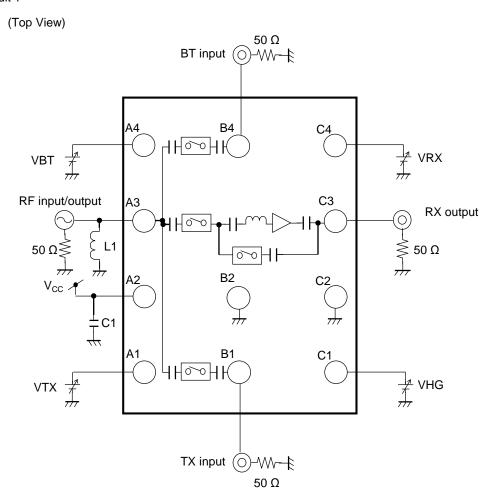
Note)See parameters B No. DC-6/ B No.DC-7 in the Electrical Characteristics for control voltage retention ranges.

VTX	VBT	VHG	VRX	Mode
High	Low	Low	Low	TX mode
Low	High	Low	Low	BT mode
Low	Low	High	High	RX mode / High Gain
Low	Low	Low	High	RX mode / Low Gain
Low	Low	Low	Low	ALL Off mode



■ Test Circuit Diagram

• Test Circuit 1



• External Components

Components	Size	Value	Part Number	Vendor
L1	0603	2.5 nH	LQP03T2N5B04	Murata
C1	0603	0.1 uF	GRM033B30J104KE18	Murata

■ Technical Data

— I/O block circuit diagram and pin function descriptions

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

Pin No.	Waveform and voltage	Internal circuit	Impedance	Description
A1		(A1) — W — GND	300 kΩ	TX mode SW
A2	3.3 V	(A2) GND	3.1 ΜΩ	Vcc
А3	_	V _{CC} A3 GND	_	RF input/output
A4	_	(A4) (GND)	300 kΩ	BT mode SW



■ Technical Data(continued.)

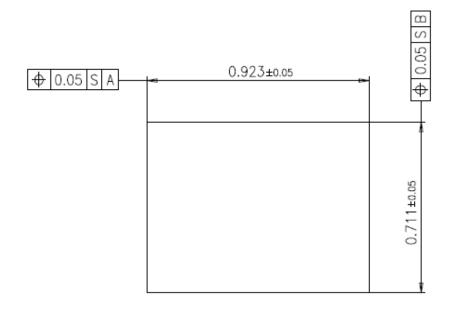
— I/O block circuit diagram and pin function descriptions

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

Pin No.	Waveform and voltage	Internal circuit	Impedance	Description
B1		V _{CC} B1 GND	_	TX input
B2	_	_	_	GND
B4	_	V _{CC} B4 GND	_	BT input

■ PACKAGE INFORMATION (Reference Data)

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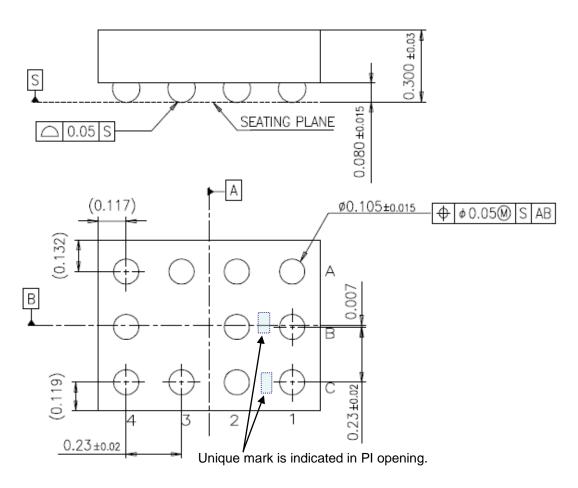


Unit:mm

PIN Location on Bottom View

※ Coordinate origin is defined as 0,0

Pin	Х	Υ
A1	0.345	0.223
A2	0.115	0.223
А3	-0.115	0.223
A4	-0.345	0.223
B1	0.345	-0.007
B2	0.115	-0.007
B4	-0.345	-0.007
C1	0.345	-0.237
C2	0.115	-0.237
C3	-0.115	-0.237
C4	-0.345	-0.237



■ Technical Data(continued.)

— I/O block circuit diagram and pin function descriptions

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

Pin No.	Waveform and voltage	Internal circuit	Impedance	Description
C1	_	©1 — W — GND	420 kW	High Gain mode / Low Gain mode SW
C2	_	_	_	GND
C3	_	V _{CC} GND	_	RX output
C4	_	C4 GND	300 kW	RX mode SW

■ Usage Notes

- Pay attention to the direction of the IC. When mounting it in the wrong direction onto the PCB (printed-circuit-board), it might be damaged.
- Pay attention in the PCB (printed-circuit-board) pattern layout in order to prevent damage due to short circuit between pins.
 - In addition, refer to the Pin Description for the pin configuration.
- Perform visual inspection on the PCB before applying power, otherwise damage might happen due to problems such as solder-bridge between the pins of the IC. Also, perform full technical verification on the assembly quality, because the same damage possibly can happen due to conductive substances, such as solder ball, that adhere to the IC during transportation.
- 4. Take notice in the use of this IC that it might be damaged when an abnormal state occurs such as output pin-VCC short (Power supply fault), output pin-GND short (Ground fault), or output-to-output-pin short (load short). Safety measures such as installation of fuses are recommended because the extent of the above-mentioned damage will depend on the current capability of the power supply.
- 5. Due to the unshielded structure of this IC, functions and characteristics of the IC cannot be guaranteed under the exposure of light. During normal operation or even under testing condition, please ensure that the IC is not exposed to light.
- Please ensure that your design does not have metal shield parts touching the chip surface as the surface potential is GND voltage.
- 7. Pay attention to the breakdown voltage of this IC when using.

Pin No.	Pin Name	ESD Model	Standard	ESD Voltage (*1)
A2	VCC	НВМ	GND	-900 V
A1	VTX	НВМ	VCC	+900 V
A4	VBT	НВМ	VCC	+900 V
B2	GND	НВМ	VCC	+900 V
C1	VHG	НВМ	VCC	+ 900 V
C2	GND	НВМ	VCC	+ 900 V
C4	VRX	НВМ	VCC	+ 900 V

The other pins are more than +/- 1000V.

(*1) This is the max value which is not broken.



Revision History

Date	Revision	Description
2021.2.2	1.00	1. Initially issued.
2021.9.21	1.01	Changed document name from Product Standards to Datasheet.
		Changed important notice on page2
2022.2.1	1.02	2. Remove important notice page from previous version page 18
		3. Added usage notes on page19,20

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