

nuvoTon

ISD15100

ISD3900

ISD15C00

Errata

Version 0.1

This document describes the errata for the ISD15100/3900/15C00 series devices including available workarounds. This document should be used in conjunction with the design guide to fully describe device functionality. Please contact your local Nuvoton Sales Representative if you have further questions.

Part numbers affected

Part Number	Device Characteristics
ISD3900	All Packages
ISD15C00	All Packages
ISD15100 series devices, including ISD15102, ISD15104, ISD15108 and ISD15116	All Packages

Errata Summary

The following table defines the errata in the ISD15100/3900/15C00 digital ChipCorder devices. An "X" indicates that the errata pertain to the selected device.

Item #	Issues	ISD15100 ISD3900 ISD15C00	Rev Letter	Fix Status
1	After Reset or Power-Up the values for registers 0x19-0x1F are not predictable	X	Rev-B for all series	An appropriate fix has been identified; see "Mandatory Fix" for details.

Errata symptom:

After Reset and Power-Up the values for registers 0x19 ~0x1F are not predictable. ¹

These registers are defined below.

Register Address	Suggested value	Function	Value	
0x19	0x00	Output data register	1= output high	0= output low
0x1A	0x00	Output Enable	1= enable output	0= disable output
0x1B	0xFF	Pull Enable	1= enable pull	0= disable pull
0x1D	0xFF	Pull Select	1= pull high	0= pull low
0x1F	0x00	Set alternate functions of I2S	1= enable GPIO	0= enable I2S

Table 1.1: Registers 0x19 ~ 0x1F definition.

¹ Note: Register 0x1C and 0x1E are excluded because register 0x1C is read-only and register 0x1E doesn't exist.

Since the values in registers 0x19 ~ 0x1F may change after Reset or Power-Up, it is mandatory for the user to configure these registers in the desired state for the application. If GPIO pins are not used, the user should configure these registers to the suggested values in **Table 1.1**.

Mandatory Fix:

Registers 0x19-0x1F should be programmed in POI and PU macros to correctly define register values.

Technical support contact:

Please follow the link below for technical questions http://www.nuvoton-usa.com/en/component/option,com_facileforms/Itemid,508/ or call you local Field Application Engineer.

VERSION HISTORY

VERSION	DATE	PAGE	DESCRIPTION
0.1	August 17, 2011	4	- Default value for registers 0x19 ~0x1F inconsistent with design guide.

Important Notice

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