

N79E715 Sample Code Directory

Directory Introduction for 8-bit 8051 MCU Family

Directory Information

Document	Revision history.
Sample Code	Sample code of N79E715.

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1 Document Information

**N79E715 Sample Code
Revision History.pdf**

This document shows the revision history of N79E715 sample code.

2 Sample Code Information

Sample Code	Demonstrate N79E715 function in Keil C51.
Common	Common files for Tiny Board sample code.
Include	Header file of N79E715
Startup	Startup file of N79E715

3 \Sample_Code

ADC	Demonstrate ADC function and show the conversion result to PC.
BOD_Interrupt	Demonstrate BOD interrupt.
GPIO	Demonstrate GPIO toggling function.
I2C	Demonstrate I2C Master-Slave mode function.
Interrupt	Demonstrate wake up function from idle mode through 14 interrupt sources.
ISP_APproData	Demonstrate ISP function through APROM programming Data Flash.
ISP_LDproAP	Demonstrate ISP function through LDROM programming APROM.
ISP_LDproData	Demonstrate ISP function through LDROM programming Data Flash.
ISP_SHBDA	Dynamically adjust SHBDA to decide APROM/Data Flash size and program Data Flash by ISP.
KBI	Demonstrate KBI function.
Power_Down_Wake_up	Demonstrate wake up function from power down mode through interrupt sources.
PWM	Demonstrate PWM function.
SPI_Interrupt	Demonstrate SPI Master-Slave mode function by using interrupt service routine.
SPI_Polling	Demonstrate SPI Master-Slave mode function by using polling flag.
Timer	Demonstrate Timer0 mode 1 (16-bit timer) function.
Timer2	Demonstrate Timer2 auto-reload function.
Timer2_Capture	Demonstrate Timer2 capture function.
UART	Demonstrate UART function.

WDT

Demonstrate Watch Dog function.

Appendix Limitations of KEIL™ C51 Evaluation Edition

KEIL™ development tools without a current product license run as an Evaluation edition and have the following restrictions:

- The 8051 compiler, assembler, linker, and debugger are limited to 2 Kbytes of object code. Source code may be of any size.
- Programs that generate more than 2 Kbytes of object code will not compile, assemble, or link.
- The debugger supports programs that are 2 Kbytes or smaller.
- The startup code generated includes LJMPs. Code generated cannot be used in single-chip devices that support 2 Kbytes or less of program space.
- Programs start at offset 0x0800. Programs generated with the evaluation software may not be programmed into single-chip devices with less than 2 Kbytes of on-chip ROM.
- No hardware support for multiple DPTR registers is provided.
- No support for floating-point arithmetic and no support for user libraries are provided.
- No support for in-line assembly using #pragma ASM.
- The following components which are present in the PK51 Full Version are not included in the Evaluation Version: Linker for Code Banking, Library Manager, and RTX51 Tiny Real-time Operating System.

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