M480 Series BSP Guide

Directory Introduction for 32-bit NuMicro® Family

Directory Information

Document	Driver reference guide and revision history.
Library	Driver header and source files.
SampleCode	Driver sample code.
ThirdParty	Library from third party, including FatFs, LibMAD, lwIP, uIP, FreeRTOS™, libjpeg, and Mbed TLS.

For additional information or questions, please contact: Nuvoton Technology Corporation.

www.nuvoton.com

TABLE OF CONTENTS

1 Document

CMSIS.html	Document of CMSIS version 4.5.0.
NuMicro M480 CMSIS BSP Revision History.pdf	This document shows the revision history of M480 BSP.
NuMicro M480 Driver Reference Guide.html	This document describes the usage of drivers in M480 BSP.

2 Library

CMSIS	Cortex® Microcontroller Software Interface Standard (CMSIS) V4.5.0 definitions by Arm® Corp.
Device	CMSIS compliant device header file.
FWUpdate	Firmware update library binary and header files.
SmartcardLib	Smartcard library binary and header file.
StdDriver	All peripheral driver header and source files.
UsbHostLib	USB host library source code.

3 SampleCode

CortexM4	Cortex®-M4 sample code.
Crypto_MbedTLS	Mbed TLS test suites using Crypto accelerator.
FreeRTOS	Simple FreeRTOS™ demo code.
Hard_Fault_Sample	Show hard fault information when hard fault happened.
ISP	ISP firmware samples.
NuMaker-ETM-M487	Sample codes for NuMaker-ETM-M487 board.
NuMaker-M487KMCAN	Sample codes for NuMaker-M487KMCAN board.
NuMaker-PFM-M487	Sample codes for NuMaker-PFM-M487 board.
NuMaker-PFM-M487D	Sample codes for NuMaker-PFM-M487D board.
SecureBoot	Firmware update samples in secure boot mode.
SecureBootLD	Sample codes for M48xGC/M48xG8 secure boot.
Semihost	Show how to debug with semi-host message print.
StdDriver	Sample code to demonstrate the usage of M480 MCU peripheral driver APIs.
Template	A project template for M480.

4 ThirdParty

BLE_AB1602	AB1602 BLE (Bluetooth Low Energy) module driver.
FatFs	A generic FAT file system module for small embedded systems. Its official website is: http://elm-chan.org/fsw/ff/00index_e.html.
FreeRTOS	A real time operating system available for free download. Its official website is: http://www.freertos.org/.
libjpeg	A software implements JPEG baseline, extended-sequential, and progressive compression processes maintained and published by the Independent JPEG Group (IJG). Its official website is: http://ijg.org/.
LibMAD	A MPEG audio decoder library that currently supports MPEG-1 and the MPEG-2 extension to lower sampling frequencies, as well as the de facto MPEG 2.5 format. All three audio layers — Layer I, Layer II, and Layer III (i.e. MP3) are fully implemented. This library is distributed under GPL license. Please contact Underbit Technologies (http://www.underbit.com/) for the commercial license.
lwIP	A widely used open source TCP/IP stack designed for embedded systems. Its official website is: http://savannah.nongnu.org/projects/lwip/.
mbedtls-2.13.0	A portable, easy to use, readable and flexible SSL library. Unless specifically indicated otherwise files are licensed under the Apache 2.0 license. The official website: https://tls.mbed.org/
paho.mqtt.embedded-c	Eclipse Paho MQTT C/C++ client for Embedded platforms. Its official website is: https://www.eclipse.org/paho/clients/c/embedded/
uip-0.9	uIP is a very small implementation of the TCP/IP stack that is written by Adam Dunkels <adam@sics.se>. More information can be obtained from the uIP homepage at http://www.sics.se/~adam/uip/.

5 SampleCode\CortexM4

BitBand	Demonstrate the usage of Cortex®-M4 Bit-band.
DSP_FFT	Demonstrate how to call ARM CMSIS DSP library to calculate FFT.
MPU	Demonstrate the usage of Cortex®-M4 MPU.

6 SampleCode\ISP

ISP_CAN	In-System-Programming sample code through CAN interface.
ISP_DFU	In-System-Programming sample code through USBD DFU(Device Firmware Upgrade) class.
ISP_DFU_20	In-System-Programming sample code through HSUSBD DFU(Device Firmware Upgrade) class.
ISP_HID	In-System-Programming sample code through a USBD HID interface.
ISP_HID_20	In-System-Programming sample code through a HSUSBD HID interface.
ISP_I2C	In-System-Programming sample code through I^2C interface.
ISP_RS485	In-System-Programming sample code through RS485 interface.
ISP_SPI	In-System-Programming sample code through SPI interface.
ISP_UART	In-System-Programming sample code through UART interface.
ISP_UART_SPIFLASH_M487KM	In-System-Programming sample code that supports SPI Flash programming through UART interface.

7 SampleCode\NuMaker-M487MKCAN

APROM_Loader	Show how to execute application stored in SPI Flash for M487MKCAN.
SPIM_DMM_RUN_CODE	Show how to make an application booting from APROM with a sub-routine resided on SPIM flash for M487MKCAN.
SYS_DPDMode_Wakeup	Demonstrate how to wake up system from Deep Power-down mode by Wake-up pin(PC.0) or Wake-up Timer for M487MKCAN.

8 SampleCode\NuMaker-PFM-M487

BLE_AB1602	BLE (Bluetooth Low Energy) samples demonstrate HOGP (HID over GATT Profile) and data transfer.
Heart_beating	Measure the heartbeat rate by amplifying and filtering electrocardiogram signals through OPA, and converting them into digit values through ADC. The calculated heartbeat rate will be sent and printed on screen by UART.
lwIP	Common drivers for LwIP samples.
LwIP_httpd_netconn	A simple HTTP server that demonstrates LwIP netconn API under FreeRTOS™. This HTTP server's IP address can be configured statically to 192.168.0.2, or assign by DHCP server.
LwIP_httpd_scoket	A simple HTTP server that demonstrates LwIP socket API under FreeRTOS™. This HTTP server's IP address can be configured statically to 192.168.0.2, or assigned by DHCP server.
LwIP_MQTT	A MQTT client sample. The lower level MQTT client functions are from eclipse paho.
LwIP_SSL_Client	A simple HTTPS client that sends a fixed request and displays the response
LwIP_SSL_Server	A simple HTTPS server that sends a fixed response. It serves a single client at a time.
LwIP_TCP_EchoClient	A TCP echo client which is implemented with LwIP under FreeRTOS™. This client sends "nuvoton" string to server.
LwIP_TCP_EchoServer	A TCP echo server which is implemented with LwIP under FreeRTOS™. This echo server listens to port 80, and its IP address can be configured statically to 192.168.1.2 or assigned by DHCP server. This server replies "Hello World!!" if the received string is "nuvoton", otherwise replies "Wrong Password!!" to its client.
LwIP_tftp_client	A TFTP client sample that can receive a file from TFTP

	server or send a file to TFTP server.
LwIP_tftp_server	A TFTP server sample that communicates with TFTP client.
LwIP_UDP_EchoClient	A UDP echo server which is implemented with LwIP under FreeRTOS™. This client sends “Hi there…” string to the server.
LwIP_UDP_EchoServer	A UDP echo server which is implemented with LwIP under FreeRTOS™. This echo server listens to port 80, and its IP address can be configured statically to 192.168.1.2 or assigned by DHCP server. After receiving any string from its peer, this server echoes that string back.

9 SampleCode\ PowerManagement

SYS_DPDMode_Wakeup	Show how to wake up system form DPD Power-down mode by Wake-up pin(PC.0). or Wake-up Timer or RTC Tick or RTC Alarm or RTC Tamper 0.
SYS_PowerDownMode	Show how to enter to different Power-down mode and wake-up by RTC.
SYS_PowerDown_MinCurrent	Demonstrate how to minimize power consumption when entering power down mode.
SYS_PowerMode	Show how to set different core voltage.
SYS_SPDMode_Wakeup	Show how to wake up system form SPD Power-down mode by Wake-up pin(PA.0) or Wake-up Timer or Wake-up ACMP or RTC Tick or RTC Alarm and RTC Tamper 0 or LVR or BOD.

10 SampleCode\SecureBoot

HSUSBD_FWUpdate	Use MKROM API to update firmware. This sample code is executed in APROM, and uses MKROM API to update LDROM. After system reset, the program will boot from LDROM.
HSUSBD_IAP	Use MKROM API to do IAP. This sample code is executed in APROM, and loads an image to LDROM to do Secure boot ISP to update APROM. Remember to reset the system after firmware update is complete.
OTA_FWUpdate	Use MKROM API to update firmware. This sample code is executed in LDROM, and uses it to boot from APROM or update APROM.
UART_FWUpdate	Use MKROM API to update firmware. This sample code is executed in LDROM, and uses MKROM API to update APROM. After system reset, the program will boot from APROM.

11 SampleCode\SecureBoot_LD

SecureBootAP	Demonstrate how to generate a secure boot application. A post-build tool SecureBootMaker.exe is used to translate a bin file into a hex file for secure boot.
SecureISP	An example implementation of secure ISP firmware. Secure ISP firmware connects to the Host(PC) SecureISP tool via an UART or USB connection. It receives commands from Host to update the user application in APROM/LDROM. InM48xGC/M48xG8secure boot scenario, Secure Bootloader (MKROM) verifies and loads the ISP firmware, then in turn ISP firmware verifies and loads user applications.

12 SampleCode\StdDriver

System Manager (SYS)

SYS_BODWakeup	Demonstrate how to wake up system from Power-down mode by brown-out detector interrupt.
SYS_PLLClockOutput	Change system clock to different PLL frequency and output system clock from CLKO pin.
SYS_TrimIRC	Demonstrate how to use LXT to trim HIRC.

Clock Controller (CLK)

CLK_ClockDetector	Demonstrate the usage of clock fail detector and clock frequency range detector function.

Flash Memory Controller (FMC)

FMC_CRC32	Demonstrate how to use FMC CRC32 ISP command to calculate the CRC32 checksum of APROM, LDROM, and SPROM.
FMC_Dual_Bank	Show FMC dual bank capability. Non-blocking program APROM bank1 while program is running on APROM bank0 is running on bank0 without being blocked
FMC_ExeInSRAM	Implement a code and execute in SRAM to program embedded Flash.
FMC_IAP	Demonstrate FMC IAP boot mode and show how to use vector remap function. LDROM image was embedded in APROM image and be programmed to LDROM Flash at run-time. This sample also shows how to branch between APROM and LDROM.
FMC_MultiBoot	Implement a multi-boot system to boot from different applications in APROM. A LDROM code and 4 APROM code are implemented in this sample code.
FMC_MultiWordProgram	Show FMC multi-word program ISP command to program APROM 0x00000~0x20000 area.

FMC_OTP	Demonstrate how to program, read, and lock OTP.
FMC_ReadAllOne	Demonstrate how to use FMC Read-All-One ISP command to verify APROM/LDROM pages are all 0xFFFFFFFF or not.
FMC_RW	Show FMC read Flash IDs, erase, read, and write functions.
FMC_SecureKey	Show how to setup the KPROM and how to perform KPROM comparison.
FMC_SPROM	Show how to make an application running on APROM but with a sub-routine on SPROM, which can be secured
FMC_XOM	An example of using FMC driver to set up and erase XOM regions.
FMC_XOM_LibDemo	Show a solution of calling the library resided in an XOM region.

External Bus Interface (EBI)

EBI_NOR	Configure EBI interface to access NOR Flash connected to EBI interface.
EBI_SRAM	Configure EBI interface to access SRAM connected to EBI interface.

General Purpose I/O (GPIO)

GPIO_EINTAndDebounce	Show the usage of GPIO external interrupt function and de-bounce function.
GPIO_INT	Show the usage of GPIO interrupt function.
GPIO_OutputInput	Show how to set GPIO pin mode and use pin data input/output control.
GPIO_PowerDown	Show how to wake up system from Power-down mode by GPIO interrupt.

PDMA Controller (PDMA)

PDMA_BasicMode	Use PDMA channel 2 to demonstrate memory to memory transfer.
PDMA_ScatterGather	Use PDMA channel 5 to demonstrate memory to memory transfer by scatter-gather mode.
PDMA_ScatterGather_ PingPongBuffer	Use PDMA to implement Ping-Pong buffer by scatter-gather mode (memory to memory).
PDMA_Stride	Use PDMA channel 2 to transfer data from memory to memory with stride.
PDMA_Stride_Repeat	Use PDMA channel 0 to transfer data from memory to memory with stride and repeat.
PDMA_TimeOut	Demonstrate PDMA timeout feature.

Timer Controller (TIMER)

TIMER_ACMPTrigger	Use ACMP to trigger timer reset mode.
TIMER_CaptureCounter	Show how to use the timer2 capture function to capture timer2 counter value.
TIMER_Delay	Demonstrate the usage of TIMER_Delay() API to generate a 1 second delay.
TIMER_EventCounter	Use pin PD.4 to demonstrate timer event counter function.
TIMER_FreeCountingMode	Use the timer pin PA.7 to demonstrate timer free counting mode function. And displays the measured input frequency to UART console.
TIMER_InterTimerTriggerMode	Use the timer pin PD.4 to demonstrate inter-timer trigger mode function. Also display the measured input frequency to UART console.
TIMER_Periodic	Use the timer periodic mode to generate timer interrupt every 1 second.
TIMER_PeriodicINT	Implement timer counting in periodic mode.

TIMER_PWM_Brake	Demonstrate how to use Timer PWM brake function.
TIMER_PWM_ChangeDuty	Change duty cycle and period of output waveform by Timer PWM Double Buffer function.
TIMER_PWM_DeadTime	Demonstrate how to use Timer PWM Dead Time function.
TIMER_PWM_OutputWaveform	Enable 4 Timer PWM output channels with different frequency and duty ratio.
TIMER_TimeoutWakeup	Use Timer to wake up system from Power-down mode periodically.
TIMER_ToggleOut	Demonstrate the timer 0 toggle out function on pin PD.4.

Watchdog Timer (WDT)

WDT_TimeoutWakeupAnd Reset	Implement WDT time-out interrupt event to wake up system and generate time-out reset system event while WDT time-out reset delay period expired.

Window Watchdog Timer (WWDT)

WWDT_CompareINT	Show how to reload the WWDT counter value.

Real Timer Clock (RTC)

RTC_Alarm_Test	Demonstrate the RTC alarm function. It sets an alarm 10 seconds after execution.
RTC_Alarm_Wakeup	Use RTC alarm interrupt event to wake up system.
RTC_Dynamic_Tamper	Show how to use RTC dynamic tamper function.
RTC_Spare_Access	Show how to access RTC spare registers.
RTC_Static_Tamper	Show how to use RTC static tamper function.
RTC_Time_Display	Demonstrate the RTC function and displays current time to the UART console.

Basic PWM Generator and Capture Timer (BPWM)

BPWM_Capture	Use BPWM0 Channel 0 (PA.0) to capture the BPWM1 Channel 0(PE.13) Waveform
BPWM_DoubleBuffer	Change duty cycle and period of output waveform by BPWM Double Buffer function.
BPWM_OutputWaveform	Demonstrate how to use BPWM counter output waveform.
BPWM_SwitchDuty	Change duty cycle of output waveform by configured period.
BPWM_SyncStart	Demonstrate how to use BPWM counter synchronous start function.

Enhanced PWM Generator and Capture Timer (EPWM)

EPWM_AccumulatorINT_TriggerPDMA	Demonstrate EPWM accumulator interrupt trigger PDMA.
EPWM_AccumulatorStopMode	Demonstrate EPWM accumulator stop mode.
EPWM_Brake	Demonstrate how to use EPWM brake function.
EPWM_Capture	Capture the EPWM1 Channel 0 waveform by EPWM1 Channel 2.
EPWM_DeadTime	Demonstrate how to use EPWM Dead Time function.
EPWM_DoubleBuffer	Change duty cycle and period of output waveform by EPWM Double Buffer function.
EPWM_OutputWaveform	Demonstrate how to use PWM output waveform.
EPWM_PDMA_Capture	Capture the EPWM1 Channel 0 waveform by EPWM1 Channel 2, and use PDMA to transfer captured data.
EPWM_SwitchDuty	Change duty cycle of output waveform by configured period.
EPWM_SyncStart	Demonstrate how to use PWM counter synchronous start function.

Enhanced Input Capture Timer (ECAP)

ECAP_GetInputFreq	Show how to use ECAP to measure clock frequency.
ECAP_GetQEIFreq	Show how to use ECAP interface to get QEIA frequency.

Quadrature Encoder Interface(QEI)

QEI_CompareMatch	Show the usage of QEI compare function.

UART Interface Controller (UART)

UART_AutoBaudRate	Show how to use auto baud rate detection function.
UART_AutoFlow	Transmit and receive data using auto flow control.
UART_IrDA	Transmit and receive UART data in UART IrDA mode.
UART_LIN	Demonstrate how to send data to LIN bus.
UART_PDMA	Demonstrate UART transmit and receive function with PDMA.
UART_RS485	Transmit and receive data in UART RS485 mode.
UART_TxRxFunction	Transmit and receive data from PC terminal through a RS232 interface.
UART_Wakeup	Show how to wake up system from Power-down mode by UART interrupt.

Smartcard Host Interface (SC)

SC_ReadATR	Read the smartcard ATR from smartcard 1 interface.
SC_ReadSIM_PhoneBook	Demonstrate how to read phone book information in the SIM card.
SC_Timer	Demonstrate how to use SC embedded timer
SCUART_TxRx	Demonstrate smartcard UART mode by connecting PA.0 and PA.1 pins.

Quad Serial Peripheral Interface (QSPI)

QSPI_DualMode_Flash	Access SPI Flash using QSPI dual mode.
QSPI_QuadMode_Flash	Access SPI Flash using QSPI quad mode.
QSPI_Slave3Wire	Demonstrate QSPI0 3-wire mode.

Serial Peripheral Interface (SPI)

SPI_Flash	Access SPI Flash through a SPI interface.
SPI_HalfDuplex	Demonstrate SPI half-duplex mode.
SPI_LoopBack	A SPI read/write demo connecting SPI0 MISO and MOSI pins.
SPI_MasterFIFOMode	Configure SPI0 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device with FIFO mode. This sample code needs to work with SPI_SlaveFIFOMode sample code.
SPI_PDMA_LoopTest	Demonstrate SPI data transfer with PDMA. SPI0 will be configured as Master mode and SPI1 will be configured as Slave mode. Both TX PDMA function and RX PDMA function will be enabled.
SPI_SlaveFIFOMode	Configure SPI0 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device with FIFO mode. This sample code needs to work with SPI_MasterFIFOMode sample code.
SPII2S_Master	Configure SPI1 as I^2S Master mode and demonstrate how I^2S works in Master mode.
SPII2S_PDMA_Codec	An I^2S demo with PDMA function connected with audio codec.
SPII2S_PDMA_Play	An I^2S demo for playing data and demonstrating how I^2S works with PDMA.
SPII2S_PDMA_PlayRecord	An I^2S demo for playing and recording data with PDMA function.

SPII2S_PDMA_Record	An I²S demo for recording data and demonstrating how I²S works with PDMA.
SPII2S_Slave	Configure SPI1 as I²S Slave mode and demonstrate how I²S works in Slave mode. This sample code needs to work with SPII2S_Master.

Serial Peripheral Interface Master Mode (SPIM)

SPIM_CIPHER	Demonstrate SPIM DMA read/write with cipher enabled. This sample also dumps SPI Flash content via I/O mode read to prove it is encrypted cipher context.
SPIM_DMA_RW	Demonstrate SPIM DMA mode read/write function.
SPIM_DMM	Demonstrate SPIM DMM mode read function. This sample programs SPI Flash with DMA write and verify Flash with DMA read and CPU read respectively.
SPIM_DMM_RUN_CODE	Show how to make an application booting from APROM with a sub-routine resided on SPIM flash.
SPIM_IO_RW	Demonstrate how to issue SPI Flash erase, program, and read commands under SPIM I/O mode.

I²C Serial Interface Controller (I²C)

I2C_EEPROM	Read/write EEPROM via I²C interface.
I2C_Loopback	Demonstrate how a Master accesses Slave.
I2C_Master	An I²C master mode demo code.
I2C_Master_PDMA	Demonstrate how a Master accesses Slave using PDMA TX mode and PDMA RX mode.
I2C_MultiBytes_Master	Demonstrate how to use multi-bytes API to access slave. This sample code needs to work with I2C_Slave.
I2C_PDMA_TRX	Demonstrate I²C PDMA mode that needs to connect I2C0 (Master) and I2C1 (Slave).

I2C_SingleByte_Master	Demonstrate how to use single byte API to access slave. This sample code needs to work with I2C_Slave.
I2C_Slave	An I²C slave mode demo code.
I2C_Slave_PDMA	Demonstrate how a Slave uses PDMA Rx mode receive data from a Master.
I2C_SMBus	Show how to control SMBus interface and use SMBus protocol between host and slave.
I2C_Wakeup_Slave	Demonstrate how to set I²C to wake up MCU from Power-down mode. This sample code needs to work with I2C_Master.

Universal Serial Control Interface Controller - UART Mode (USCI-UART)

USCI_UART_AutoBaudRate	Show how to use auto baud rate detection function.
USCI_UART_Autoflow_Master	Transmit and receive data with auto flow control. This sample code needs to work with USCI_UART_Autoflow_Master.
USCI_UART_Autoflow_Slave	Transmit and receive data with auto flow control. This sample code needs to work with USCI_UART_Autoflow_Slave.
USCI_UART_PDMA	Demonstrate USCI_UART data transfer with PDMA.
USCI_UART_RS485_Master	Transmit and receive data in RS485 mode. This sample code needs to work with USCI_UART_RS485_Master.
USCI_UART_RS485_Slave	Transmit and receive data in RS485 mode. This sample code needs to work with USCI_UART_RS485_Slave.
USCI_UART_TxRxFunction	Transmit and receive data from PC terminal through a RS232 interface.
USCI_UART_Wakeup	Show how to wake up system from Power-down mode by USCI interrupt in UART mode.

Universal Serial Control Interface Controller - I2C Mode (USCI-I2C)

USCI_I2C_EEPROM	Show how to use USCI_I2C interface to access EEPROM.
USCI_I2C_Lookback	Show a Master how to access 7-bit address Slave (loopback).
USCI_I2C_Loopback_10bit	Show a Master how to access 10-bit address Slave (loopback).
USCI_I2C_Master	Show a Master how to access Slave.
USCI_I2C_Master_10bit	Show a Master how to access 10-bit address Slave.
USCI_I2C_Monitor	Use USCI_I2C to monitor and log I^2C bus traffic.
USCI_I2C_MultiBytes_Master	Demonstrate how to use multi-bytes API to access slave. This sample code needs to work with USCI_I2C_MultiBytes_Master.
USCI_I2C_SingleByte_Master	Demonstrate how to use single byte API to access slave. This sample code needs to work with USCI_I2C_SingleByte_Master.
USCI_I2C_Slave	Show a Slave how to receive data from Master.
USCI_I2C_Slave_10bit	Show a 10-bit address Slave how to receive data from Master.
USCI_I2C_Wakeup_Slave	Show how to wake-up USCI_I2C from deep sleep mode.

Universal Serial Control Interface Controller - SPI Mode (USCI-SPI)

USCI_SPI_Loopback	A USCI_SPI read/write demo connecting USCI_SPI0 and USCI_SPI1 interface.
USCI_SPI_MasterMode	Configure USCI_SPI1 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device. Needs to work with USCI_SPI_MasterMode sample code.
USCI_SPI_PDMA_LoopTest	Demonstrate USCI_SPI data transfer with PDMA. USCI_SPI0 will be configured as Master mode and

	USCI_SPI1 will be configured as Slave mode. Both TX PDMA function and RX PDMA function will be enabled.
USCI_SPI_SlaveMode	Configure USCI_SPI1 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device. This sample code needs to work with USCI_SPI_SlaveMode sample code.

I²S Controller (I²S)

I2S_Codec	An I²S demo used to play back the input from line-in or MIC interface.
I2S_Codec_PDMA	An I²S with PDMA demo used to play back the input from line-in or MIC interface.
I2S_MP3PLAYER	A MP3 player sample that plays MP3 files stored on SD memory card.
I2S_WAVPLAYER	A WAV file player that plays back WAV file stored in a USB pen drive.

Controller Area Network (CAN)

CAN_BasicMode_Rx	Demonstrate CAN bus receive a message with basic mode. This sample code could work with CAN_BasicMode_Tx sample code.
CAN_BasicMode_Tx	Demonstrate CAN bus transmit a message with basic mode. This sample code could work with CAN_BasicMode_Rx sample code.
CAN_BasicMode_Tx_Rx	Demonstrate CAN bus transmit and receive a message with basic mode by connecting CAN0 and CAN1 to the same CAN bus.
CAN_NormalMode_Rx	Demonstrate CAN bus receive a message with normal mode. This sample code could work with CAN_NormalMode_Tx sample code.
CAN_NormalMode_Tx	Demonstrate CAN bus transmit a message with normal mode. This sample code could work with

	CAN_NormalMode_Rx sample code.
CAN_NormalMode_Tx_Rx	Demonstrate CAN bus transmit and receive a message with normal mode by connecting CAN 0 and CAN1 to the same CAN bus.

USB 1.1 Device Controller (USBD)

USBD_Audio_Codec	Demonstrate how to implement a USB audio class device.
USBD_Audio_Headset	An UAC1.0 sample used to play the sound sent from PC through the USB interface.
USBD_HID_Keyboard	Demonstrate how to implement a USB keyboard device. This sample code supports to use GPIO to simulate key input.
USBD_HID_Mouse	Simulate a USB mouse and draws circle on the screen.
USBD_HID_MouseKeyboard	Simulate an USB HID mouse and HID keyboard. Mouse draws circle on the screen and Keyboard uses GPIO to simulate key input.
USBD_HID_RemoteWakeup	Simulate a HID mouse supports USB suspend and remote wakeup.
USBD_HID_Touch	Demonstrate how to implement a USB touch digitizer device. Two lines demo in Paint.
USBD_HID_Transfer	Demonstrate how to transfer data between a USB device and PC through a USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
USBD_HID_Transfer_ And_Keyboard	Demonstrate how to implement a composite device (HID Transfer and keyboard). Transfer data between USB device and PC through a USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
USBD_HID_Transfer_ And_MSC	Demonstrate how to implement a composite device (HID Transfer and Mass storage). Transfer data between USB device and PC through a USB HID interface. A

	windows tool is also included in this sample code to connect with a USB device.
USBD_HID_Transfer_CTRL	Use USB Host core driver and HID driver. It shows how to submit HID class request and how to read data from control pipe. A windows tool is also included in this sample code to connect with a USB device.
USBD_Mass_Storage_CDROM	Demonstrate the emulation of USB Mass Storage Device CD-ROM.
USBD_Mass_Storage_Flash	Use internal Flash as back end storage media to simulate a USB pen drive.
USBD_Mass_Storage_SD	Implement a SD card reader.
USBD_Mass_Storage_SRAM	Use internal SRAM as back end storage media to simulate a 30 KB USB pen drive.
USBD_Micro_Printer	Demonstrate how to implement a USB micro printer device.
USBD_Printer_And_HID_ Transfer	Demonstrate how to implement a composite device (USB micro printer device and HID Transfer). Transfer data between USB device and PC through a USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
USBD_VCOM_And_HID_ Keyboard	Demonstrate how to implement a composite device (VCOM and HID keyboard).
USBD_VCOM_And_HID_ Transfer	Demonstrate how to implement a composite device (VCOM and HID Transfer). Transfer data between USB device and PC through a USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
USBD_VCOM_And_Mass_ Storage	Demonstrate how to implement a composite device (Virtual COM port and Mass storage device).
USBD_VCOM_DualPort	Demonstrate how to implement a USB dual virtual COM port device.
USBD_VCOM_SerialEmulator	Demonstrate how to implement a USB virtual COM port device.

USBD_VENDOR_LBK	Implement a proprietary Vendor LBK device. This sample requires a M480 USB host running sample HSUSBD_USBH_VENDOR_LBK to be connected.

High Speed USB 2.0 Device Controller (HSUSBD)

HSUSBD_Audio10_Codec	An UAC1.0 sample used to record and play the sound sent from PC through the USB interface
HSUSBD_Audio10_Headset	An UAC1.0 sample and used to plays the sound send from PC through the USB interface
HSUSBD_Audio20_Codec	An UAC2.0 sample used to record and play the sound sent from PC through the USB interface.
HSUSBD_Audio20_Headset	An UAC2.0 sample used to play the sound sent from PC through the USB interface.
HSUSBD_HID_Mouse	Simulate a USB mouse and draws circle on the screen.
HSUSBD_HID_MouseKeyboard	Simulate a USB mouse and a USB keyboard.
HSUSBD_HID_Transfer	Demonstrate how to transfer data between USB device and PC through USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
HSUSBD_HID_Transfer_ And_MSC	Demonstrate how to implement a composite device (HID Transfer and Mass storage). Transfer data between USB device and PC through the USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
HSUSBD_Mass_Storage_ DataFlash	Use embedded Data Flash as storage to implement a USB Mass-Storage device.
HSUSBD_Mass_Storage SactterGather	Demonstrate the usage of USBD DMA scatter gather function.
HSUSBD_Mass_Storage_SD	Implement a SD card reader.
HSUSBD_Mass_Storage_ ShortPacket	Implement a mass storage class sample to demonstrate how to receive a USB short packet.

HSUSBD_Mass_Storage_SRAM	Use internal SRAM as back end storage media to simulate a 30 KB USB pen drive.
HSUSBD_RNDIS	Demonstrate how to implement a USB Ethernet (Remote Network Driver Interface Specification).
HSUSBD_VCOM_SerialEmulator	Demonstrate how to implement a USB virtual com port device.
HSUSBD_VENDOR_LBK	Implement a proprietary Vendor LBK device. This sample requires a M480 USB host running sample HSUSBD_USBH_VENDOR_LBK to be connected.

USB 1.1/2.0 Host Controller (HSUSBH)

HSUSBH_USBH_AudioClass	Demonstrate how to use USBH Audio Class driver. It shows the mute, volume, auto-gain, channel, and sampling rate control.
HSUSBH_USBH_DEV_CONN	Use connect/disconnect callback functions to handle of device connect and disconnect events.
HSUSBH_USBH_Firmware_ Update	Automatically search and read new firmware from USB drive, if found, update APROM Flash with it.
HSUSBH_USBH_HID	Use USB Host core driver and HID driver. This sample demonstrates how to submit HID class request and read data from interrupt pipe. This sample supports dynamic device plug/un-plug and multiple HID devices.
HSUSBH_USBH_HID_Keyboard	Demonstrate reading key inputs from USB keyboards. This sample includes an USB keyboard driver which is based on the HID driver.
HSUSBH_USBH_HIDMouse_ Keyboard	Demonstrates how to support USB mouse and keyboard input.
HSUSBH_USBH_MassStorage	Use a command-shell-like interface to demonstrate how to use USBH mass storage driver and make it work as a disk driver under the FATFS file system.
HSUSBH_USBH_SPIM_Writer	Provide a command line interface for reading files from USB disk and writing to SPIM Flash. This sample code also provides functions of dump SPIM Flash, compares

	USB disk file with SPIM Flash, and branches to run code on SPIM.
HSUSBH_USBH_UAC_HID	Show how to use USBH Audio Class driver and HID driver at the same time. The target device is a Game Audio (UAC+HID composite device).
HSUSBH_USBH_UAC_ LoopBack	Receive audio data from an UAC device, and immediately send back to the UAC device.
HSUSBH_USBH_VCOM	Demonstrate how to use the USB Host core driver and CDC driver to connect a CDC class VCOM device.
HSUSBH_USBH_VENDOR_LBK	Show how to do transfer on a known device with a vendor driver. This sample requires a M480 USB device running sample HSUSBD_VENDOR_LBK or USBD_VENDOR_LBK to be connected.

USB On-The-Go (OTG)

OTG_Dual_Role_UMAS	An OTG sample code that will become a USB host when connected with a Micro-A cable, and can access the pen drive when plugged in. It will become a removable disk when connected with a Micro-B cable, and then plug into PC.
OTG_HNP	Show HID mouse with OTG HNP protocol.

High Speed USB On-The-Go (HSOTG)

HSOTG_Dual_Role_UMAS	An OTG sample code that will become a USB host when connected with a Micro-A cable, and can access the pen drive when plugged in. It will become a removable disk when connected with a Micro-B cable, and then plug into PC.
HSOTG_HNP	Show HID mouse with OTG HNP protocol.

Ethernet MAC Controller (EMAC)

EMAC_httpd	A LwIP httpd sample that supports CGI and SSI.

EMAC_lwiperf	A LwIP iperf speed test sample in M480.
EMAC_TimeStamp	Demonstrate the usage of Ethernet time stamp function. It sets current time to 1000 second and prints out current time every second. It also sets an alarm at 1010 second. And rewind current time by 5 seconds after the alarm.
EMAC_TxRx	This Ethernet sample tends to get a DHCP lease from DHCP server, and use 192.168.10.10 as IP address if failed to get a lease. After IP address configured, this sample can reply to PING packets.
EMAC_uIP_httpd	Implement a HTTP server using uIP.
EMAC_uIP_telnetd	Implement a Telnet server using uIP.

SD Host Controller (SDH)

SDH_FATFS	Access a SD card formatted in FAT file system.
SDH_FATFS_Dual	Access two SD cards formatted in FAT file system.
SDH_Firmware_Update	Automatically search and read new firmware from SD card, if found, update APROM Flash with it.

Crypto Accelerator (CRYPTO)

CRYPTO_AES	Show Crypto IP AES-128 ECB mode encrypt/decrypt function.
CRYPTO_ECC_ GenerateSecretZ	Show Crypto IP ECC CDH secret Z generation.
CRYPTO_ECC_KeyGeneration	Show Crypto IP ECC P-192 key generation function.
CRYPTO_ECC_Signature Generation	Show Crypto IP ECC P-192 ECDSA signature generation function.
CRYPTO_ECC_Signature Verification	Show Crypto IP ECC P-192 ECDSA signature verification function.
CRYPTO_HMAC	Show Crypto IP HMAC function.

CRYPTO_PRNG	Generate random numbers using Crypto IP PRNG.
CRYPTO_SHA	Use Crypto IP SHA engine to run through known answer SHA1 test vectors.
CRYPTO_TDES	Show Crypto IP Triple DES CBC mode encrypt/decrypt function.

True Random Number Generator (TRNG)

TRNG_GenRndNum	Generate random numbers using TRNG.

Camera Capture Interface Controller (CCAP)

CCAP_Mono_1Bit_Luma	Use luminance 8-bit to 1-bit conversion to store captured image from HM01B0 sensor to SRAM.
CCAP_Packet_DownScale	Use packet format (all the luma and chroma data interleaved) to store captured image from NT99141 to SRAM.
CCAP_Packet_JpegEncode	Capture an image and convert to JEPG format.

CRC Controller (CRC)

CRC_CCITT	Implement CRC in CRC-CCITT mode and get the CRC checksum result.
CRC_CRC8	Implement CRC in CRC-8 mode and get the CRC checksum result.
CRC_CRC32	Implement CRC in CRC-32 mode and get the CRC checksum result.

Enhanced 12-bit Analog-to-Digital Converter (EADC)

EADC_ADINT_Trigger	Use ADINT interrupt to do the EADC continuous scan conversion.
EADC_BandGap	Convert Band-gap (Sample module 16) and print

	conversion result.
EADC_EPWM_Trigger	Demonstrate how to trigger EADC by EPWM.
EADC_PDMA_EPWM_Trigger	Demonstrate how to trigger EADC by EPWM and transfer conversion data by PDMA.
EADC_Pending_Priority	Demonstrate how to trigger multiple sample modules and got conversion results in order of priority.
EADC_ResultMonitor	Monitor the conversion result of channel 2 by the digital compare function.
EADC_SimultaneousMode	Demonstrate EADC0 and EADC1 are triggered in simultaneous mode.
EADC_SWTRG_Trigger	Trigger EADC by writing EADC_SWTRG register.
EADC_TempSensor	Convert temperature sensor (Sample module 17) and print conversion result.
EADC_Timer_Trigger	Show how to trigger EADC by timer.

Digital-to-Analog Converter (DAC)

DAC_EPWMTrigger	Demonstrate EPWM trigger DAC to convert sine wave outputs.
DAC_ExtPinTrigger	Demonstrate external pin trigger DAC convert sine wave outputs.
DAC_GroupMode	Show how to make 2 DAC output channels work in group mode.
DAC_PDMA_EPWMTrigger	Show EPWM trigger DAC to fetch data with PDMA and convert sine wave outputs.
DAC_PDMA_TimerTrigger	Show timer trigger DAC to fetch data with PDMA and convert sine wave outputs.
DAC_SoftwareTrigger	Demonstrate software trigger DAC to convert sine wave outputs.
DAC_TimerTrigger	Demonstrate timer trigger DAC to convert sine wave

	outputs.

Analog Comparator Controller (ACMP)

ACMP_ComapreDAC	Demonstrate analog comparator (ACMP) comparison by comparing ACMP0_P0 input and DAC voltage and shows the result on UART console.
ACMP_ComapreVBG	Demonstrate analog comparator (ACMP) comparison by comparing ACMP0_P0 input and VBG voltage and shows the result on UART console.
ACMP_Wakeup	Use ACMP to wake up system from Power-down mode while comparator output changes.
ACMP_WindowCompare	Show how to monitor ACMP input with window compare function.
ACMP_WindowLatch	Demonstrate how to use ACMP window latch mode.

OP Amplifier (OPA)

OPA_Control	Show how to control OPA.

13 Sample Code Compatibility List

Part Number / Sample Code	M48xxGAAE / IDEA, M487KMCAN	M48xxG8AE / GCAE
CortexM4	√	√
Crypto_MbedTLS	√	√
FreeRTOS	√	√
Hard_Fault_Sample	√	√
Semihost	√	√
Template	√	√
ISP_CAN	√	√
ISP_DFU	√	√
ISP_DFU_20	√	-
ISP_HID	√	√
ISP_HID_20	√	-
ISP_I2C	√	√
ISP_RS485	√	√
ISP_SPI	√	√
ISP_UART	√	√
ISP_UART_SPIFLASH_M487KM	√	-
NuMaker-ETM-M487	√	√
NuMaker-M487KMCAN	√	-
BLE_AB1602	√	-
Heart_beating	√	√
lwIP	√	-
LwIP_httpd_netconn	√	-
LwIP_httpd_scoket	√	-
LwIP_MQTT	√	-
LwIP_SSL_Client	√	-
LwIP_SSL_Server	√	-
LwIP_TCP_EchoClient	√	-

LwIP_TCP_EchoServer	√	-
LwIP_tftp_client	√	-
LwIP_tftp_server	√	-
LwIP_UDP_EchoClient	√	-
LwIP_UDP_EchoServer	√	-
SecureBoot	√	-
SecureBoot_LD	-	√
ACMP_ComapreDAC	√	√
ACMP_ComapreVBG	√	√
ACMP_Wakeup	√	√
ACMP_WindowCompare	√	√
ACMP_WindowLatch	√	√
BPWM_Capture	√	√
BPWM_DoubleBuffer	√	√
BPWM_OutputWaveform	√	√
BPWM_SwitchDuty	√	√
BPWM_SyncStart	√	√
CAN_BasicMode_Rx	√	√
CAN_BasicMode_Tx	√	√
CAN_BasicMode_Tx_Rx	√	√
CAN_NormalMode_Rx	√	√
CAN_NormalMode_Tx	√	√
CAN_NormalMode_Tx_Rx	√	√
CCAP_Mono_1Bit_Luma	-	√
CCAP_Packet_DownScale	-	√
CCAP_Packet_JpegEncode	-	√
CLK_ClockDetector	√	√
CRC_CCITT	√	√
CRC_CRC8	√	√
CRC_CRC32	√	√

CRYPTO_AES	√	√
CRYPTO_ECC_GenerateSecretZ	√	√
CRYPTO_ECC_KeyGeneration	√	√
CRYPTO_ECC_SignatureGeneration	√	√
CRYPTO_ECC_SignatureVerification	√	√
CRYPTO_HMAC	√	√
CRYPTO_PRNG	√	√
CRYPTO_SHA	√	√
CRYPTO_TDES	√	-
DAC_EPWMTrigger	√	√
DAC_ExtPinTrigger	√	√
DAC_GroupMode	√	-
DAC_PDMA_EPWMTrigger	√	√
DAC_PDMA_TimerTrigger	√	√
DAC_SoftwareTrigger	√	√
DAC_TimerTrigger	√	√
EADC_ADINT_Trigger	√	√
EADC_BandGap	√	√
EADC_EPWM_Trigger	√	√
EADC_PDMA_EPWM_Trigger	√	√
EADC_Pending_Priority	√	√
EADC_ResultMonitor	√	√
EADC_SimultaneousMode	-	√
EADC_SWTRG_Trigger	√	√
EADC_TempSensor	√	√
EADC_Timer_Trigger	√	√
EBI_NOR	√	√
EBI_SRAM	√	√
ECAP_GetInputFreq	√	√
ECAP_GetQEIFreq	√	√

EMAC_httpd	√	-
EMAC_lwiperf	√	-
EMAC_TimeStamp	√	-
EMAC_TxRx	√	-
EMAC_uIP_httpd	√	-
EMAC_uIP_telnetd	√	-
EPWM_AccumulatorINT_TriggerPDMA	√	√
EPWM_AccumulatorStopMode	-	√
EPWM_Brake	√	√
EPWM_Capture	√	√
EPWM_DeadTime	√	√
EPWM_DoubleBuffer	√	√
EPWM_OutputWaveform	√	√
EPWM_PDMA_Capture	√	√
EPWM_SwitchDuty	√	√
EPWM_SyncStart	√	√
FMC_CRC32	√	√
FMC_Dual_Bank	√	-
FMC_ExeInSRAM	√	√
FMC_IAP	√	√
FMC_MultiBoot	√	√
FMC_MultiWordProgram	√	√
FMC_OTP	√	√
FMC_ReadAllOne	√	√
FMC_RW	√	√
FMC_SecureKey	√	√
FMC_SPROM	√	-
FMC_XOM	-	√
FMC_XOM_LibDemo	-	√
GPIO_EINTAndDebounce	√	√

GPIO_INT	√	√
GPIO_OutputInput	√	√
GPIO_PowerDown	√	√
HSOTG_Dual_Role_UMAS	√	-
HSOTG_HNP	√	-
HSUSBD_Audio10_Codec	√	-
HSUSBD_Audio10_Headset	√	-
HSUSBD_Audio20_Codec	√	-
HSUSBD_Audio20_Headset	√	-
HSUSBD_HID_Mouse	√	-
HSUSBD_HID_MouseKeyboard	√	-
HSUSBD_HID_Transfer	√	-
HSUSBD_HID_Transfer_And_MSC	√	-
HSUSBD_Mass_Storage_DataFlash	√	-
HSUSBD_Mass_Storage SactterGather	√	-
HSUSBD_Mass_Storage_SD	√	-
HSUSBD_Mass_Storage_ShortPacket	√	-
HSUSBD_Mass_Storage_SRAM	√	-
HSUSBD_RNDIS	√	-
HSUSBD_VCOM_SerialEmulator	√	-
HSUSBD_VENDOR_LBK	√	-
HSUSBH_USBH_AudioClass	√	√
HSUSBH_USBH_DEV_CONN	√	√
HSUSBH_USBH_Firmware_Update	√	√
HSUSBH_USBH_HID	√	√
HSUSBH_USBH_HID_Keyboard	√	√
HSUSBH_USBH_HID_Mouse_Keyboard	√	√
HSUSBH_USBH_MassStorage	√	√
HSUSBH_USBH_SPIM_Writer	√	√

HSUSBH_USBH_UAC_HID	√	√
HSUSBH_USBH_UAC_LoopBack	√	√
HSUSBH_USBH_VCOM	√	√
HSUSBH_USBH_VENDOR_LBK	√	√
I2C_EEPROM	√	√
I2C_Loopback	√	√
I2C_Master	√	√
I2C_Master_PDMA	√	√
I2C_MultiBytes_Master	√	√
I2C_PDMA_TRX	√	√
I2C_SingleByte_Master	√	√
I2C_Slave	√	√
I2C_Slave_PDMA	√	√
I2C_SMBus	√	√
I2C_Wakeup_Slave	√	√
I2S_Codec	√	√
I2S_Codec_PDMA	√	√
I2S_MP3PLAYER	√	√
I2S_WAVPLAYER	√	√
OPA_Control	√	-
OTG_Dual_Role_UMAS	√	-
OTG_HNP	√	-
PDMA_BasicMode	√	√
PDMA_ScatterGather	√	√
PDMA_ScatterGather_ PingPongBuffer	√	√
PDMA_Stride	√	√
PDMA_Stride_Repeat	-	√
PDMA_TimeOut	√	√
QEI_CompareMatch	√	√

QSPI_DualMode_Flash	√	√
QSPI_QuadMode_Flash	√	√
QSPI_Slave3Wire	√	√
RTC_Alarm_Test	√	√
RTC_Alarm_Wakeup	√	√
RTC_Dynamic_Tamper	√	√
RTC_Spare_Access	√	√
RTC_Static_Tamper	√	√
RTC_Time_Display	√	√
SC_ReadATR	√	√
SC_ReadSIM_PhoneBook	√	√
SC_Timer	√	√
SCUART_TxRx	√	√
SDH_FATFS	√	√
SDH_FATFS_Dual	√	√
SDH_Firmware_Update	√	√
SPI_Flash	√	√
SPI_HalfDuplex	√	√
SPI_LoopBack	√	√
SPI_MasterFIFOMode	√	√
SPI_PDMA_LoopTest	√	√
SPI_SlaveFIFOMode	√	√
SPII2S_Master	√	√
SPII2S_PDMA_Codec	√	√
SPII2S_PDMA_Play	√	√
SPII2S_PDMA_PlayRecord	√	√
SPII2S_PDMA_Record	√	√
SPII2S_Slave	√	√
SPIM_CIPHER	√	-

SPIM_DMA_RW	√	-
SPIM_DMM	√	-
SPIM_DMM_RUN_CODE	√	-
SPIM_IO_RW	√	-
SYS_BODWakeup	√	√
SYS_PLLClockOutput	√	√
SYS_TrimIRC	√	√
TIMER_ACMPTrigger	√	√
TIMER_CaptureCounter	√	√
TIMER_Delay	√	√
TIMER_EventCounter	√	√
TIMER_FreeCountingMode	√	√
TIMER_InterTimerTriggerMode	√	√
TIMER_Periodic	√	√
TIMER_PeriodicINT	√	√
TIMER_PWM_Brake	√	√
TIMER_PWM_ChangeDuty	√	√
TIMER_PWM_DeadTime	√	√
TIMER_PWM_OutputWaveform	√	√
TIMER_TimeoutWakeup	√	√
TIMER_ToggleOut	√	√
TRNG_GenRndNum	√	-
UART_AutoBaudRate	√	√
UART_AutoFlow	√	√
UART_IrDA	√	√
UART_LIN	√	√
UART_PDMA	√	√
UART_RS485	√	√
UART_TxRxFunction	√	√
UART_Wakeup	√	√

USBD_Audio_Codec	√	√
USBD_Audio_Headset	√	√
USBD_HID_Keyboard	√	√
USBD_HID_Mouse	√	√
USBD_HID_MouseKeyboard	√	√
USBD_HID_RemoteWakeup	√	√
USBD_HID_Touch	√	√
USBD_HID_Transfer	√	√
USBD_HID_Transfer_And_Keyboard	√	√
USBD_HID_Transfer_And_MSC	√	√
USBD_HID_Transfer_CTRL	√	√
USBD_Mass_Storage_CDROM	√	√
USBD_Mass_Storage_Flash	√	√
USBD_Mass_Storage_SD	√	√
USBD_Mass_Storage_SRAM	√	√
USBD_Micro_Printer	√	√
USBD_Printer_And_HID_Transfer	√	√
USBD_VCOM_And_HID_Keyboard	√	√
USBD_VCOM_And_HID_Transfer	√	√
USBD_VCOM_And_Mass_Storage	√	√
USBD_VCOM_DualPort	√	√
USBD_VCOM_SerialEmulator	√	√
USBD_VENDOR_LBK	√	√
USCI_I2C_EEPROM	√	-
USCI_I2C_Lookback	√	-
USCI_I2C_Loopback_10bit	√	-
USCI_I2C_Master	√	-
USCI_I2C_Master_10bit	√	-
USCI_I2C_Monitor	√	-
USCI_I2C_MultiBytes_Master	√	-

USCI_I2C_SingleByte_Master	√	-
USCI_I2C_Slave	√	-
USCI_I2C_Slave_10bit	√	-
USCI_I2C_Wakeup_Slave	√	-
USCI_SPI_Loopback	√	-
USCI_SPI_MasterMode	√	-
USCI_SPI_PDMA_LoopTest	√	-
USCI_SPI_SlaveMode	√	-
USCI_UART_AutoBaudRate	√	-
USCI_UART_Autoflow_Master	√	-
USCI_UART_Autoflow_Slave	√	-
USCI_UART_PDMA	√	-
USCI_UART_RS485_Master	√	-
USCI_UART_RS485_Slave	√	-
USCI_UART_TxRxFunction	√	-
USCI_UART_Wakeup	√	-
WDT_TimeoutWakeupAndReset	√	√
WWDT_CompareINT	√	√

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