

**ARM[®] Cortex[®]-M0
32-bit Microcontroller****NuMicro[®] Family
M0564 Series BSP
Revision History**

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Revision 3.00.004 (Released 2021-01-17)

1. Fixed USCI_SPI driver issue in Library/StdDriver/src/usci_spi.c
2. Fixed warnings of adc and pwm driver in Library/StdDriver/inc/adc.h, /src/adc.c and /src/pwm.c
3. Fixed USPI_SET_SS_HIGH macro in Library/StdDriver/inc/usci_spi.h
4. Corrected adc definitions for conversion sampling time selection in Library/StdDriver/inc/adc.h
5. Added Apache-2.0 license declaration in driver source.
6. Added README.md file.
7. Added SPI_TRIGGER_TX_RX_PDMA and SPI_DISABLE_TX_RX_PDMA API.

Revision 3.00.003 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Added FMCIDLE_MODULE definition in CLK driver.
4. Added SYS_VoltageDetector sample code.
5. Fixed UART_Wakeup sample code comment typo.

Revision 3.00.002 (Released 2017-05-05)

1. Update multi-function pin name definitions.
2. Add single byte and multi byte write/read APIs for I2C.

Revision 3.00.001 (Released 2016-10-17)

1. Initial Release

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