

User's Manual for KE-KA44169A Evaluation Board

This **KE-KA44169A** evaluation board provides to verify the function of our original Auto Phase Control (APC) technology installed in KA44169A, which is the single phase motor driver for Fan and Pump.
 This EVB helps to accelerate products design-in to market-in.



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Disclaimer

Regarding the specifications of this product, it is considered that you have agreed to the disclaimer described below.

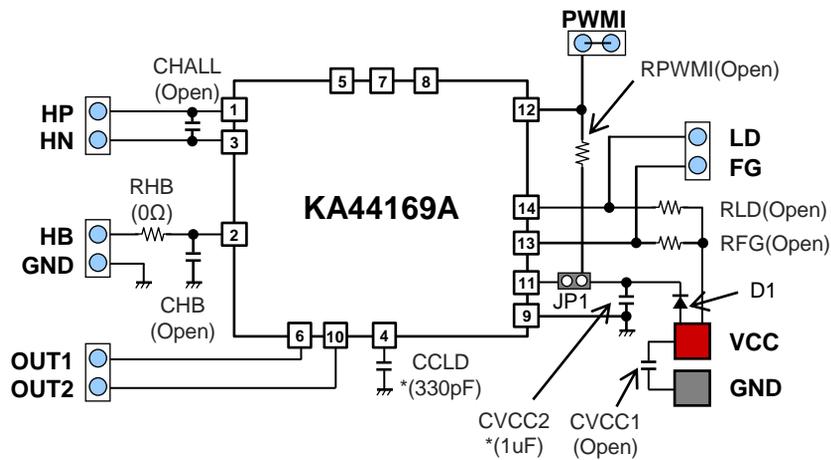
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Recommended Operating Conditions

Parameter	Pin Name	Min.	Typ.	Max.	Unit	Notes
Supply voltage range	VCC	5.0	—	28	V	*1
Input voltage range	HP	0	—	1.5	V	*2
	HN	0	—	1.5	V	*2
	PWMI	0	—	28	V	*2

Notes *1: It is a value under the conditions which do not exceed the absolute maximum rating and the power dissipation.
 *2: For setting range of input control voltage, refer to the IC's Datasheet.

Circuit of Evaluation Board



() are default values of the mounted parts.

* () : Operation of mass production set is not guaranteed. Perform enough evaluation and verification on the design of mass production set. If the VCC Pin voltage is raised by the regenerative current, at the time of start-up or stop operating please connect a zener diode between VCC – GND Pin.

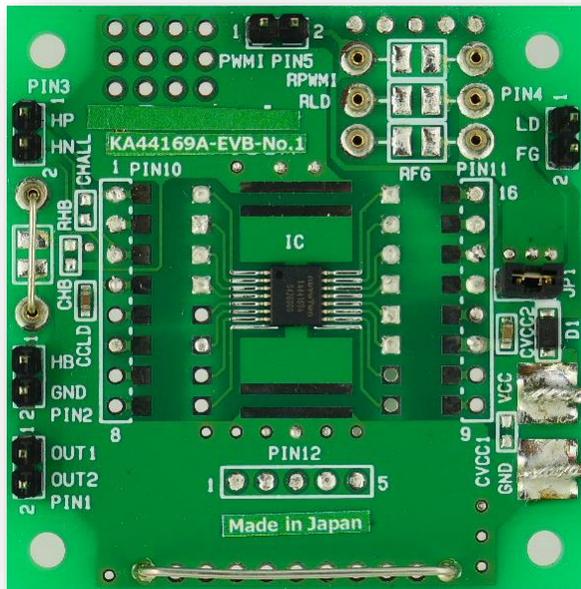
Description for Evaluation Board

Inputs & Outputs & Jumper setting

PWMI :

External signal I/F
 Input pin for torque direction. (Direct PWM system)
 The range of input signal frequency is 15kHz~50kHz.
 Normally, please input the 3.3V-GND level signal.
 (Please adjust the input voltage level within the rating voltage.)

PWMI



HP, HN :

Input pin for Hall signals
 Connect to Hall effect device.
 Refer to "Voltage polarity" shown below.

**HP
HN**

HB, GND :

Output pin for Hall bias
 Connect to the power-Pin of Hall effect device.

**HB
GND**

OUT1, OUT2 :

Output pin for driving a motor
 Connect to a motor's coil..

**OUT1
OUT2**

**LD
FG**

LD, FG :

External signal I/F
 Output pin for FG and LD(Open).

JP1 :

Jumper for VCC short.
 Normally, please be used in short JP1.

JP1

VCC

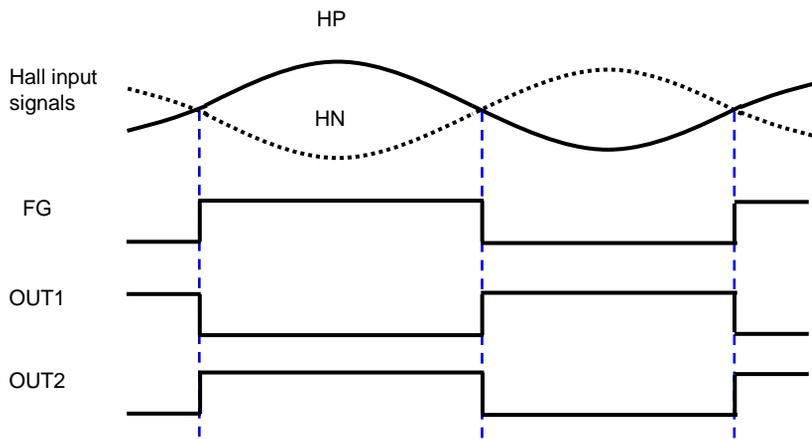
VCC, GND :

External power supply pin
 Supply the recommended operating power voltage(5.0V~28V).

GND

Voltage polarity (exclude delay)

The voltage polarity of FG and OUT1/OUT2 to Hall input signals are as shown below.
 Please note the voltage polarity when connecting to a motor.



Description for Evaluation Board

Resistance & Capacitor settings

RPWMI :
 Pull-up resistor for PWM signal
 The default setting is "open".
 If you want to drive PWMI by open-drain, please set a resistor.

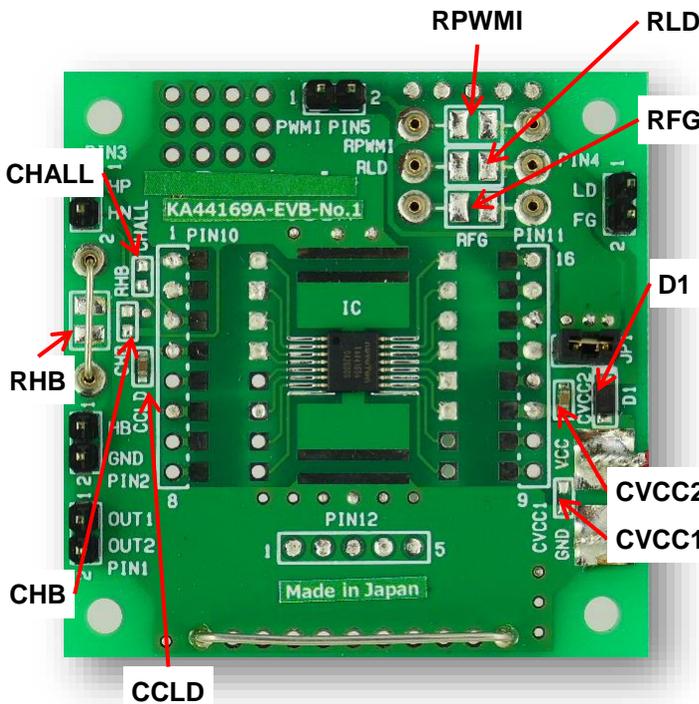
RLD :
 Pull-up resistor for LD
 The default setting is "open".
 If you want to pull LD-pin up to VCC, please set a resistor.

CHALL :
 Bypass capacitor for Hall signals
 If necessary, please mount a capacitor for protection against noise.
 (Open~100pF)

RFG :
 Pull-up resistor for FG
 The default setting is "open".
 If you want to pull FG-pin up to VCC, please set a resistor.

D1 :
 Reverse connection protection diode
 If necessary, please mount the reverse connection protection diode.

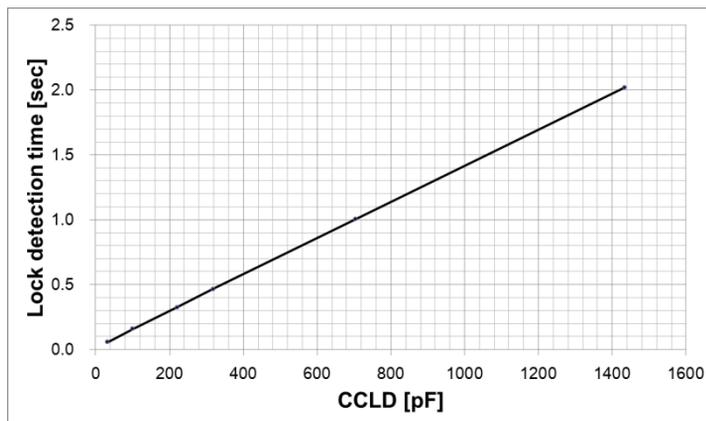
CVCC1,2 :
 Bypass capacitor for power supply
 If necessary, please mount a capacitor for protection against noise.
 (Open~10μF)



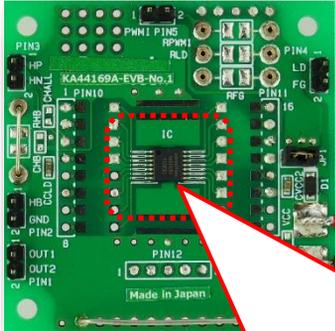
RHB :
 Resistor for Hall bias
 Default = "short".
 If you need "current limit" and "bias-adjustment" of Hall effect device, please set a resistor.

CHB :
 Bypass capacitor for Hall bias
 If necessary, please mount a capacitor for protection against noise.
 (Open~0.1μF)

CCLD :
 Capacitor for setting "Lock detection time"
 The default setting is 330pF. (Lock detection time: 0.48sec(Typ), Lock release time: 0.48sec x 10 = 4.8sec)
 If you want to change settings, please set CCLD by seeing the graph below.

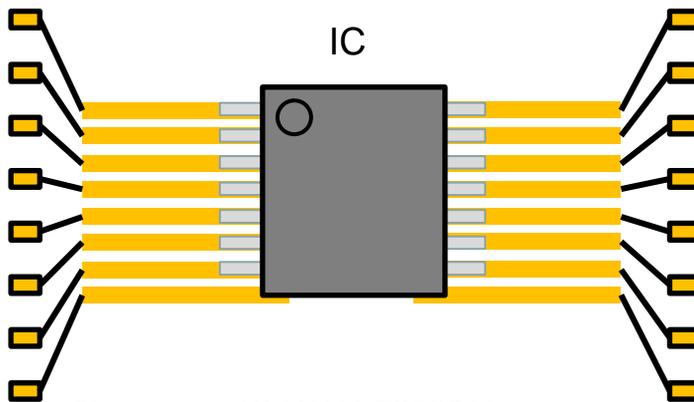


Notes about Mounting KA44169A



Please refer to the following figure for the position to mount IC.

KA44169A (TSSOP14)



Please mount KA44169A(TSSOP14)
at the position shown above.
put pin1 of IC on the first land from the upper left.

Revision History

Date	Revision	Description	Page.
2023.11.1	1.00	1. initially issued.	

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